1. The following features have been modeled
    a. Frequency foldback during soft-start
    b. Current limit, Hiccup
    c. PGOOD functionality
    d. MODE Pin functionality for FPWM
    e. Sync functionality
    f. Different COMP Settings functionality
    g. Min On Time
    h. UVLO functionality
    i. Frequency selection functionality
    j. Current through Pins VIN,SS/TR,SW,PG,GND are modelled and measured.
    k. STEADY_STATE: The parameter STEADY_STATE has been used to reach the steady state faster.
    Keep STEADY_STATE = 0 to observe startup behaviour and STEADY_STATE = 1 for faster Steady state.

2. The following features have not been modeled
    a. The operating quiescent current and thermal shutdown characteristics have not been modeled.
    b. Spread Spectrum clocking functionality is not modeled.
    c. Current through Pins EN,MODE_SYNC,COMP_FSET,FB are not modelled.

3. Ground Pins have been tied to 0V internally and hence model does not support Inverting topologies.

4. This model supports both transient & AC analysis.