1.6MM 10 LAYER		IT180A	
		PCB Stack	Finished(㎛)
Top mask			20
L1	Top		40
	prepreg		94
L2	GROUND PLANE		30.5
	laminate		152.4
L3	INTERNAL SIGNAL 1		30.5
	prepreg		203.2
L4	GROUND PLANE		30.5
	laminate		101.6
L5	POWER PLANE		30.5
	prepreg		152.4
L6	POWER PLANE		30.5
	laminate		101.6
L7	GROUND PLANE		30.5
	prepreg		203.2
L8	SIGNAL LAYER 2		30.5
	laminate		152.4
L9	GROUND PLANE		30.5
	prepreg		94
L10	BOTTOM		40
Bottom mask			20
			1618.8
	Final Total		1618.8

50 ohm

UMT's Suggestion

Impedance layer -> reference layer	Line width (um)From UMT	Impedance (ohm)From UMT
L1->L2	139.7	50
L2->L1/L3		
L3->L2/L4	129.54	50
L4->L3/L5		
L5->L4/L6		
L6->L5/L7		
L7->L6/L8		
L8->L7/L9	129.54	50
L9->L8/L10		
L10->L9	139.7	50

90 ohm

UMT's Suggestion

Impedance layer -> reference layer	Line width (um)From UMT	Impedance (ohm)From UMT
L1->L2	101/114	90
L2->L1/L3		
L3->L2/L4	101/127	90
L4->L3/L5		
L5->L4/L6		
L6->L5/L7		
L7->L6/L8		
L8->L7/L9	101/127	90
L9->L8/L10		
L10->L9	101/114	90

100 ohm

UMT's Suggestion

Impedance layer -> reference layer	Line width (um)From UMT	Impedance (ohm)From UMT
L1->L2	101/177	100
L2->L1/L3		
L3->L2/L4	101/203	100
L4->L3/L5		
L5->L4/L6		
L6->L5/L7		
L7->L6/L8		
L8->L7/L9	101/203	100
L9->L8/L10		
L10->L9	101/177	100

120 ohm

UMT's Suggestion

Impedance layer -> reference layer	Line width (um)From UMT	Impedance (ohm)From UMT
L1->L4	101/142	120
L2->L1/L3		
L3->L2/L4		
L4->L3/L5		
L5->L4/L6		
L6->L5/L7		
L7->L6/L8		
L8->L7/L9		
L9->L8/L10		
L10->L9	NA	