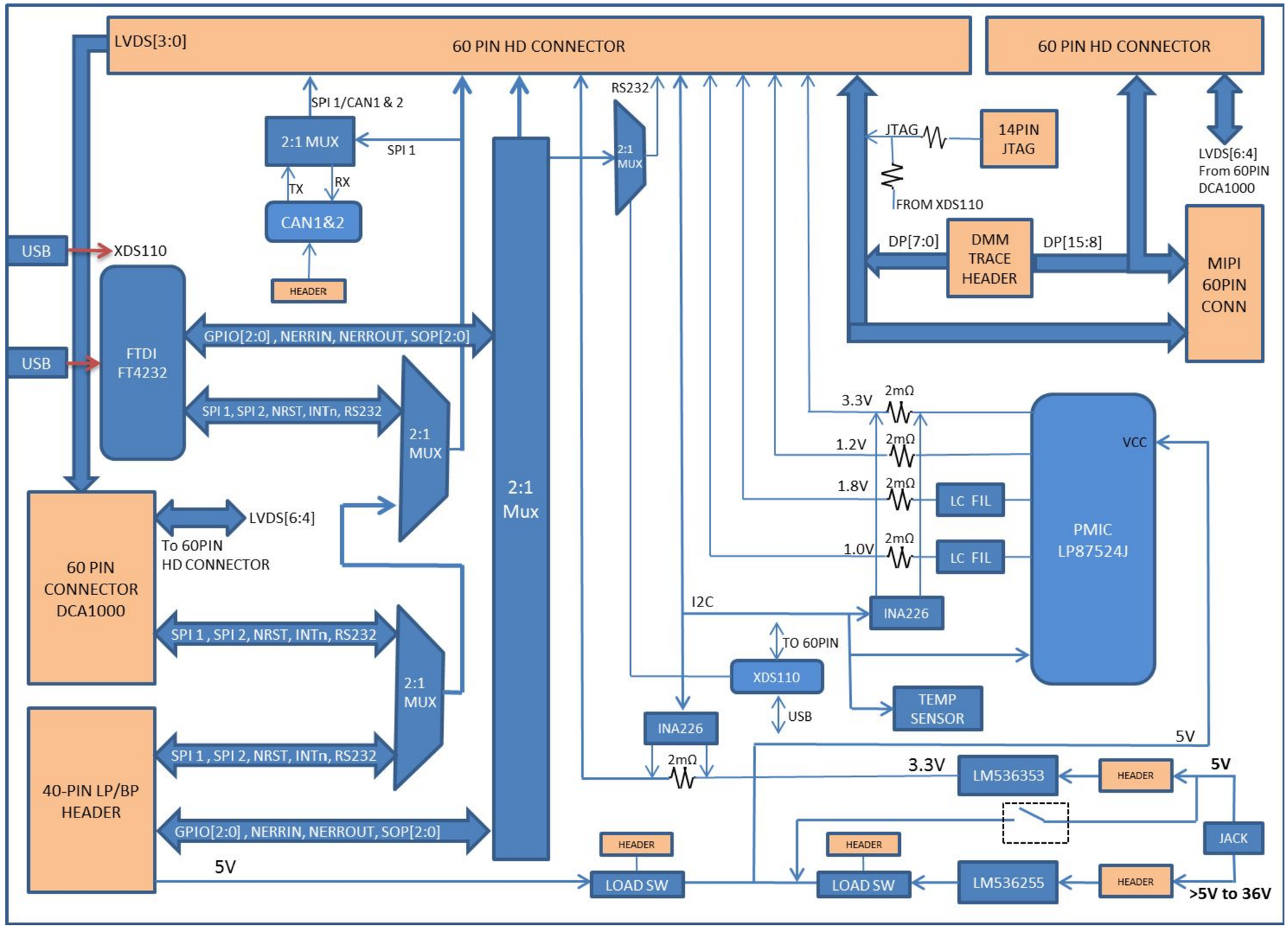
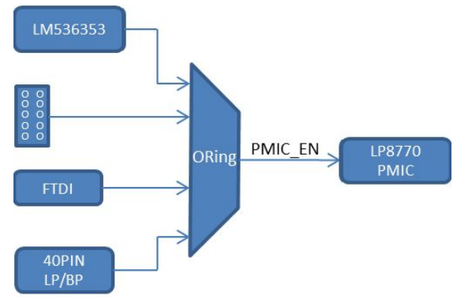
Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A



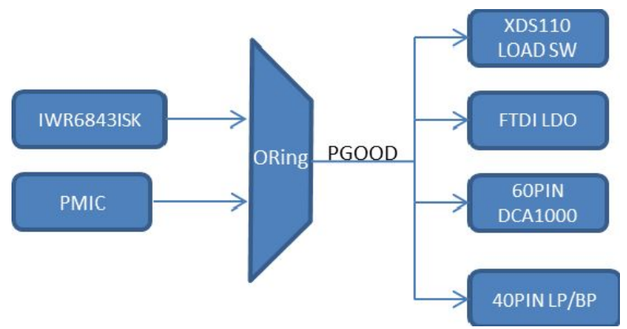
I2C DEVICES

S.No	DESCRIPTION	I2C ADDRESS
1	PMIC	110 0000
2	CURRENT SENSOR 3.3V	100 0010
3	CURRENT SENSOR 3.3V (PMIC)	100 0011
4	CURRENT SENSOR 1.8V	100 0110
5	CURRENT SENSOR 1.2V	100 0111
6	CURRENT SENSOR 1.0V	100 1100
7	TEMPERATURE SENSOR1	100 1001
8	TEMPERATURE SENSOR2	100 1000

PMIC EN ARCHITECTURE



PGOOD ARCHITECTURE



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Drawn By:		File: PROC074A_Block_diagram.SchDoc	Size: B
Engineer: Chethan Kumar Y.B		Contact: http://www.ti.com/support	

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MUX SETTINGS

Switch Refdes	Default position	Position for STAND ALONE mode*	Position for DCA1000 mode	Position for 40Pin LP/BP
S1	OFF (SPI-1)	OFF	OFF	OFF
S2	ON (40pin)	Default position	Default position	ON
S3	ON (FTDI)	ON	Default position	OFF
S4	ON (FTDI/40pin/J16)	ON	OFF	ON
S5[1]	ON (XDS110)	Default position	Default position	Default position
S6	ON (FTDI)	ON	OFF	OFF
S7	ON (FTDI)	ON	OFF	OFF
S8	OFF (60pin)	Default position	OFF	ON
S9	OFF (60pin)	Default position	OFF	ON
S10	ON (FTDI)	ON	Default position	OFF
S11	ON (FTDI)	ON	Default position	OFF
S12	ON (XDS110)	Default position	Default position	Default position

Table 1 : Switch settings for different sources

*Standalone mode means the combination of Starter kit and MMWAVEICBOOST

(1)S5 has RS232 connections from 40pin/FTDI/60pin/XDS110. Default position will be XDS110.

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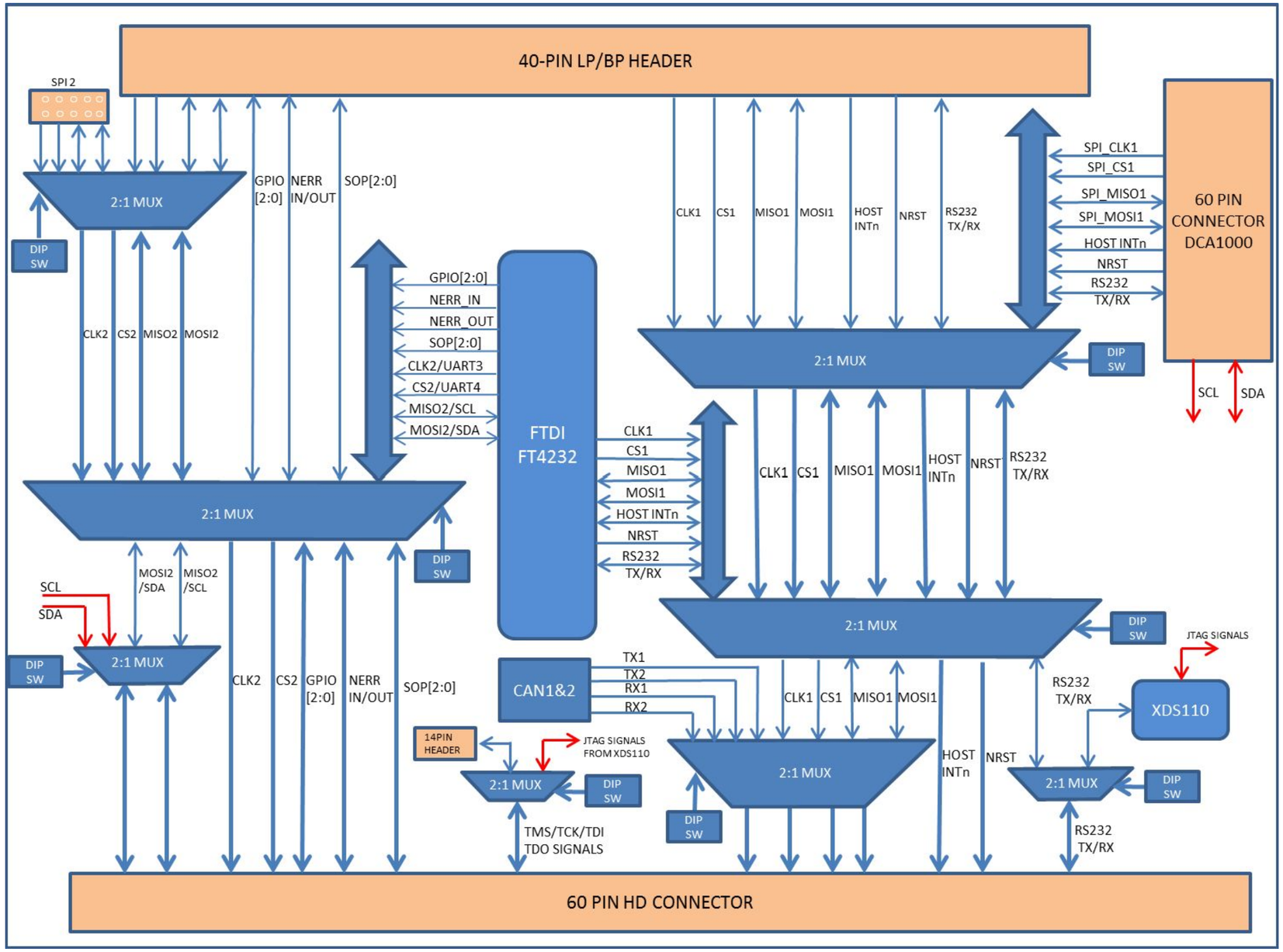
MUXING BLOCK DIAGRAM



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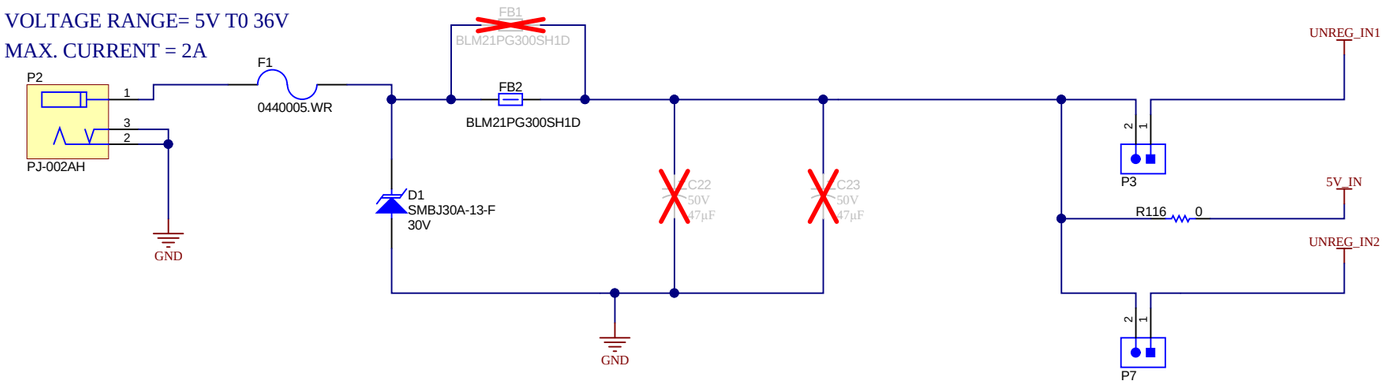
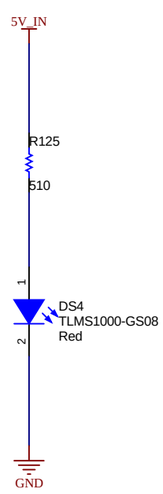
POWER SUPPLY CONNECTOR

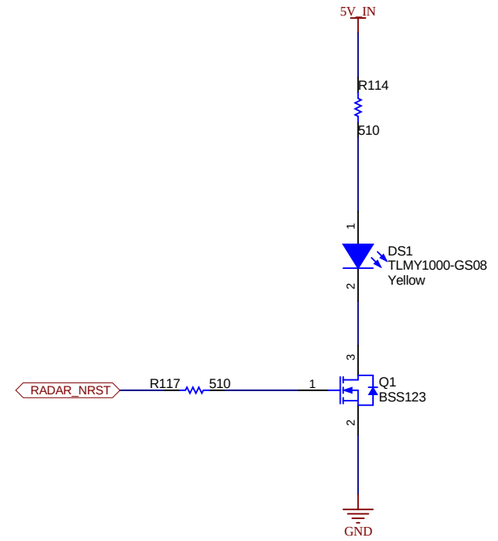
VOLTAGE RANGE= 5V T0 36V
MAX. CURRENT = 2A



Note : Replace the capacitor(PN:597D476X9050Z2T)in C22 and C23 if the input voltage exceeds 5V

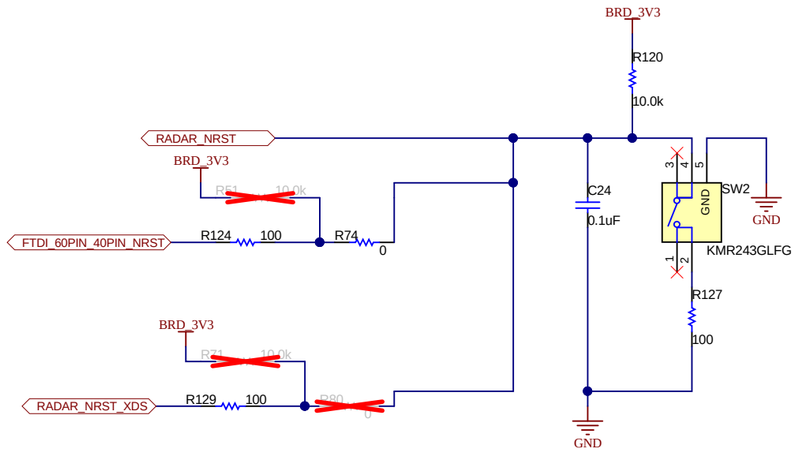
VIN= 5V : Short P3, Mount R116 and do not short P7
VIN= >5V : Short P3,P7 and Remove R116

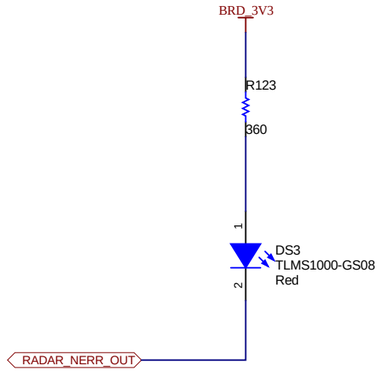


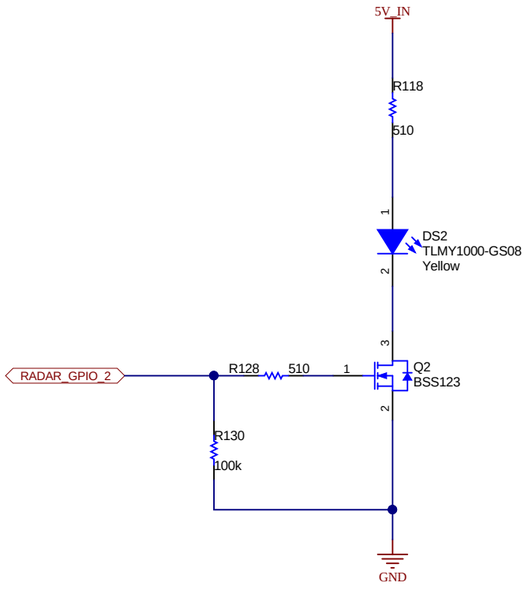


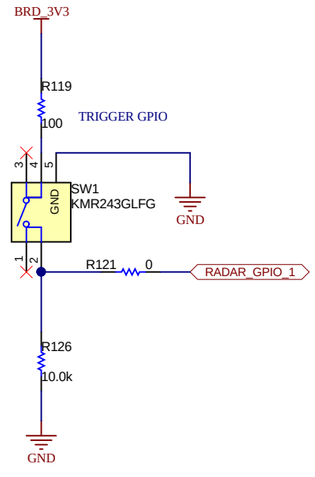
RESET AND LEDS

INDICATION LEDS









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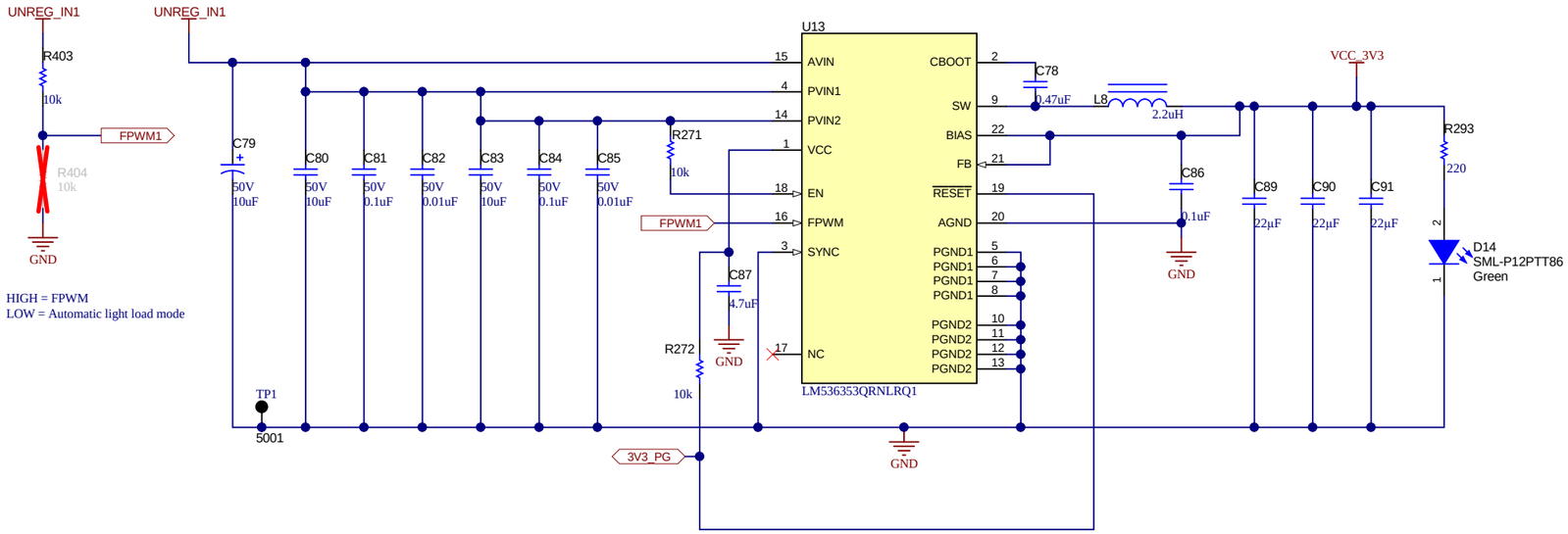
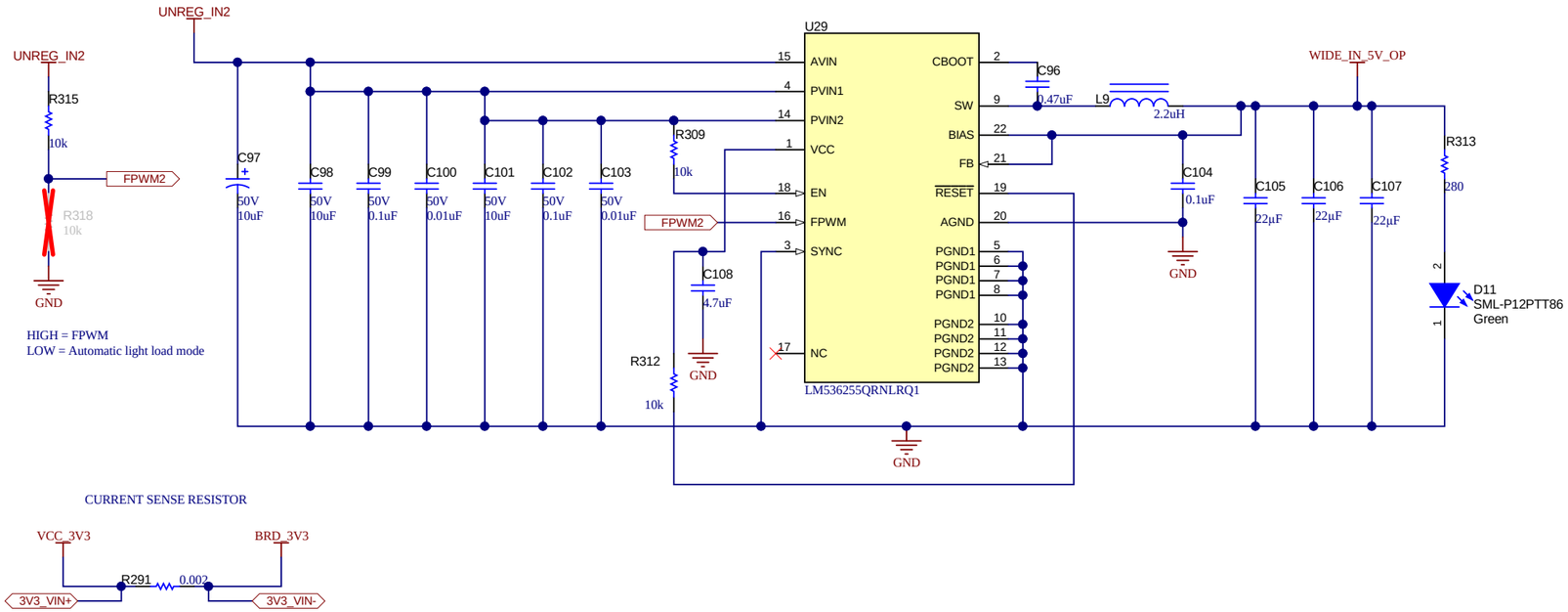
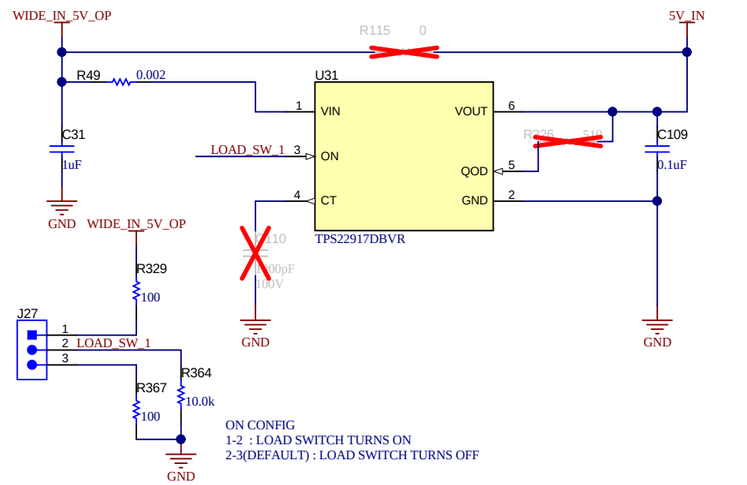
POWER SUPPLY INPUT 5V TO 3.3V OUTPUT



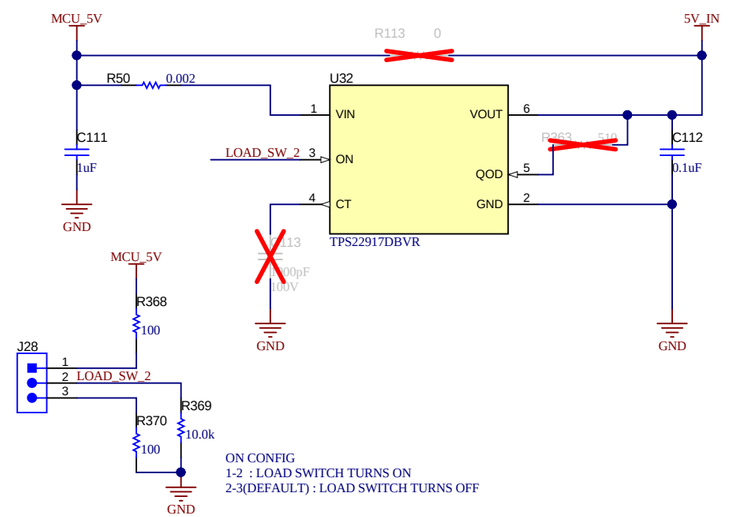
POWER SUPPLY INPUT(6V-36V) TO 5V OUTPUT



5V SUPPLY FROM JACK



5V SUPPLY FROM THE 40PIN LP/BP CONNECTOR



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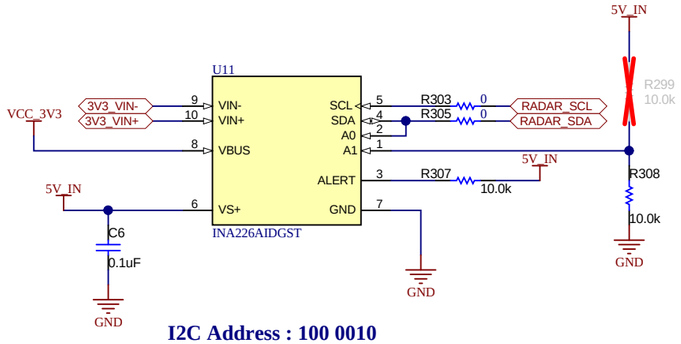
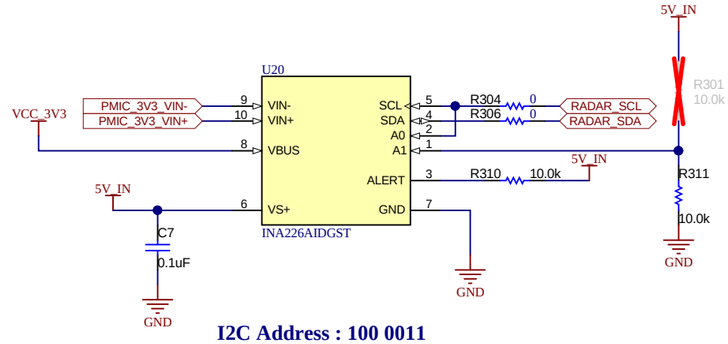
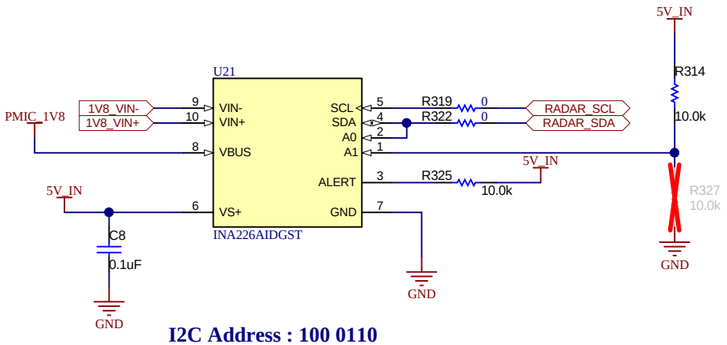
Current Sensors



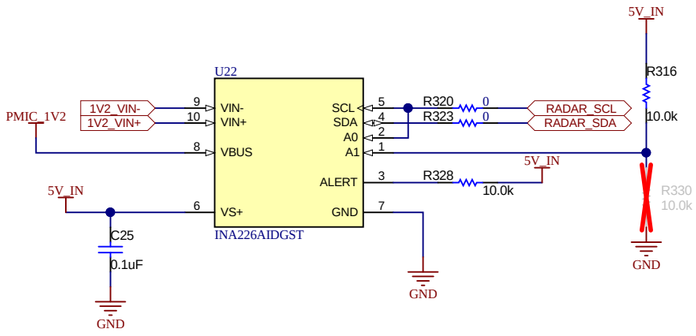
I2C Address : 100 0010



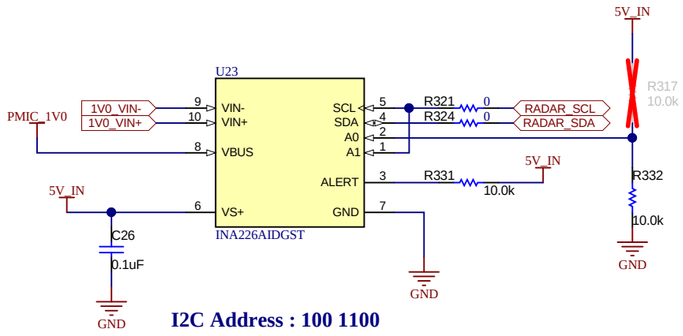
I2C Address : 100 0011



I2C Address : 100 0110



I2C Address : 100 0111



I2C Address : 100 1100

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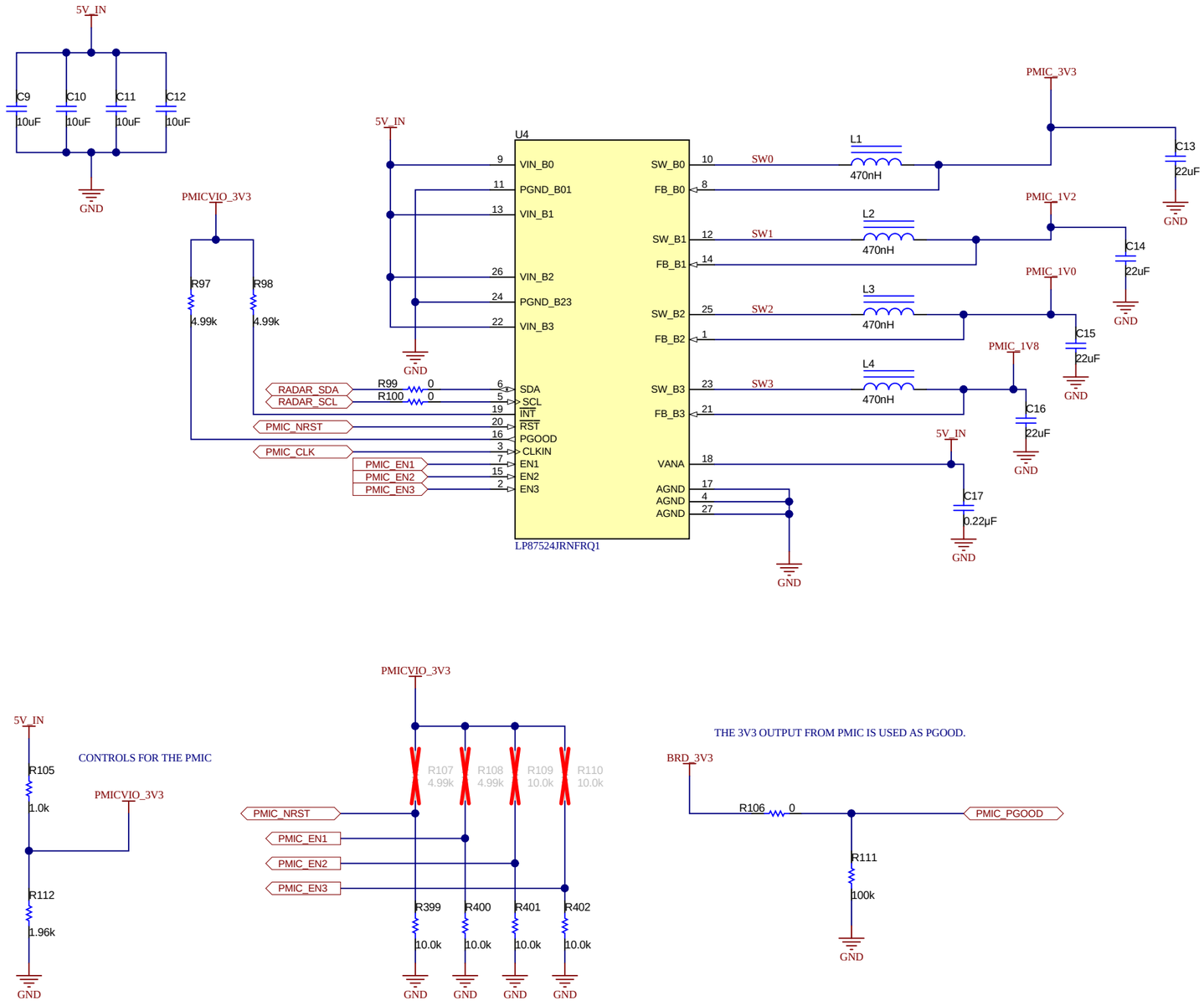
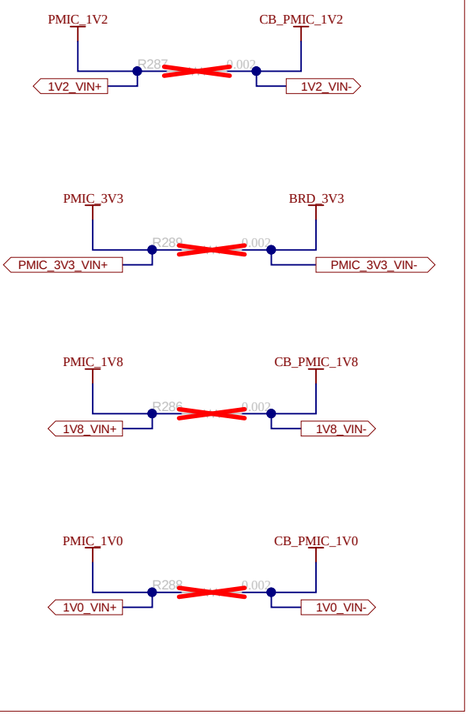
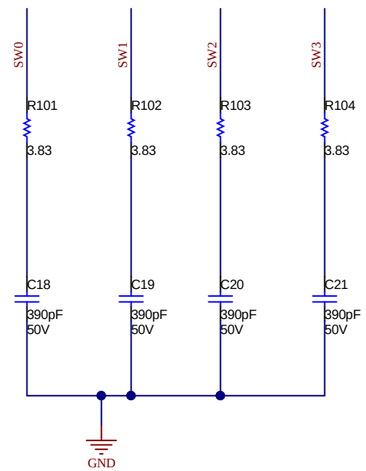
PMIC (3.3V, 1.2V, 1.8V,1.0V OUTPUTS)



CURRENT SENSE RESISTORS



SNUBBER ON SWITCHING NODES



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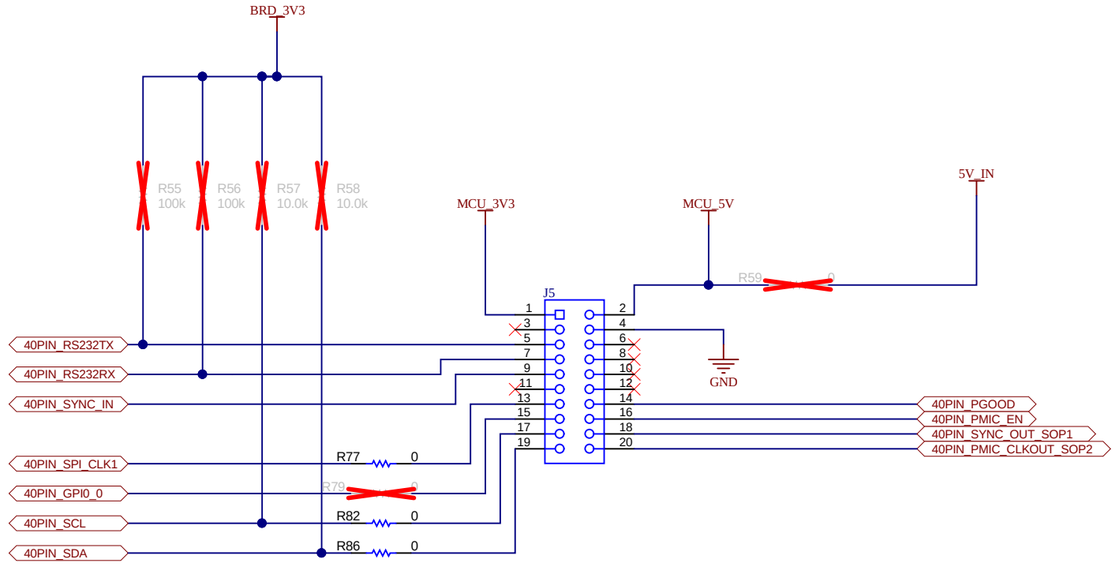
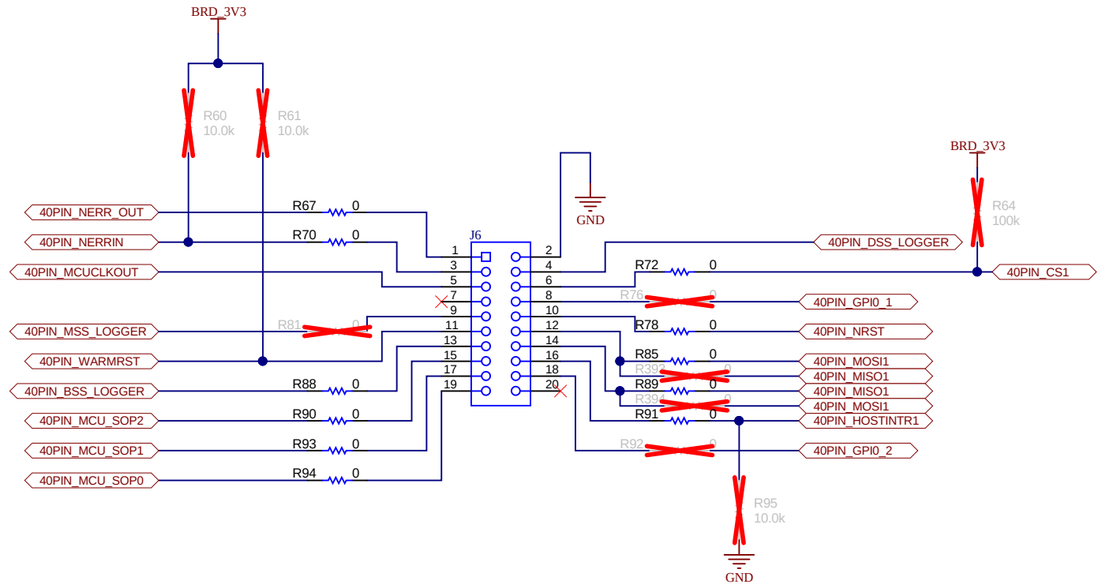
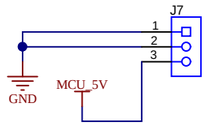
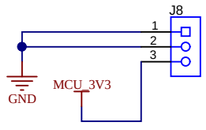
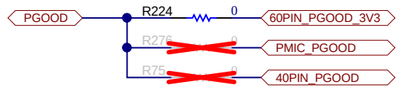
BP/LP CONNECTOR











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Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	

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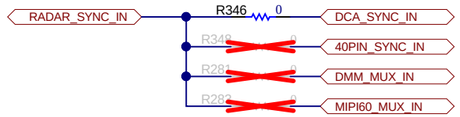
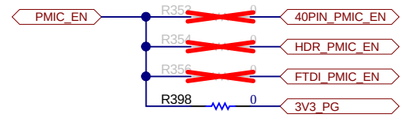
BP/LP RNR OPTIONS

RNR FOR MCUCLKOUT

RNR FOR SYNC IN



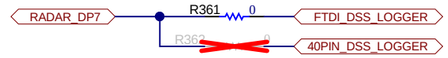
RNR FOR PMIC ENABLE



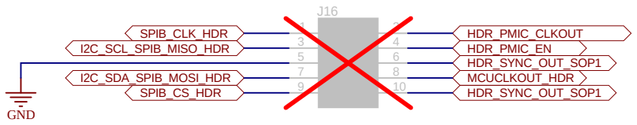
RNR FOR WARMRST

RNR FOR PMIC CLKOUT

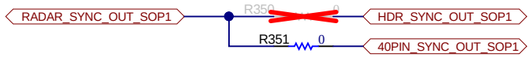
RNR FOR DSS LOGGER



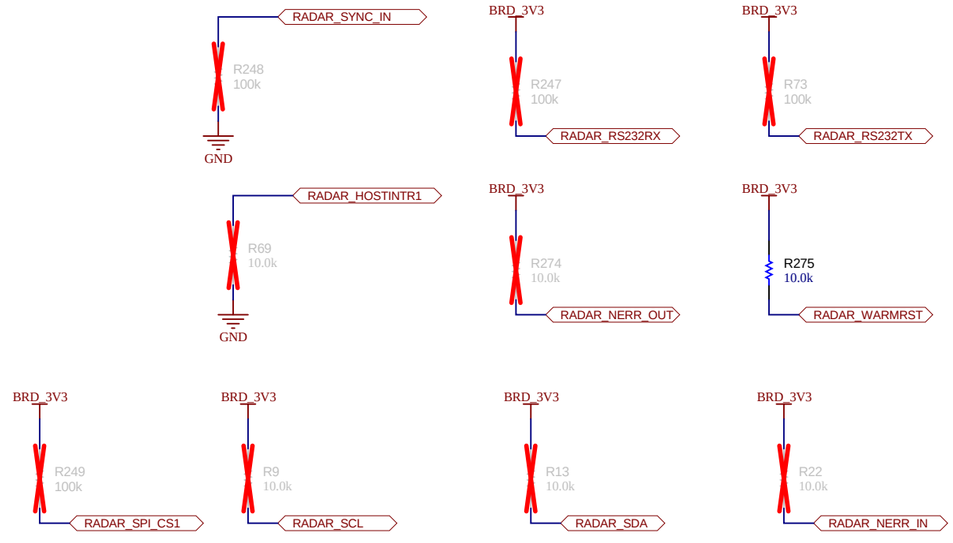
LP/BP SPARE PINS HEADER 1



RNR FOR SYNC OUT



PULL UPs/DOWNs RESISTORS FOR I2C,WARMRST,NERROUT, NERRIN, RS232, SYNC_IN & HOST_INTn



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Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	

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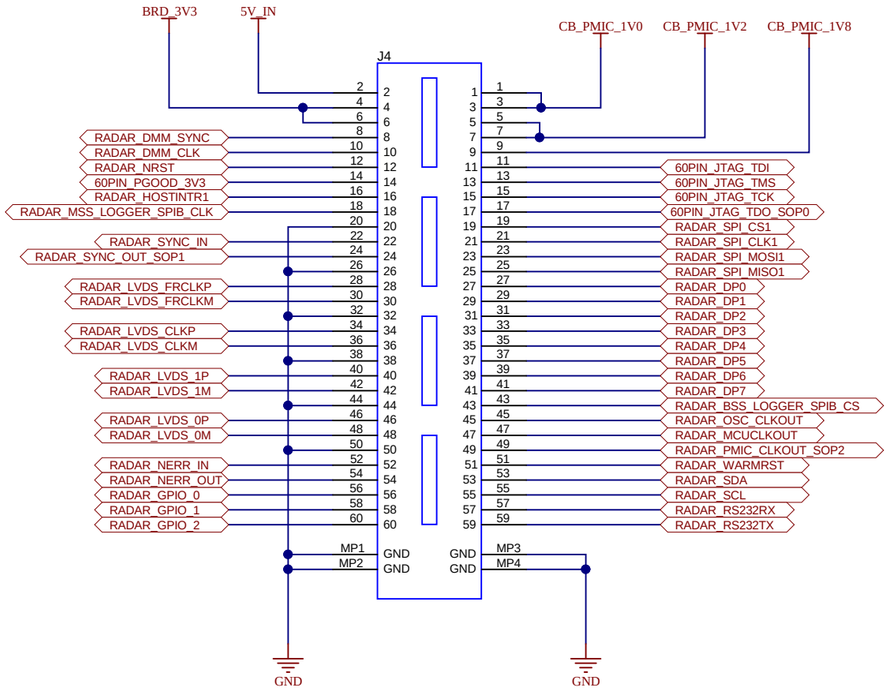
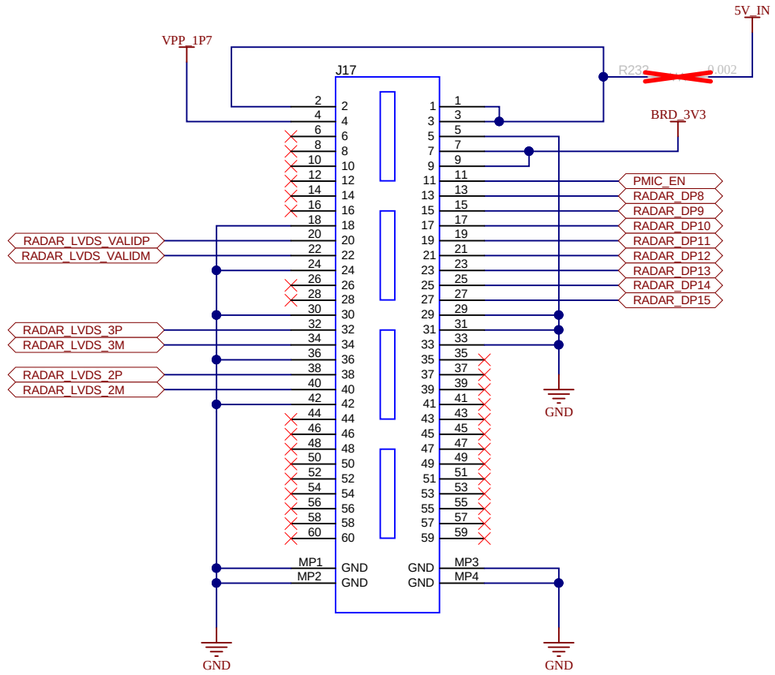
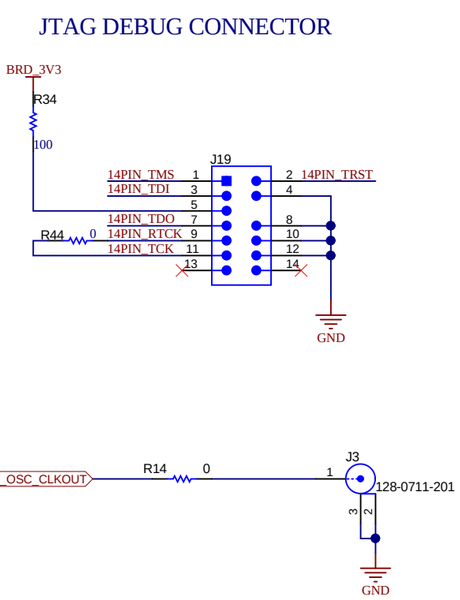
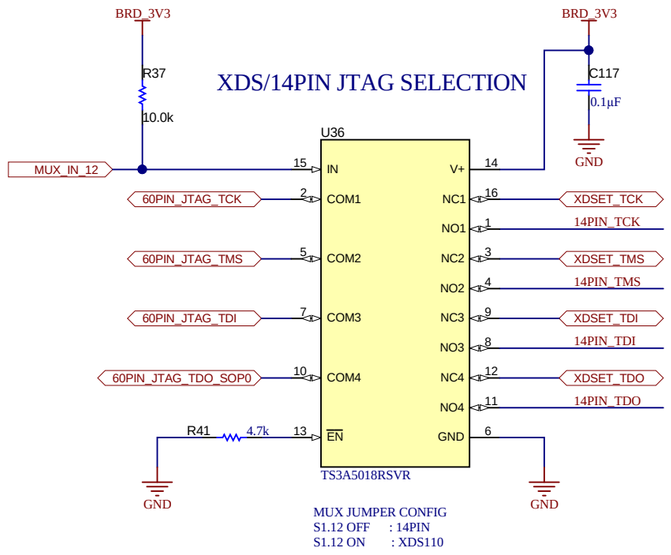
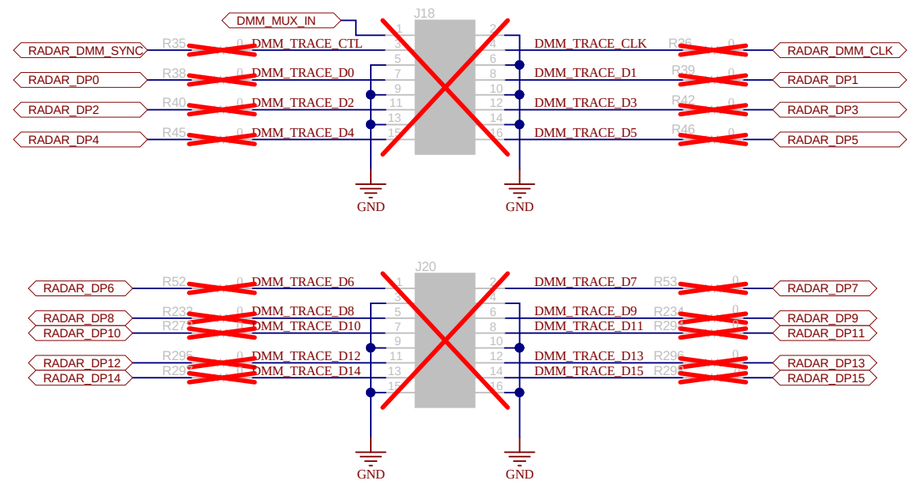
HD CONNECTOR FOR LVDS AND JTAG











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Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	

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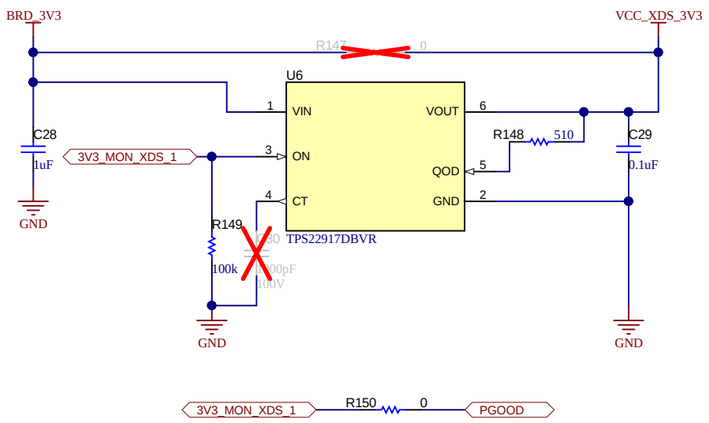
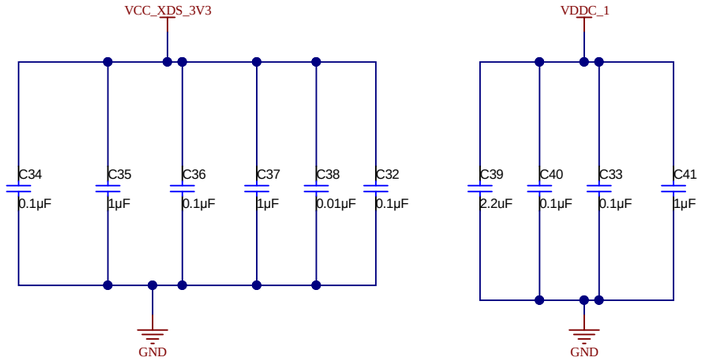
XDS110(1/2)

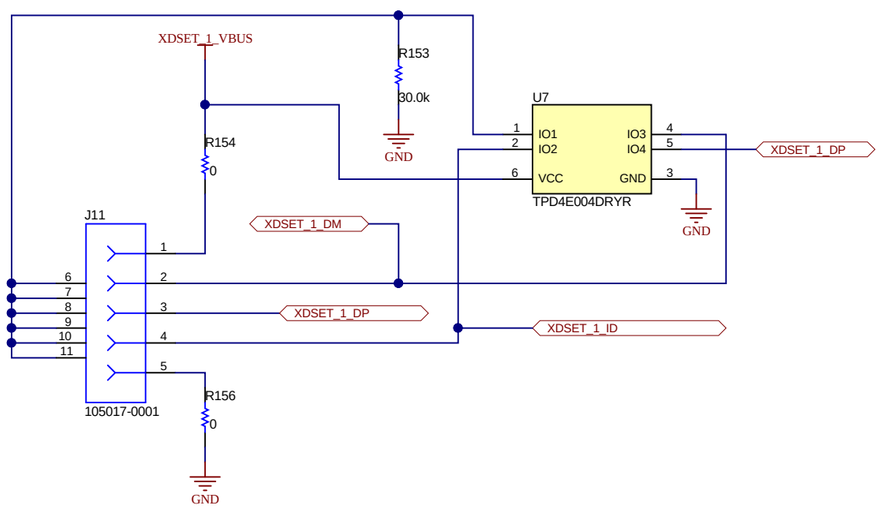
Load switch for turning on 3.3V to XDS110

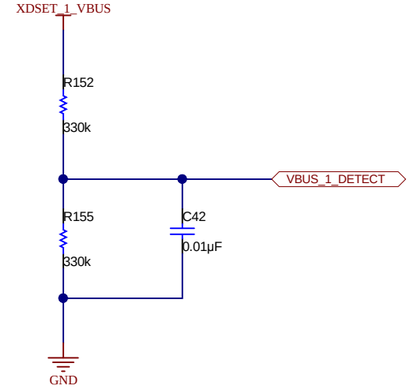


BY DEFAULT THE XDS SUPPLY IS DISABLED..

GETS ENABLED ONLY ONCE THE PMIC IS POWERED UP.







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SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 11 of 22
Drawn By:	File: PROC074A_XDS110_Interface_1A.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	

TEXAS INSTRUMENTS
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XDS110(2/2)

U8A

TM4C1294NCPDTT3

PA0/CAN0RX/I2C9SCL/T0CCP0/U0RX
PA1/CAN0TX/I2C9SDA/T0CCP1/U0TX
PA2/I2C8SCL/SSI0CLK/T1CCP0/U4RX
PA3/I2C8SDA/SSI0FSS/T1CCP1/U4TX
PA4/I2C7SCL/SSI0XDAT0/T2CCP0/U3RX
PA5/I2C7SDA/SSI0XDAT1/T2CCP1/U3TX
PA6/EPI0S8/I2C6SCL/SSI0XDAT2/T3CCP0/U2RX/USB0EPEN
PA7/EPI0S9/I2C6SDA/SSI0XDAT3/T3CCP1/U2TX/USB0EPEN/USB0PFLT

PB0/CAN1RX/I2C5SCL/T4CCP0/U1RX/USB0ID
PB1/CAN1TX/I2C5SDA/T4CCP1/U1TX/USB0VBUS
PB2/EPI0S27/I2C0SCL/T5CCP0/USB0STP
PB3/EPI0S28/I2C0SDA/T5CCP1/USB0CLK
PB4/AIN10/I2C5SCL/SSI1FSS/U0CTS
PB5/AIN11/I2C5SDA/SSI1CLK/U0RTS

PC0/SWCLK/TCK
PC1/SWDIO/TMS
PC2/TDI
PC3/SWO/TDO
PC4/C1-/EPI0S7/U7RX
PC5/C1+/EPI0S6/RTCCLK/U7TX
PC6/C0+/EPI0S5/U5RX
PC7/C0-/EPI0S4/U5TX

PD0/AIN15/C0O/I2C7SCL/SSI2XDAT1/T0CCP0
PD1/AIN14/C1O/I2C7SDA/SSI2XDAT0/T0CCP1
PD2/AIN13/C2O/I2C8SCL/SSI2FSS/T1CCP0
PD3/AIN12/I2C8SDA/SSI2CLK/T1CCP1
PD4/AIN7/SSI1XDAT2/T3CCP0/U2RX
PD5/AIN6/SSI1XDAT3/T3CCP1/U2TX
PD6/AIN5/SSI2XDAT3/T4CCP0/U2RTS/USB0EPEN
PD7/AIN4/NMI/SSI2XDAT2/T4CCP1/U2CTS/USB0PFLT

PE0/AIN3/U1RTS
PE1/AIN2/U1DSR
PE2/AIN1/U1DCD
PE3/AIN0/U1DTR
PE4/AIN9/SSI1XDAT0/U1RI
PE5/AIN8/SSI1XDAT1

PF0/EN0LED0/M0PWM0/SSI3XDAT1/TRD2
PF1/EN0LED2/M0PWM1/SSI3XDAT0/TRD1
PF2/M0PWM2/SSI3FSS/TRD0
PF3/M0PWM3/SSI3CLK/TRCLK
PF4/EN0LED1/M0FAULT0/SSI3XDAT2/TRD3

PG0/EN0PPS/EPI0S11/I2C1SCL/M0PWM4
PG1/EPI0S10/I2C1SDA/M0PWM5

PH0/EPI0S0/U0RTS
PH1/EPI0S1/U0CTS
PH2/EPI0S2/U0DCD
PH3/EPI0S3/U0DSR

PJ0/EN0PPS/U3RX
PJ1/U3TX

PK0/AIN16/EPI0S0/U4RX
PK1/AIN17/EPI0S1/U4TX
PK2/AIN18/EPI0S2/U4RTS
PK3/AIN19/EPI0S3/U4CTS
PK4/EN0LED0/EPI0S32/I2C3SCL/M0PWM6
PK5/EN0LED2/EPI0S31/I2C3SDA/M0PWM7
PK6/EN0LED1/EPI0S25/I2C4SCL/M0FAULT1
PK7/EPI0S24/I2C4SDA/M0FAULT2/RTCCLK/U0RI

PL0/EPI0S16/I2C2SDA/M0FAULT3/USB0D0
PL1/EPI0S17/I2C2SCL/PHA0/USB0D1
PL2/C0O/EPI0S18/PHB0/USB0D2
PL3/C1O/EPI0S19/IDX0/USB0D3
PL4/EPI0S26/T0CCP0/USB0D4
PL5/EPI0S33/T0CCP1/USB0D5
PL6/T1CCP0/USB0DP
PL7/T1CCP1/USB0DM

PM0/EPI0S15/T2CCP0
PM1/EPI0S14/T2CCP1
PM2/EPI0S13/T3CCP0
PM3/EPI0S12/T3CCP1
PM4/T4CCP0/TMPR3/U0CTS
PM5/T4CCP1/TMPR2/U0DCD
PM6/T5CCP0/TMPR1/U0DSR
PM7/T5CCP1/TMPR0/U0RI

PN0/U1RTS
PN1/U1CTS
PN2/EPI0S29/U1DCD/U2RTS
PN3/EPI0S30/U1DSR/U2CTS
PN4/EPI0S34/I2C2SDA/U1DTR/U3RTS
PN5/EPI0S35/I2C2SCL/U1RI/U3CTS

PP0/C2+/SSI3XDAT2/U6RX
PP1/C2-/SSI3XDAT3/U6TX
PP2/EPI0S29/U0DTR/USB0NXT
PP3/EPI0S30/RTCCLK/U0DCD/U1CTS/USB0DIR
PP4/U0DSR/U3RTS/USB0D7
PP5/I2C2SCL/U3CTS/USB0D6

PQ0/EPI0S20/SSI3CLK
PQ1/EPI0S21/SSI3FSS
PQ2/EPI0S22/SSI3XDAT0
PQ3/EPI0S23/SSI3XDAT1
PQ4/DIVSCLK/U1RX

XDS_RS232TX
XDS_RS232RX
XDSET_TCK
XDSET_TMS
XDSET_TDO
XDSET_TDI
RADAR_NRST_XDS
RADAR_MSS_LOGGER_SPIB_CLK
RADAR_BSS_LOGGER_SPIB_CS
XDSET_1_DP
XDSET_1_DM
XDS_I2C_SDA
XDS_I2C_SCL
XDSET_1_ID
XDSET_1_VBUS
VBUS_1_DETECT
VCC_XDS_3V3

R157 0
R160 0
R161 0
R162 0
R47 0
R48 0
R158 0
R159 100
R163 1.00k
R164 1.00k
R165 1.00k
R166 1.00k
R167 49.9
R168 10.0k
R169 1.00k
R170 4.87k

C43 0.1µF
C44 0.1µF
C45 0.1µF
C46 0.01µF
C47 8pF 50V
C48 8pF 50V

U8B
WAKE
RST
HIB
VREFA+
RBIAS
EN0RXIP
EN0RXIN
OSC0
OSC1
XOSC0
XOSC1
EN0TXOP
EN0TXON
TM4C1294NCPDTT3

U8C
VBAT
VDD
VDDA
VDDC
GND
GNDA
TM4C1294NCPDTT3

U9
LM4040C25QDBZR

Y1

VDDC_1

GND

Orderable: MMWAVEICBOOST		Designed for: Public Release	Mod. Date: 10/12/2018
TID #: N/A		Project Title: MMWAVEICBOOST	
Number: PROC074	Rev: A	Sheet Title:	
SVN Rev: Version control disabled		Assembly Variant: 001	Sheet: 12 of 22
Drawn By:		File: PROC074A_XDS110_Interface_1B.SchDoc	Size: B
Engineer: Chethan Kumar Y.B		Contact: http://www.ti.com/support	

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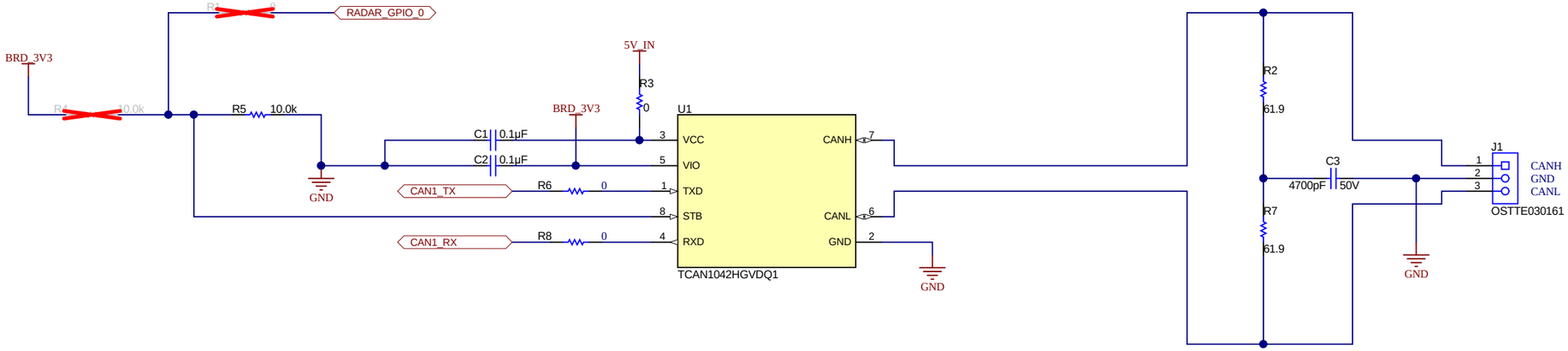
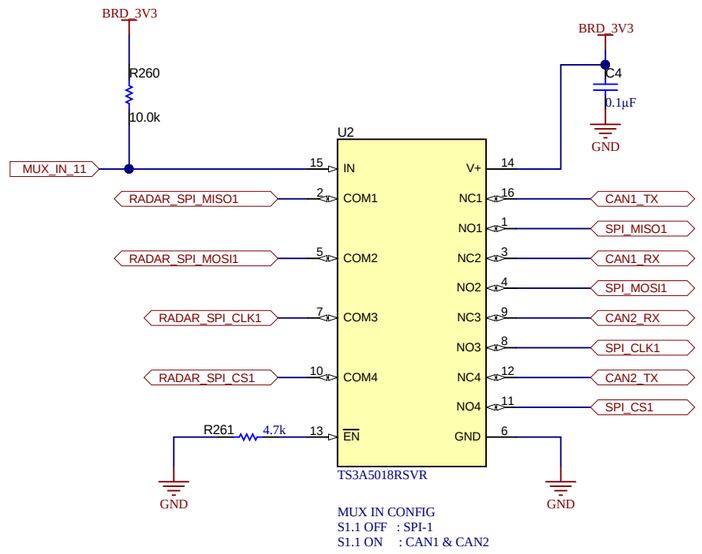
CAN_FD TRANSCEIVER

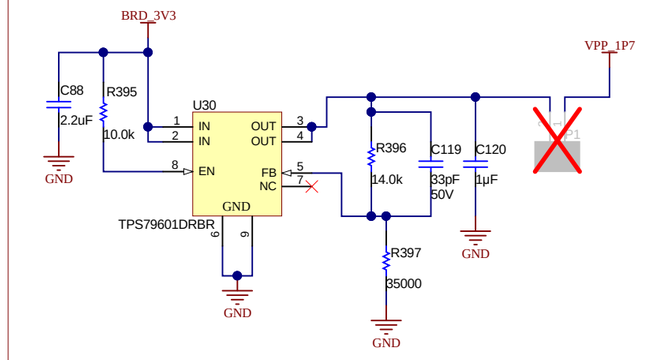


CAN/SPI SELECTION



MUX IN CONFIG
S1.1 OFF : SPI-1
S1.1 ON : CAN1 & CAN2

VPP Supply



Orderable: MMWAVEICBOOST		Designed for: Public Release	Mod. Date: 10/12/2018
TID #: N/A		Project Title: MMWAVEICBOOST	
Number: PROC074	Rev: A	Sheet Title:	
SVN Rev: Version control disabled		Assembly Variant: 001	Sheet: 13 of 22
Drawn By:		File: PROC074A_CAN_Interface.SchDoc	Size: B
Engineer: Chethan Kumar Y.B		Contact: http://www.ti.com/support	

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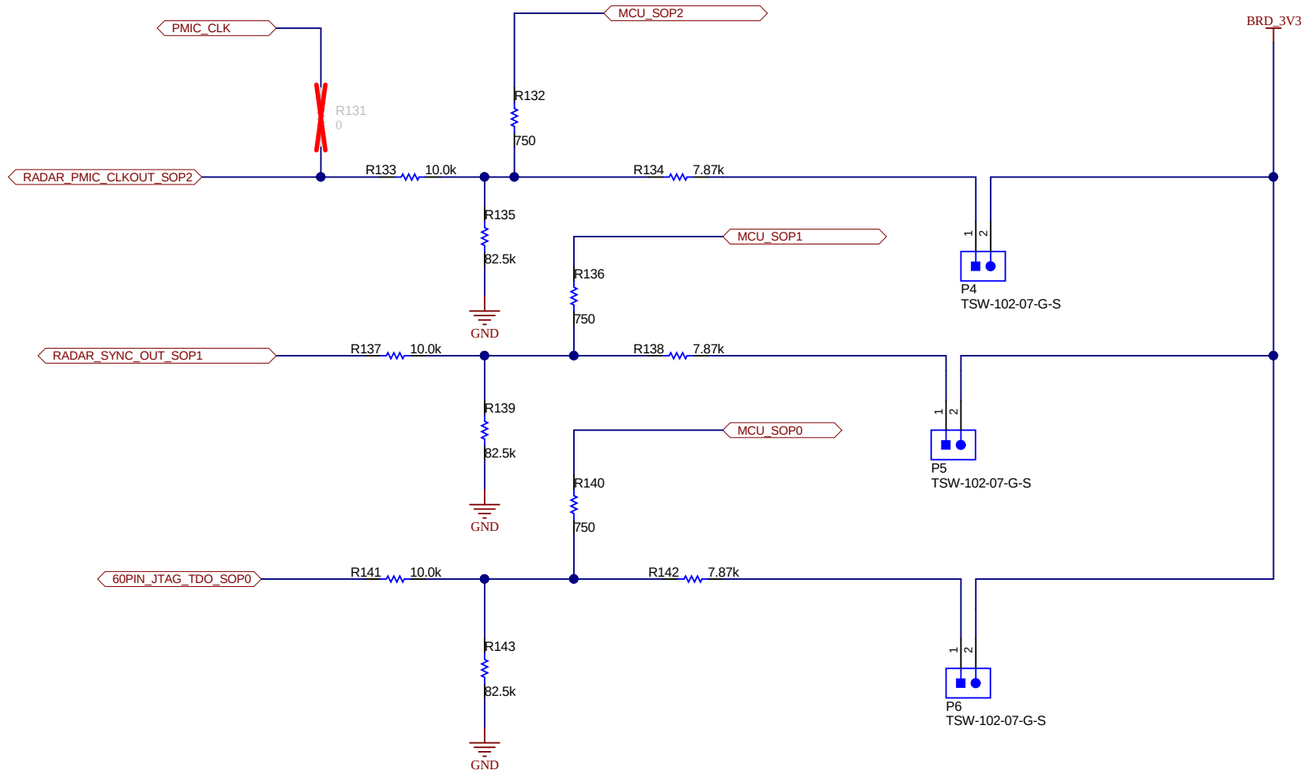
SOP HEADERS

SOP_MODE2	"011"	DEV/FLED
SOP_MODE4	"001"	FUNC - > DEFAULT VALUE FOR OUTPUTS
SOP_MODE5	"101"	DEV MANAGEMENT -> FOR FLASHING



Orderable: MMWAVEICBOOST		Designed for: Public Release	Mod. Date: 10/12/2018
TID #: N/A		Project Title: MMWAVEICBOOST	
Number: PROC074	Rev: A	Sheet Title:	
SVN Rev: Version control disabled		Assembly Variant: 001	Sheet: 14 of 22
Drawn By:		File: PROC074A_SOP_Selection.SchDoc	Size: B
Engineer: Chethan Kumar Y.B		Contact: http://www.ti.com/support	

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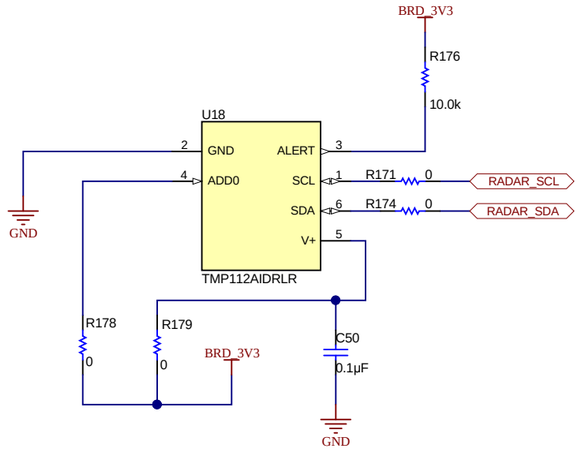
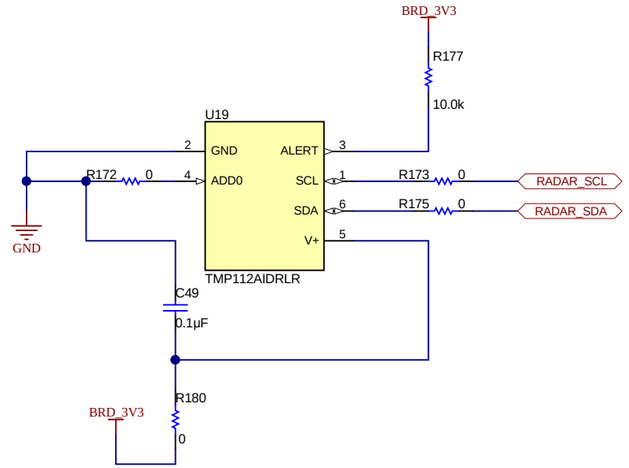
ONBOARD TEMP SENSORS



I2C ADDRESS: 100 1001

TEMP SENSOR AWAY FROM PMIC



I2C Address : 100 1000

TEMP SENSOR CLOSE TO PMIC

Orderable: MMWAVEICBOOST	Designed for: Public Release	Mod. Date: 10/12/2018
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Number: PROC074 Rev: A	Sheet Title:	
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 15 of 22
Drawn By:	File: PROC074A_Temp_Sensor.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	

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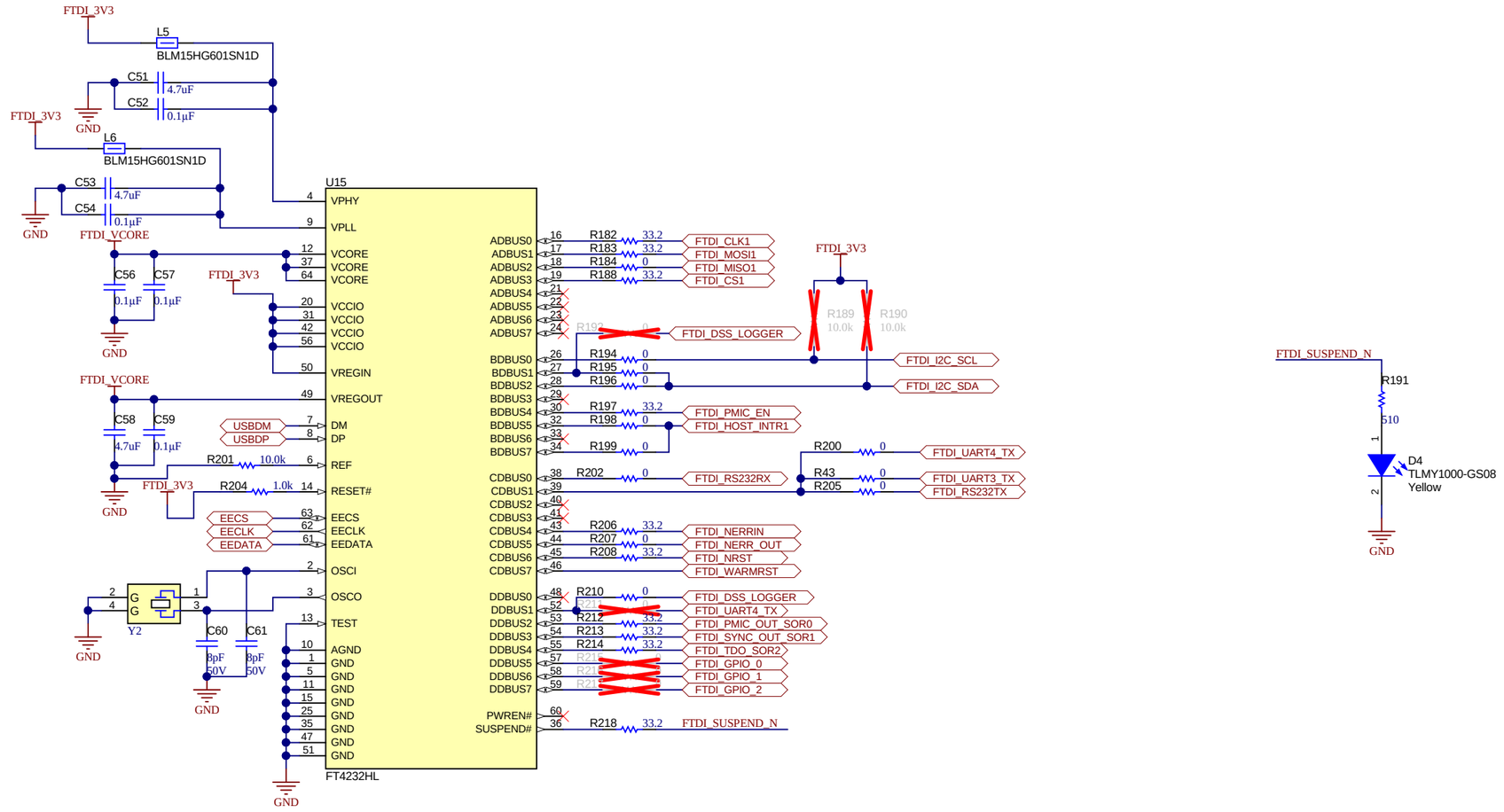
FTDI INTERFACE



Orderable: MMWAVEICBOOST	Designed for: Public Release	Mod. Date: 10/12/2018
TID #: N/A	Project Title: MMWAVEICBOOST	
Number: PROC074 Rev: A	Sheet Title:	
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Drawn By:	File: PROC074A_FTDI_Interface.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	

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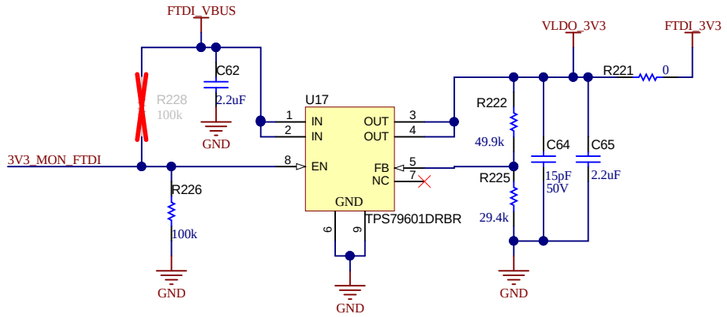
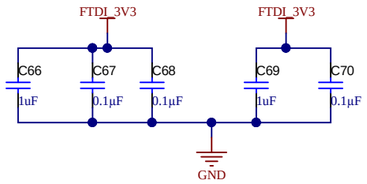
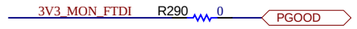
FTDI POWER SECTION

3.3V LDO FOR FTDI

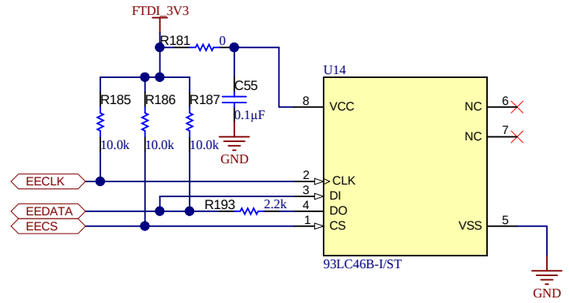


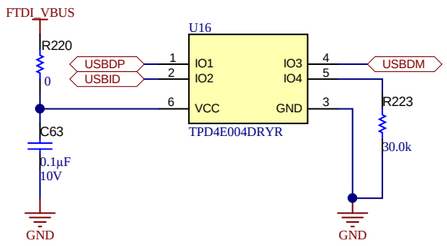
THIS 3.3V LDO WILL TURN ON
EITHER FOR IWR6XXX MODULE BOARD
OR CARRIER BOARD IS POWERED UP

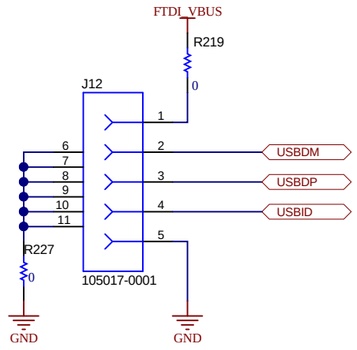




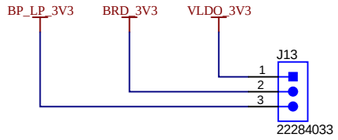
FTDI EEPROM







3.3V SUPPLY SELECTION JUMPER



POS 1-2: FROM FTDI LDO
POS 2-3: FROM 40PIN LP/BP CONNECTOR

Orderable: MMWAVEICBOOST	Designed for: Public Release	Mod. Date: 10/12/2018
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Drawn By:	File: PROC074A_FTDI_Interface_PWR.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	

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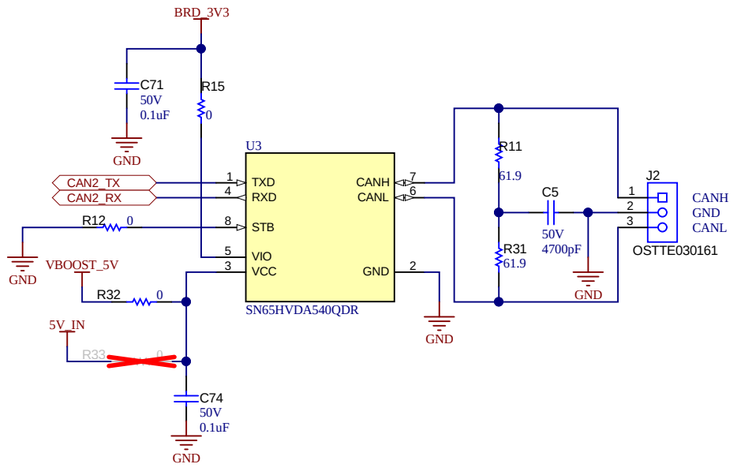
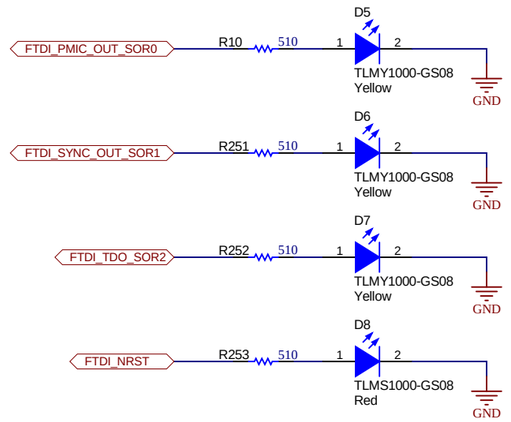
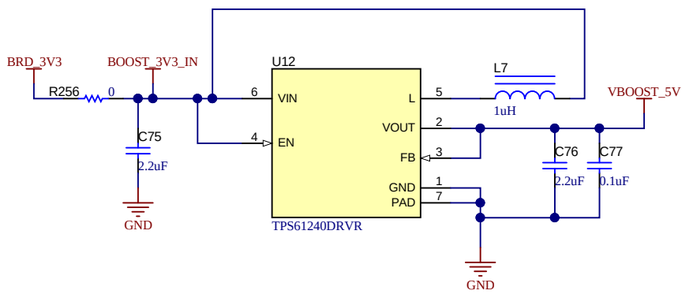
CAN TRANSCEIVER



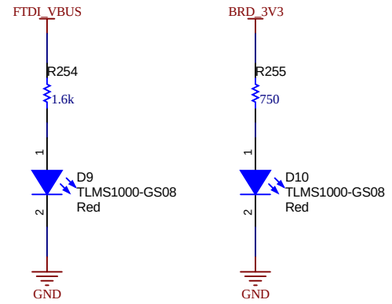
RESET & SOP STATUS INDICATION



3.3V TO 5V BOOST CONVERTER



5V & 3.3V SUPPLY INDICATION



Orderable: MMWAVEICBOOST	Designed for: Public Release	Mod. Date: 10/12/2018
TID #: N/A	Project Title: MMWAVEICBOOST	
Number: PROC074 Rev: A	Sheet Title:	
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 18 of 22
Drawn By:	File: PROC074A_CAN_Interface_LEDs.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	

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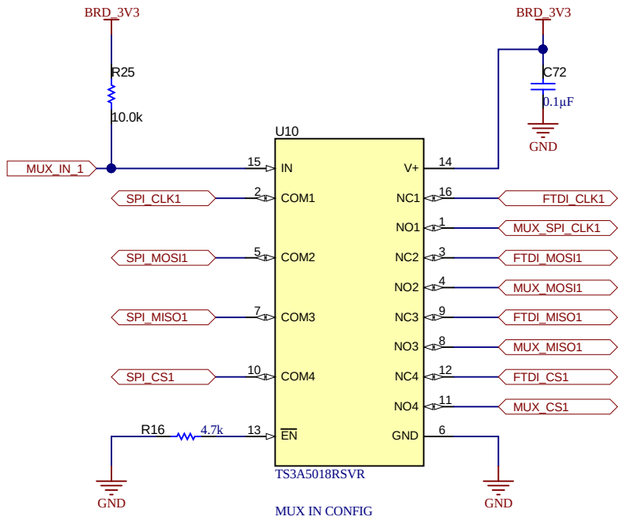
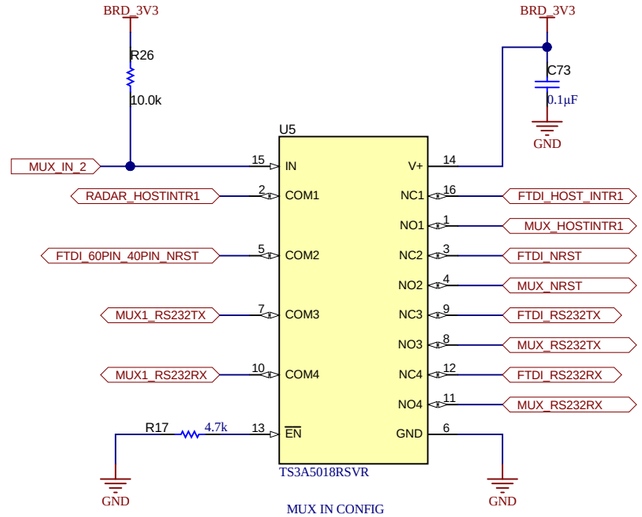
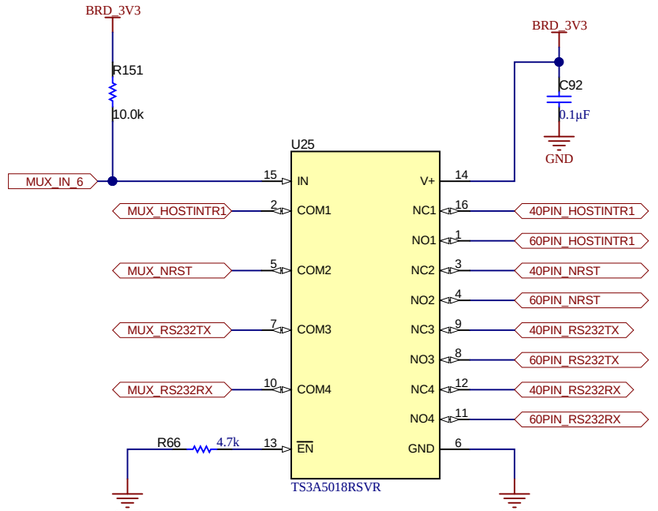
ANALOG MUX BETWEEN FTDI , DCA & 40PIN HDR 1/2



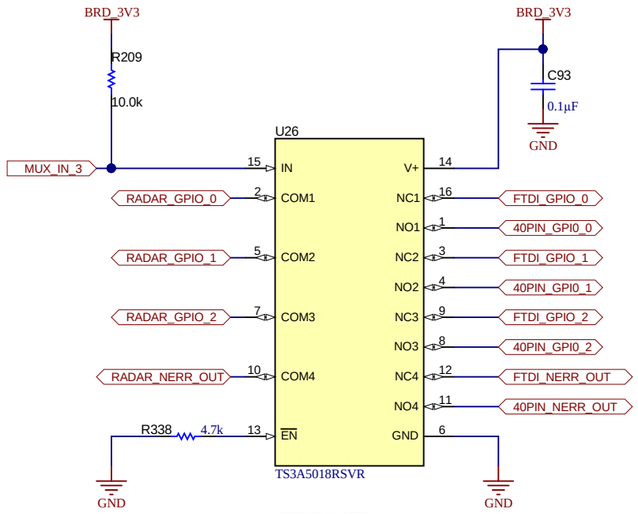
MUX IN CONFIG
S1.6 OFF : 40PIN/60PIN
S1.6 ON : FTDI



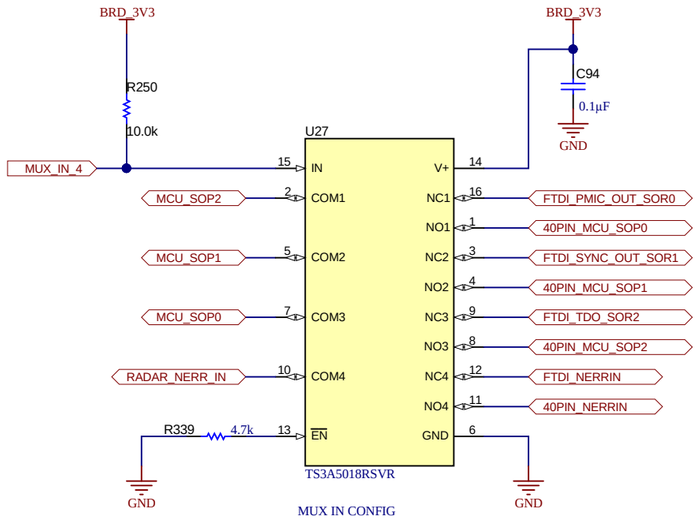
MUX IN CONFIG
S1.7 OFF : 40PIN/60PIN
S1.7 ON : FTDI



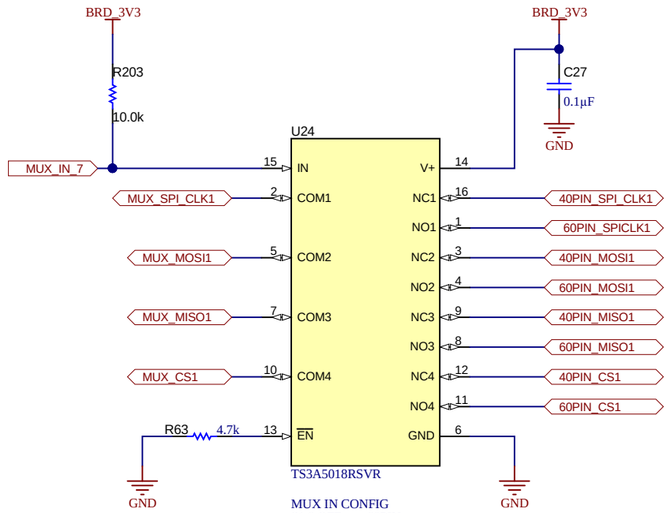
MUX IN CONFIG
S1.8 OFF : 60PIN
S1.8 ON : 40PIN



MUX IN CONFIG
S1.10 OFF : 40PIN
S1.10 ON : FTDI



MUX IN CONFIG
S1.11 OFF : 40PIN
S1.11 ON : FTDI



MUX IN CONFIG
S1.9 OFF : 60PIN
S1.9 ON : 40PIN

EN	IN	NO to COM/ COM to NO	NC to COM/ COM to NC
L	L	OFF	ON
L	H	ON	OFF
H	X	OFF	OFF

Orderable: MMWAVEICBOOST	Designed for: Public Release	Mod. Date: 10/12/2018
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Number: PROC074 Rev: A	Sheet Title:	
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 19 of 22
Drawn By:	File: PROC074A_Analog_Mux_1A.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	

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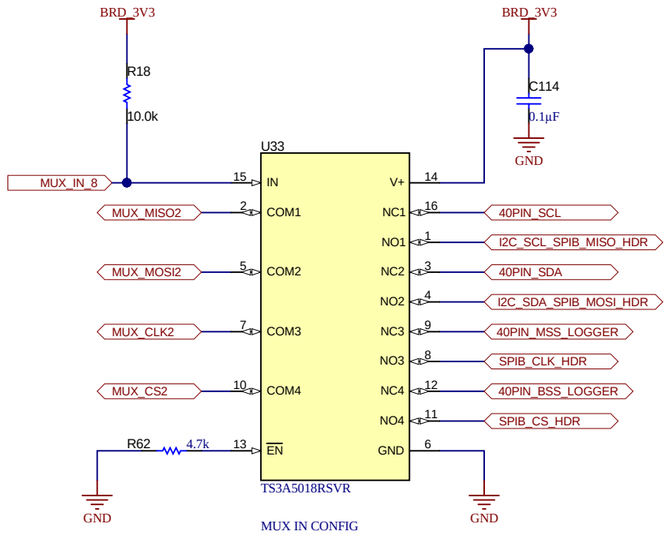
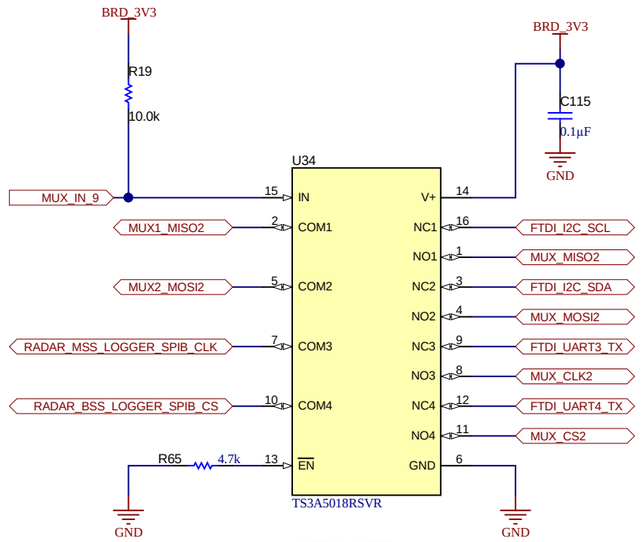
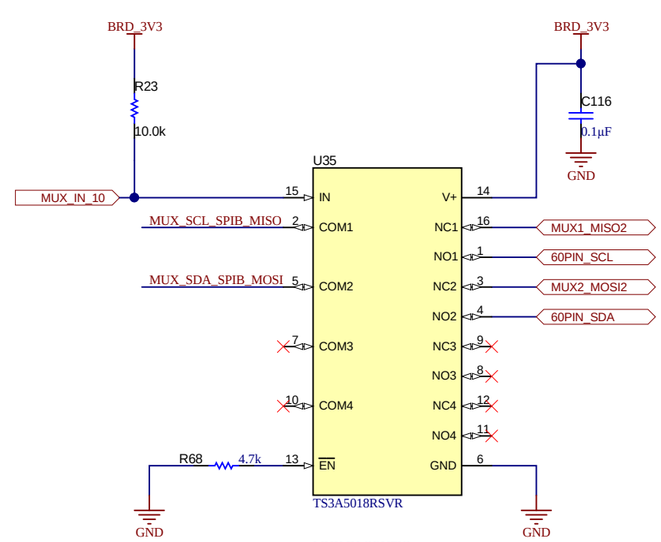
ANALOG MUX BETWEEN FTDI , DCA & 40PIN HDR (2/2)



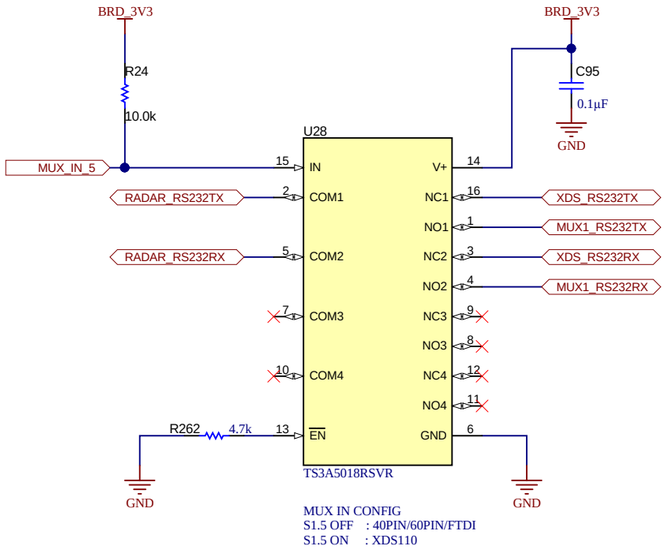
MUX IN CONFIG
S1.2 OFF : J16 HEADER
S1.2 ON : 40PIN



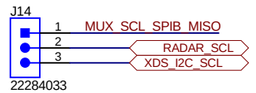
MUX IN CONFIG
S1.3 OFF : 40PIN/J16 HEADER
S1.3 ON : FTDI



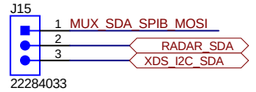
MUX IN CONFIG
S1.4 OFF : 60PIN
S1.4 ON : 40PIN/J16 HEADER/FTDI



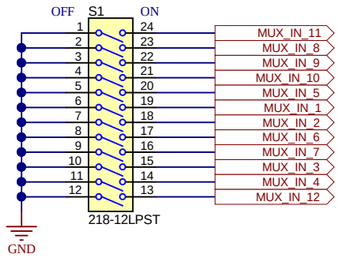
MUX IN CONFIG
S1.5 OFF : 40PIN/60PIN/FTDI
S1.5 ON : XDS110



JUMPER CONFIG
(1-2) CLOSED(DEFAULT) : FTDI/60PIN
(2-3) CLOSED : XDS110



JUMPER CONFIG
(1-2) CLOSED(DEFAULT) : FTDI/60PIN
(2-3) CLOSED : XDS110



Orderable: MMWAVEICBOOST		Designed for: Public Release	Mod. Date: 10/12/2018
TID #: N/A		Project Title: MMWAVEICBOOST	
Number: PROC074	Rev: A	Sheet Title:	
SVN Rev: Version control disabled		Assembly Variant: 001	Sheet: 20 of 22
Drawn By:		File: PROC074A_Analog_Mux_1B.SchDoc	Size: B
Engineer: Chethan Kumar Y.B		Contact: http://www.ti.com/support	

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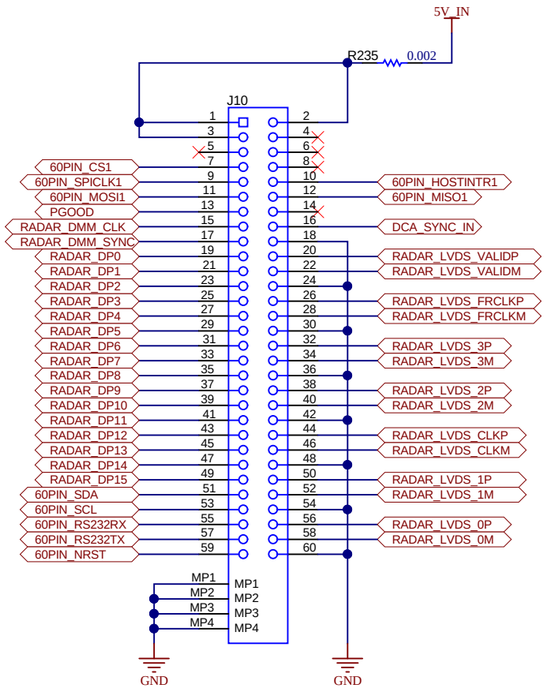
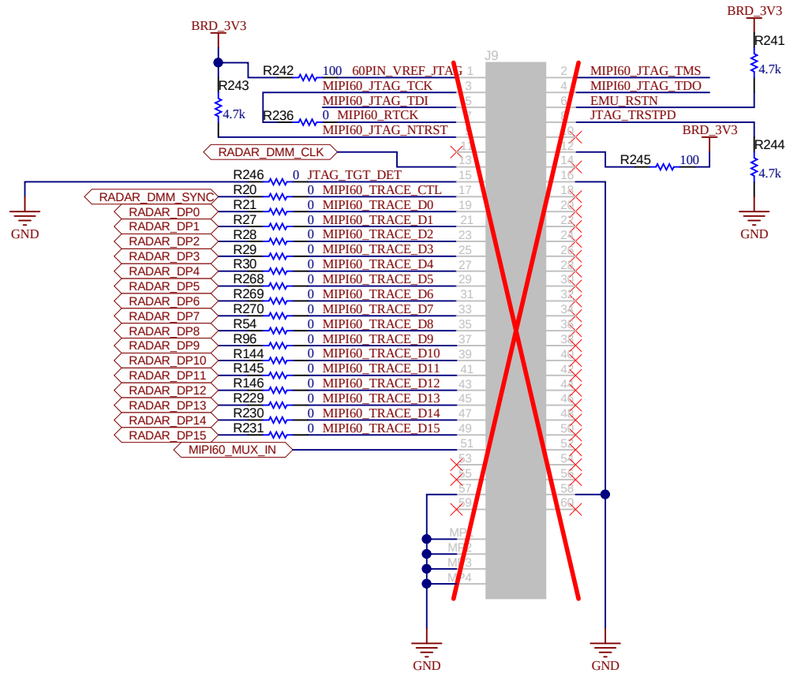
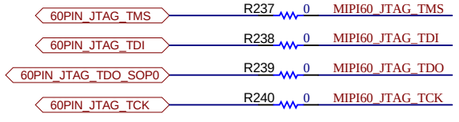
http://www.ti.com

60PIN HD CONNECTOR FOR DCA1000



MIPI-60 DEBUG TRACE CONNECTOR





Orderable: MMWAVEICBOOST		Designed for: Public Release	Mod. Date: 10/12/2018
TID #: N/A		Project Title: MMWAVEICBOOST	
Number: PROC074	Rev: A	Sheet Title:	
SVN Rev: Version control disabled		Assembly Variant: 001	Sheet: 21 of 22
Drawn By:		File: PROC074A_HD_Connector_DCA.SchDoc	Size: B
Engineer: Chethan Kumar Y.B		Contact: http://www.ti.com/support	

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H1 RM3X6MM 2701 H2 RM3X6MM 2701 H3 RM3X6MM 2701 H4 RM3X6MM 2701

H5 24395 H6 24395 H7 24395 H8 24395

H9 MFW030A H10 MFW030A H11 MFW030A H12 MFW030A

FID1 FID2 FID3 FID4 FID5 FID6

Open Top Jumper Sockets

SH-J1 M7582-05 SH-J2 M7582-05 SH-J3 M7582-05 SH-J4 M7582-05 SH-J5 M7582-05 SH-J6 M7582-05 SH-J7 M7582-05

PCB Number: PROC074
PCB Rev: A

PCB LOGO
Texas Instruments

CE Mark

PCB LOGO
FCC disclaimer

PCB LOGO
WEEE logo

CAUTION HOT SURFACE1

CAUTION HOT SURFACE

Variant/Label Table	
Variant	Label Text
001	MMWAVEICBOOST

LBL1
PCB Label
THT-14-423-10
Size: 0.65" x 0.20 "

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

HW Assembly Instructions
1.Mount the optional nuts and washers to mate with Starter Kit
2.Mount the 6 quantity of stand offs,6 qty of washers and 6 qty of pan head screws to mate with DCA1000EVM
3.Connect the 60pin samtec cable from MMWAVEICBOOST(J10) to DCA1000EVM(J3)

Orderable: MMWAVEICBOOST		Designed for: Public Release	Mod. Date: 10/12/2018
TID #: N/A		Project Title: MMWAVEICBOOST	
Number: PROC074	Rev: A	Sheet Title:	
SVN Rev: Version control disabled		Assembly Variant: 001	Sheet: 22 of 22
Drawn By:		File: PROC074A_EVM_Hardware.SchDoc	Size: B
Engineer: Chethan Kumar Y.B		Contact: http://www.ti.com/support	

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