



REVISIONS				
REV.	DESCRIPTION	DATE	APPROVED	

- Notes:
- 1) Refer to BOM for stuffing options.
 - 2) Observe polarity on diodes and capacitors.

APPLICATION		CONTRACT NO.	DWN: www.WSTSCORP.com 10/15		Texas Instruments, Inc.				
USED ON	NEXT ASSY.		CHK:						
		UNLESS OTHERWISE SPECIFIED 1. TOLERANCES: LINEAR = : 0.26 (.010) ANGULAR = : 0.50 ° 2. ALL DIMENSIONS ARE IN MM (INCHES). 3. INTERPRET DRAWING IN ACCORDANCE WITH DOD-STD-100.	P.E.:		SIZE D CAGE CODE NO. DRAWING NO. HV-1PH-DCAC REV: 3				
			DES.ENG.:						
			P.C.:						
			Q.C.:						
			C.M.:						
			REL:						
			MFG:						
			ENG.MGR:						
			OTHER:						
			OTHER:						
MATERIAL: SEE NOTE FINISH: SEE NOTE				SCALE: 1:1			SHEET 1 OF 2		
		DO NOT SCALE DRAWING							