

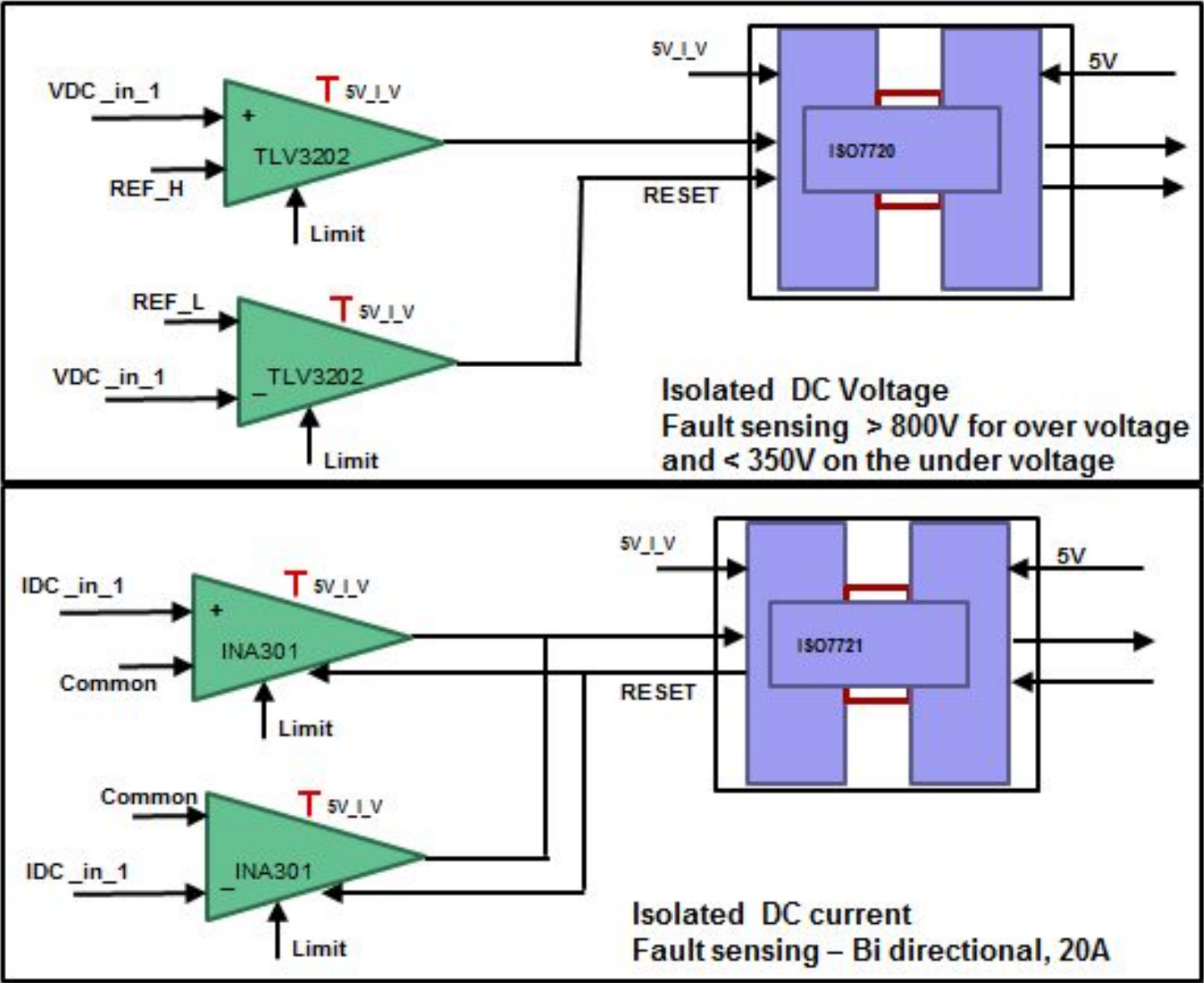
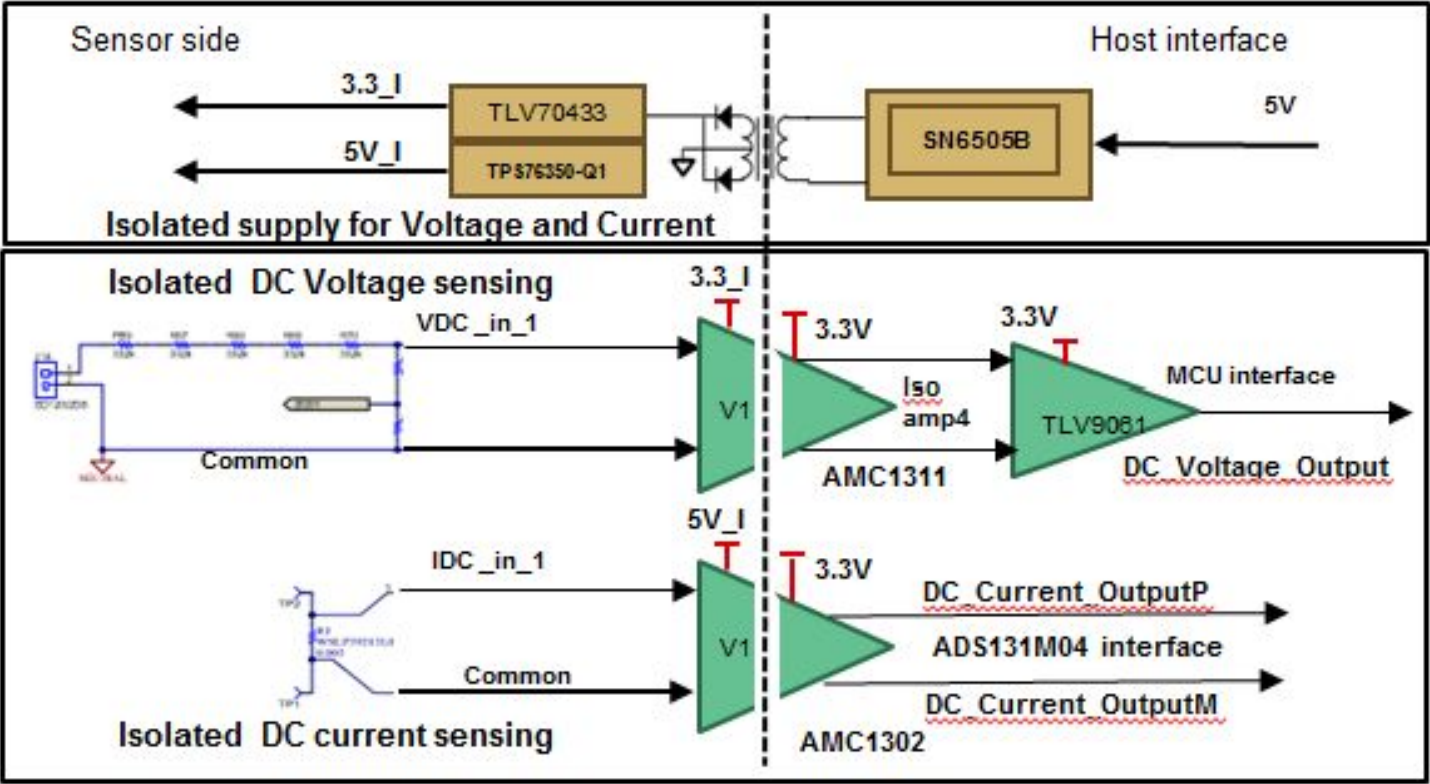


A

B

C

D

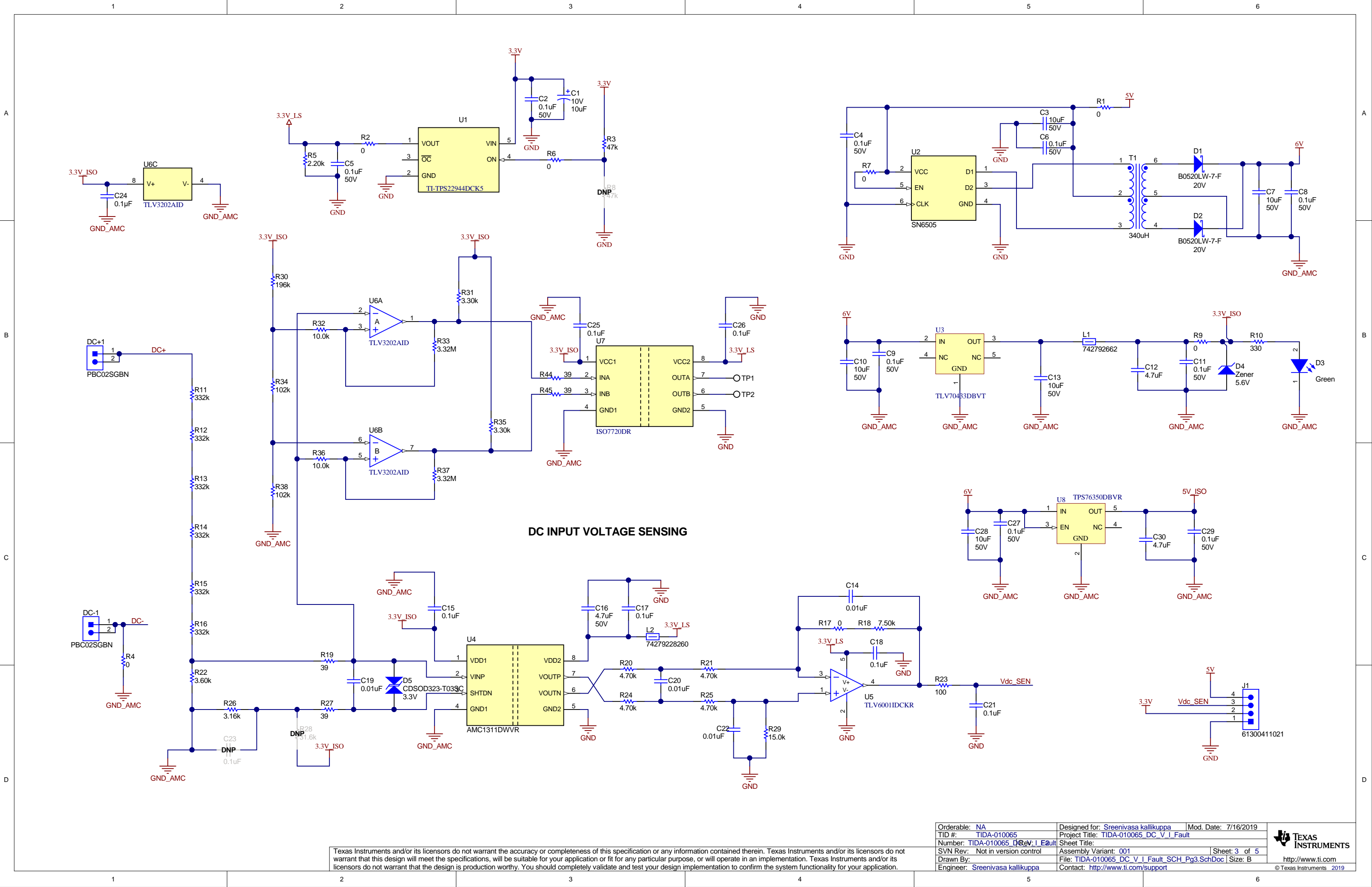


A

B

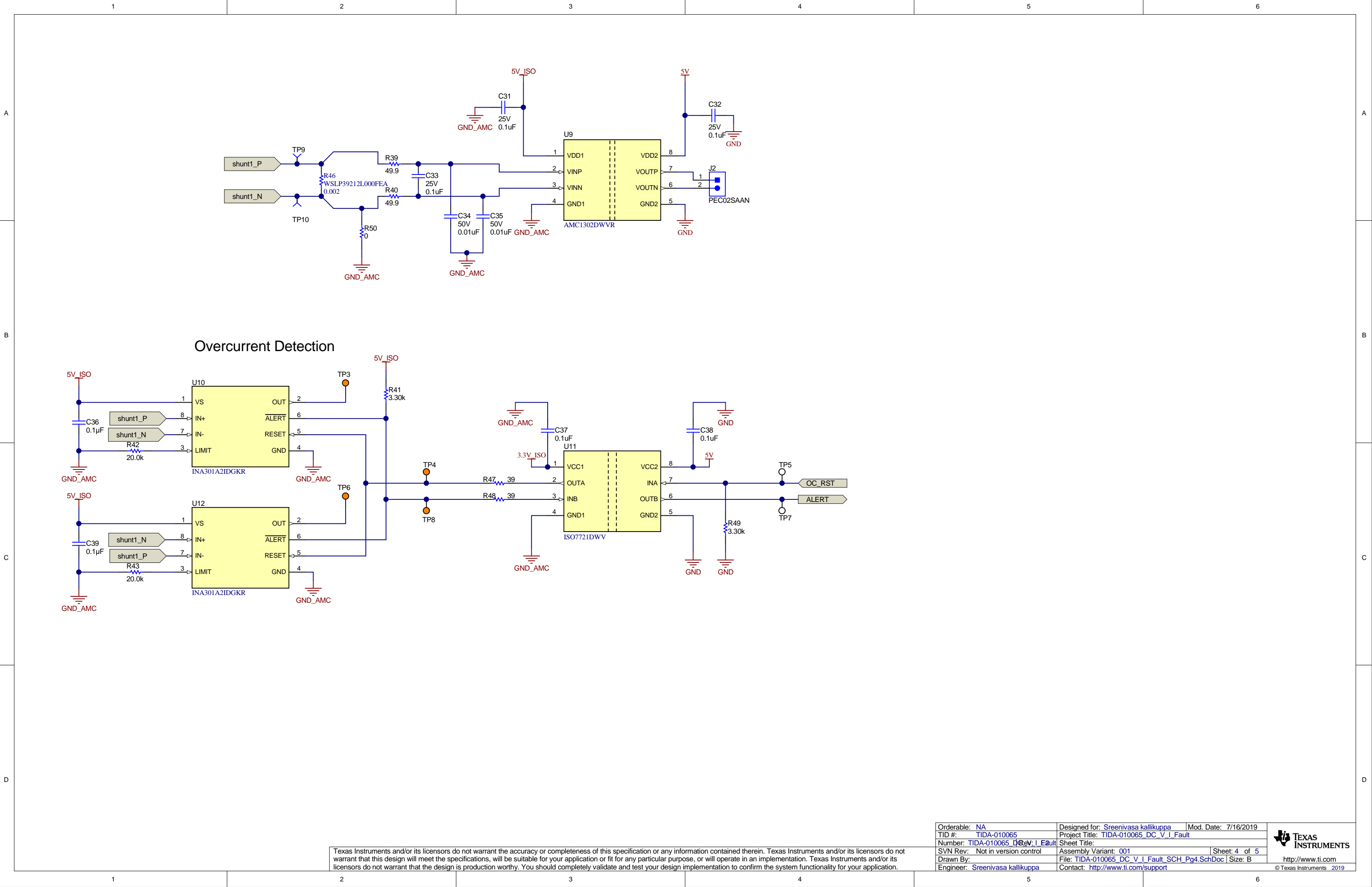
C

D



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: NA	Designed for: Sreenivasa kallikuppa	Mod. Date: 7/16/2019
TID #: TIDA-010065	Project Title: TIDA-010065_DC_V_I_Fault	
Number: TIDA-010065_D Rev: 1 Fault	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 3 of 5
Drawn By:	File: TIDA-010065_DC_V_I_Fault_SCH_Pg3.SchDoc	Size: B
Engineer: Sreenivasa kallikuppa	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	



Overcurrent Detection

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: NA	Designed for: Sreenivasa kallikuppa	Mod. Date: 7/16/2019
TID #: TIDA-010065	Project Title: TIDA-010065_DC_V_I_Fault	
Number: TIDA-010065_DC_V_I_Fault	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 4 of 5
Drawn By:	File: TIDA-010065_DC_V_I_Fault_SCH_Pg4.SchDoc	Size: B
Engineer: Sreenivasa kallikuppa	Contact: http://www.ti.com/support	

DNP

FID1

DNP

FID2

DNP

FID3

PCB Number: TIDA-010065\_DC\_V\_I\_Fault

PCB Rev: E2

PCB

LOGO

Pb-Free Symbol

PCB

LOGO

FCC disclaimer

PCB

LOGO

WEEE logo



LBL1

PCB Label

THT-14-423-10

Size: 0.65" x 0.20 "

ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Variant/Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: <a href="#">NA</a>	Designed for: <a href="#">Sreenivasa kallikuppa</a>	Mod. Date: 7/10/2019
TID #: <a href="#">TIDA-010065</a>	Project Title: <a href="#">TIDA-010065_DC_V_I_Fault</a>	
Number: <a href="#">TIDA-010065_DC_V_I_Fault</a>	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: <a href="#">001</a>	Sheet: <a href="#">5</a> of <a href="#">5</a>
Drawn By:	File: <a href="#">TIDA-010065_DC_V_I_Fault_EVM_Hardware_Pg5RevBoc</a>	
Engineer: <a href="#">Sreenivasa kallikuppa</a>	Contact: <a href="#">http://www.ti.com/support</a>	