

Page 2

Block Diagram

Page 3

50 Pin Connector to EVM Board, Hot Swap controller, EEPROM

Page 4

Isolated SPI & Isolated Fly buck Power Supply

Page 5

Analog Output With Adaptive PS

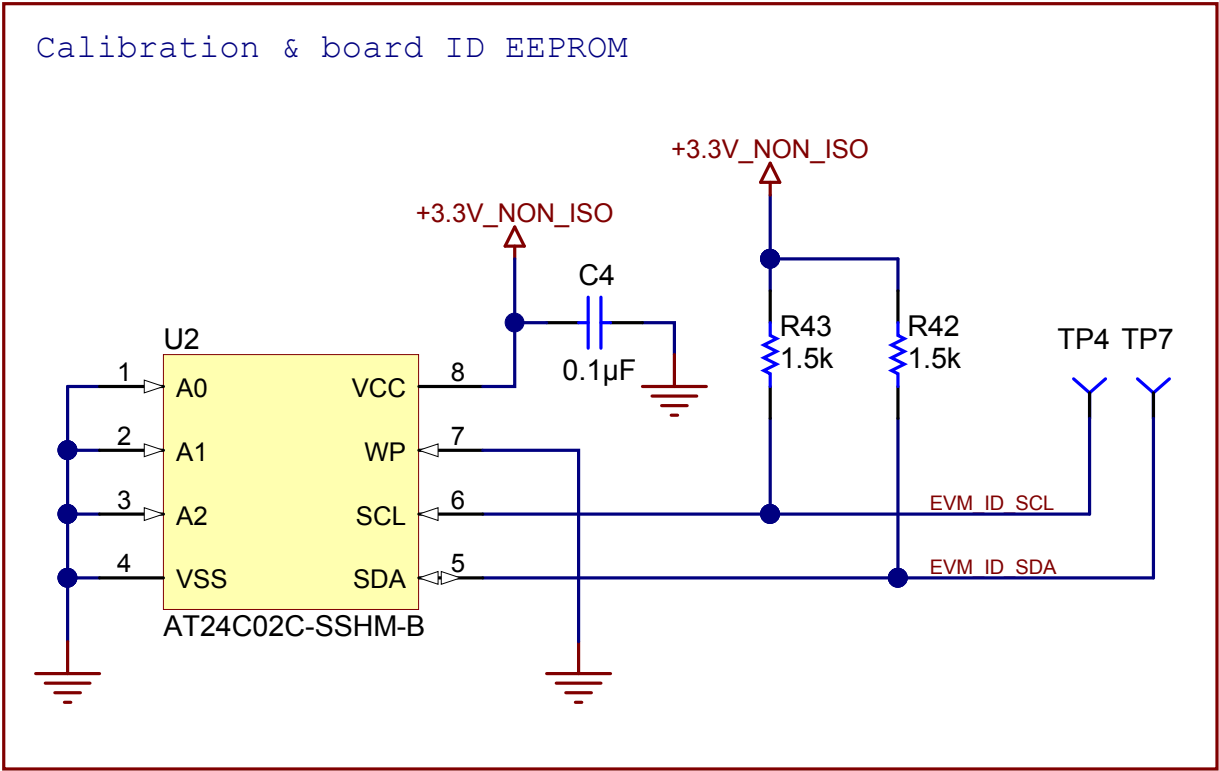
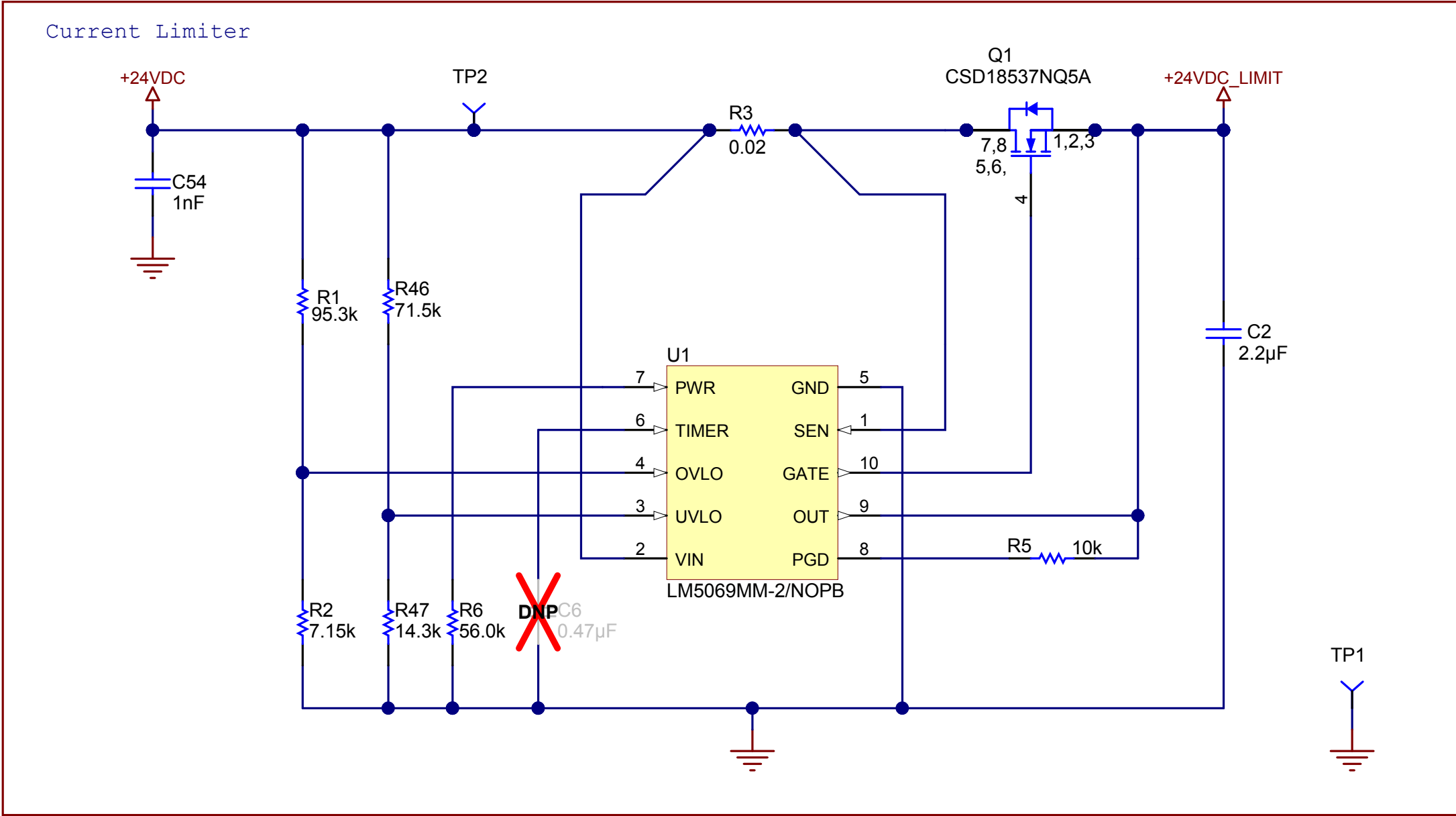
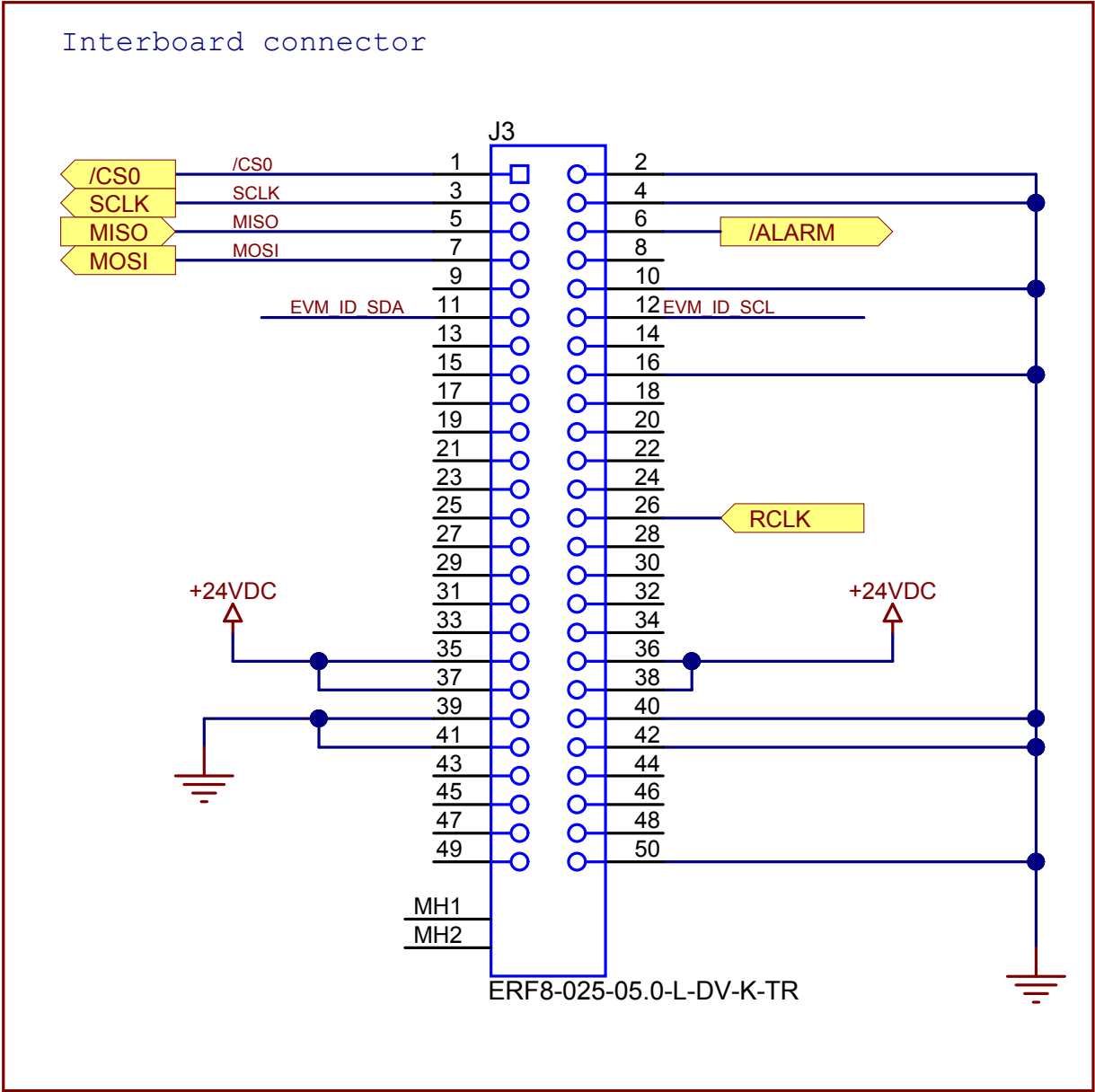
Page 6

Mounting holes, Fiducials Marking

Revision History

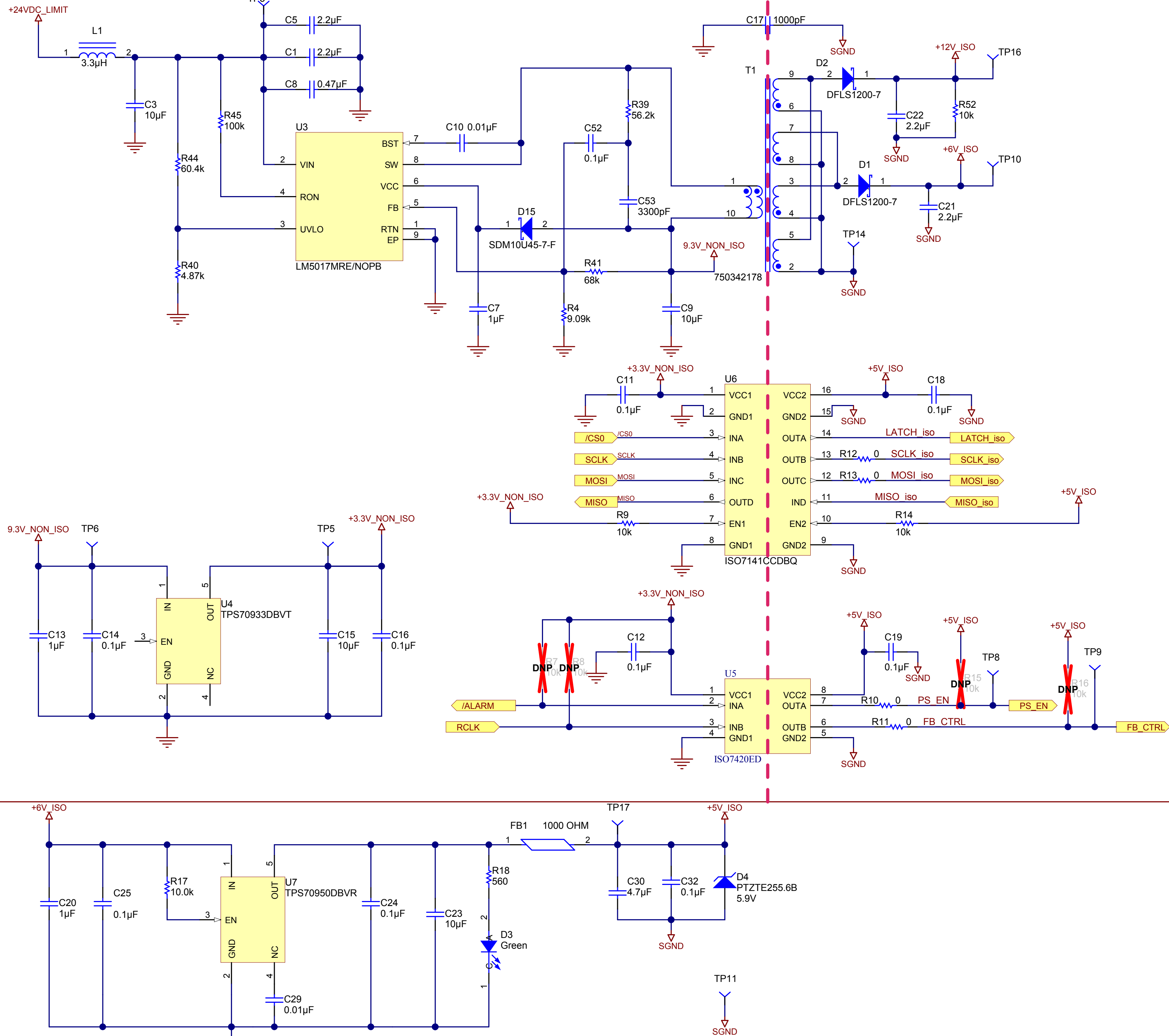
Revision	Notes





Isolated power supply and Signal Isolation

Isolated from IO controller



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Designed for: Public Release  
 Project Title: Adaptive Power Supply for DAC  
 Mod. Date: 12/12/2014  
 Number: TIDA-00231  
 Rev: E1  
 SVN Rev: Not in version control  
 Drawn By:  
 Engineer: Amol Gadkari



© Texas Instruments 2014

