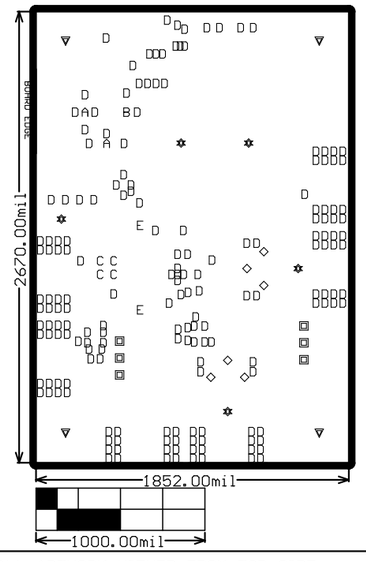
Layer	Name	Material	Thickness	Constant	Board Layer Stack
1	Top Overlay				
2	Top Solder	Solder Resist	0.40mil	3.5	
3	Top Layer L1	Copper	1.40mil		
4	Dielectric1	FR-406	20.00mil	4.6	
5	GND Plane L2	Copper	1.40mil		
6	Dielectric2	FR-406	15.00mil	4.6	
7	VDD/VDig Plane L3	Copper	1.40mil		
8	Dielectric 3	FR-406	20.00mil	4.6	
9	Bottom Layer L4	Copper	1.40mil		
10	Bottom Solder	Solder Resist	0.40mil	3.5	
11	Bottom Overlay				

DESIGN INFORMATION

MIN. TRACK WIDTH: 8 MIL
MIN. CLEARANCE: 0.2 mm
MIN. VIA PAD SIZE: 24 MIL
MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL
PER IPC-D-275 CLASS 2 LEVEL C
REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL

MATERIAL:
[ ] FR-408 [X] FR-4 [ ] OTHER _____
THICKNESS: [X] 62 MIL (1.6mm) +/-10% [ ] OTHER _____
TOLERANCE: [X] ANSI IPC-6012 TYPE 3 CLASS 2
[ ] OTHER +/- _____
BOW & TWIST: [X] ANSI IPC-6012 TYPE 3 CLASS 2
[ ] OTHER +/- _____
DRILLING:
REFERENCE: [X] AS SHOWN [X] NC_DRILL FILES
PTH MIN COPPER THICKNESS: [X] 1MIL [ ] OTHER _____
BOARD FINISH:
SILKSCREEN: [X] TOP [X] BOTTOM
SILKSCREEN COLOR: [X] WHITE [ ] OTHER _____
SOLDER RESIST COLOR:
[X] GREEN [ ] BLUE [ ] OTHER _____
SURFACE FINISH: [X] Lead-Free HAL Plating [ ] ENEPIG
[ ] IMM. TIN/SILVER OR EQUIV [ ] OTHER _____
ARRAY/PANEL: [ ] CUT AND TRIM PER MECH LAYER 1
[ ] N.C. ROUTE [X] V. SCORE
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBS TO MEET OR EXCEED THE REQUIREMENTS OF:
[X] ANSI IPC-A-600F CLASS -> [ ] 1 [X] 2 [ ] 3
[ ] UL 94V-0 [X] RoHS [ ] OTHER PER ORDER
ADDITIONAL REQUIREMENTS:
MICROSECTION: [ ] YES
BARE BOARD ELEC. TEST: [ ] NONE [X] REQUIRED [ ] PER ORDER
MANUFACTURER'S UL: [ ] RAIL [ ] METAL [X] SILK



Symbol	Quantity	Finished Hole Size	Plated	Hole Type
D	180	10.00mil (0.254mm)	PTH	Round
C	4	25.00mil (0.635mm)	PTH	Round
◇	6	29.20mil (0.742mm)	PTH	Round
◘	6	40.00mil (1.016mm)	PTH	Round
✡	5	63.00mil (1.600mm)	PTH	Round
E	2	100.00mil (2.540mm)	PTH	Round
▽	4	125.98mil (3.200mm)	PTH	Round
A	2	29.92mil (0.760mm)	PTH	Slot
B	1	39.76mil (1.010mm)	PTH	Slot
	210 Total			

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

TEXAS INSTRUMENTS

PROJECT TITLE: LaserDriver_iCHB
DESIGNED FOR: Public Release
FILE NAME: LaserDriver_iCHB-A.PcbDoc

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: 6590199	REV: A	SVN REV: Not In VersionControl
LAYER NAME = [illegible]ensions	TID #: TIDA-00725		
PLOT NAME = Fabrication Drawing	GENERATED : 10/13/2016 4:42:38 PM	TEXAS INSTRUMENTS	

ENGINEER:	LAYOUT BY:
Samir Cherian	Phung Luong
SCALE: 1.00	ALTIUM DESIGNER VERSION: 16.0.9.368