NOTES, UNLESS OTHERWISE SPECIFIED:

1. The netname "DMD_P3P3V" represents connection to the +3.3V digital power plane.
2. The netname "DMD_P2P5V" represents connection to the +2.5V digital power plane.
3. The netname "DMD_P1P8V" represents connection to the +1.8V digital power plane.
4. The symbol ⏚ represents connection to the digital ground plane.
5. A "Z" suffix on a signal name indicates an active low signal.
6. All components with designators "U", "D", "Y" and "Q" are electrostatic discharge sensitive.
7. All resistor values are in ohms, 1/16W and 5% unless otherwise specified.

Z_PCB1

PCB, Automotive DLP 0.3 S450 DMD
DLP021
2514104

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REVISIONS

REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	08/28/18	08/28/18

		DWN John Cooper	DATE 8/28/18
		ENGR John Cooper	
		SYST Jeff Farris	
		PRJ Jason Thompson	
NEXT ASSY	USED ON		
APPLICATION		SW Cadence Capture 16.6	

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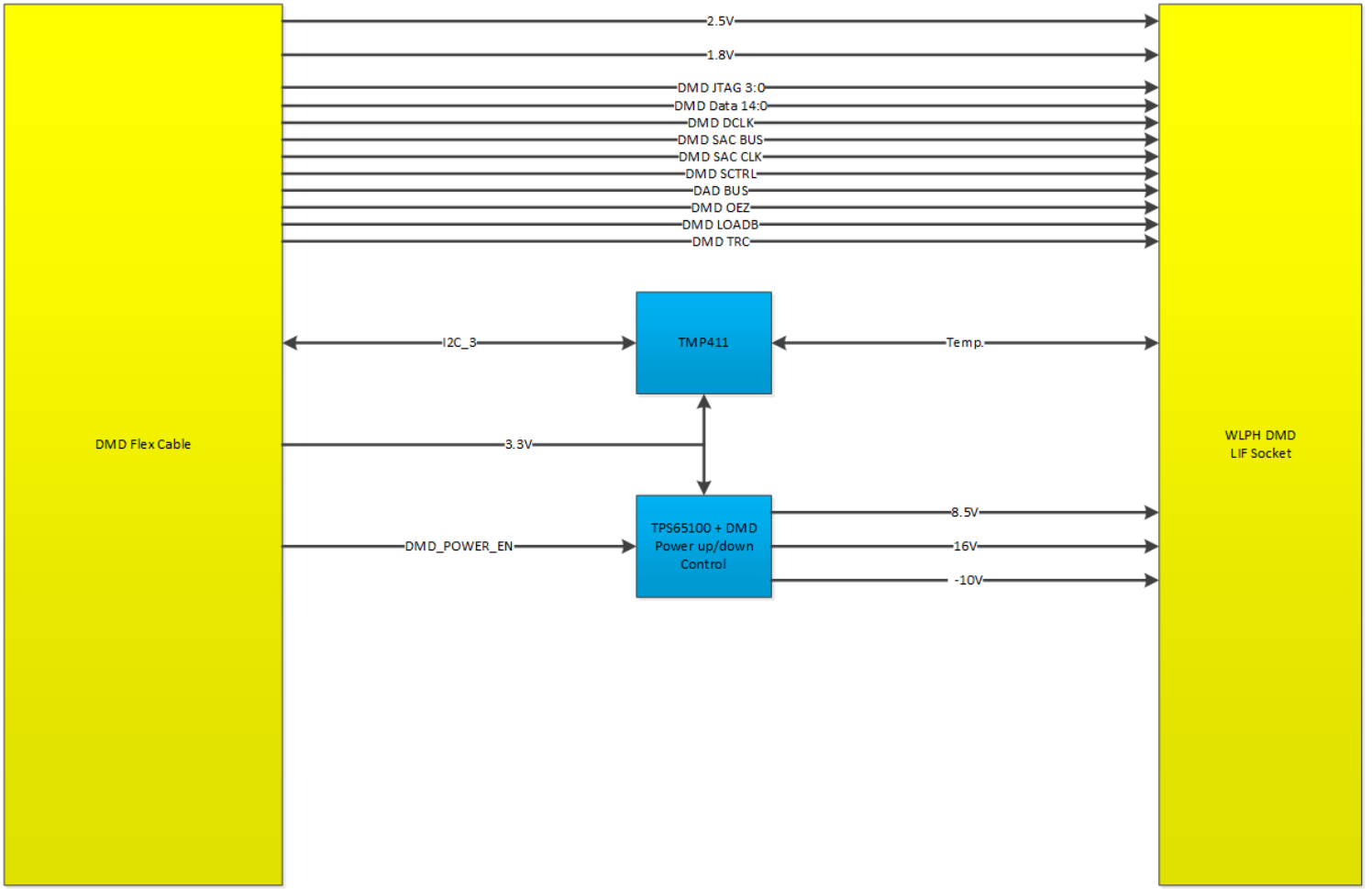
TITLE
Schematic, TIDA-080004 .3 S450 DMD Board
DLP021 TIDA-080004

D | DRAWING NO 2516392 | REV A

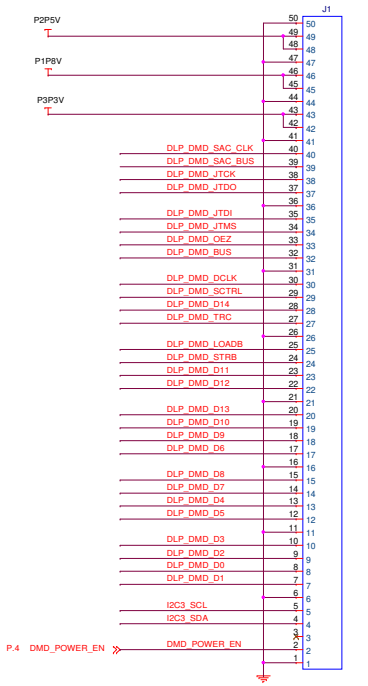
SCALE | SHEET 1 of 4



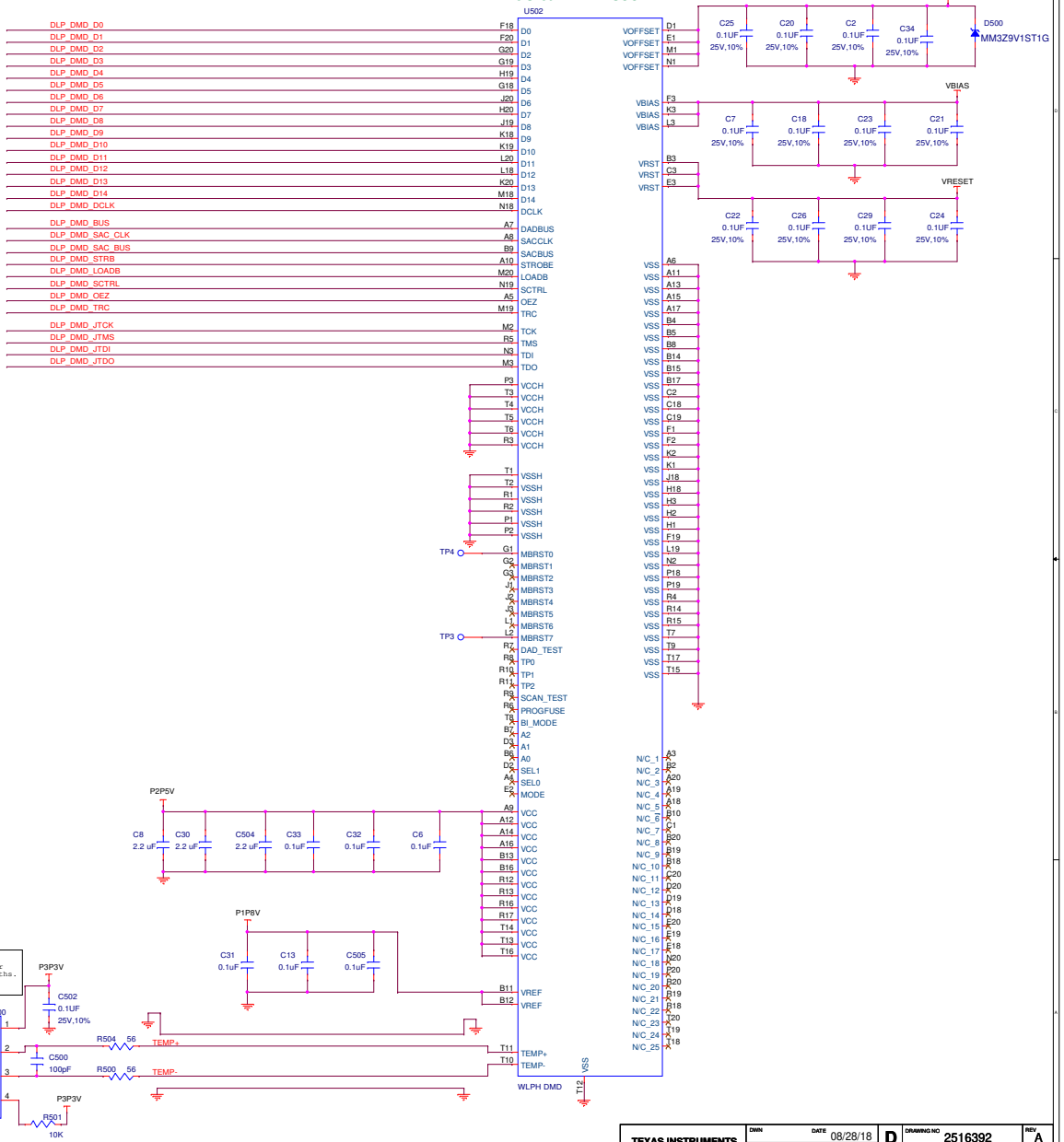
DMD BLOCK DIAGRAM

TEXAS INSTRUMENTS	DWN	DATE 08/28/18	D	DRAWING NO 2516392	REV A
	ISSUE DATE 08/28/18			SCALE	SHEET 2 OF 4

DMD Connector



.3 S450 DMD LIF SOCKET



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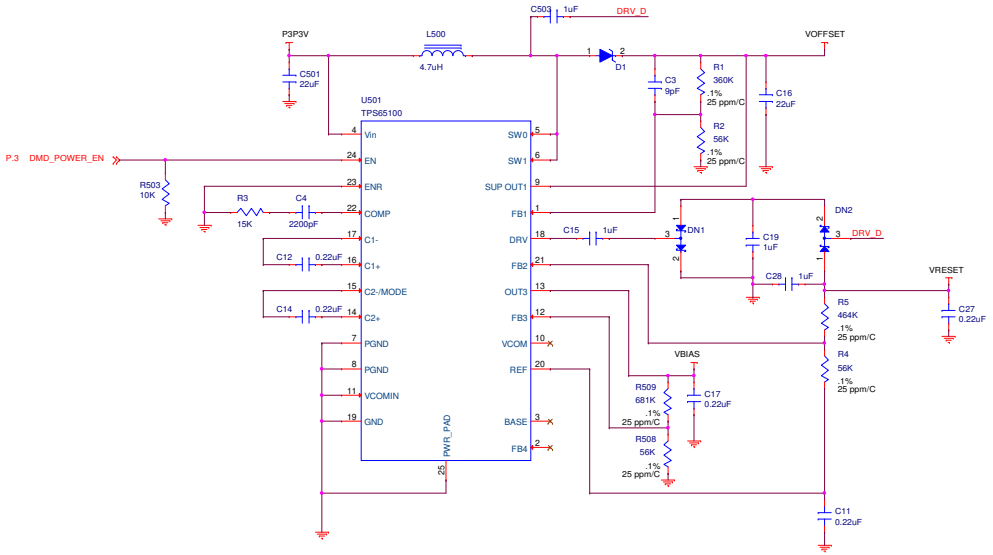
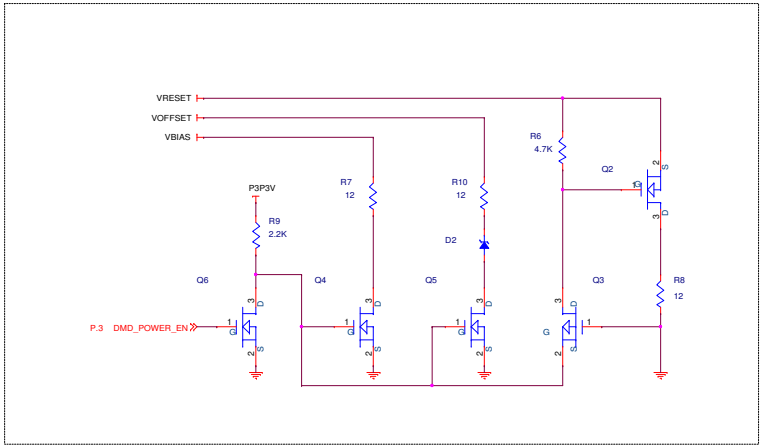
	ISSUE DATE 08/28/18			SCALE	SHEET 3 OF 4





This circuit is used to keep the power up and power down sequencing on the DMD reset voltages in spec.

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	ISSUE DATE 08/28/18		SCALE	SHEET 4 OF 4