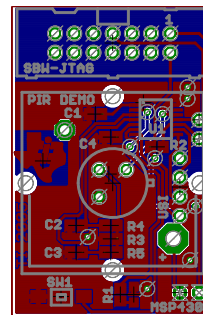
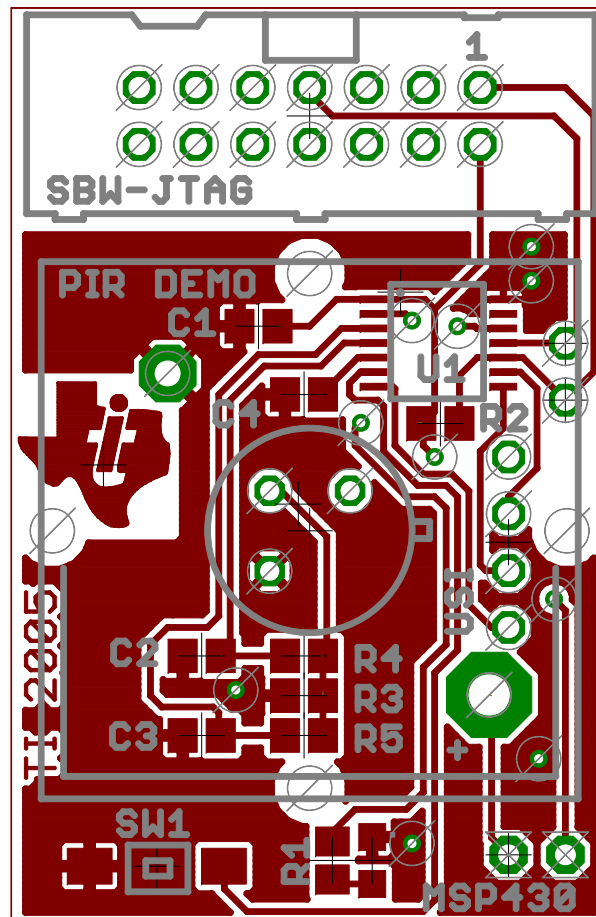


12/22/2005 12:01:57p C:\Userdata\Eagle Projects\20xx PIR Demo & Eval\20xx\_PIR\_Demo TIS.sch (Sheet: 1/1)

PCB Layout: 1x



PCB Layout Top Side: 3x



PCB Layout Bottom Side: 3x

