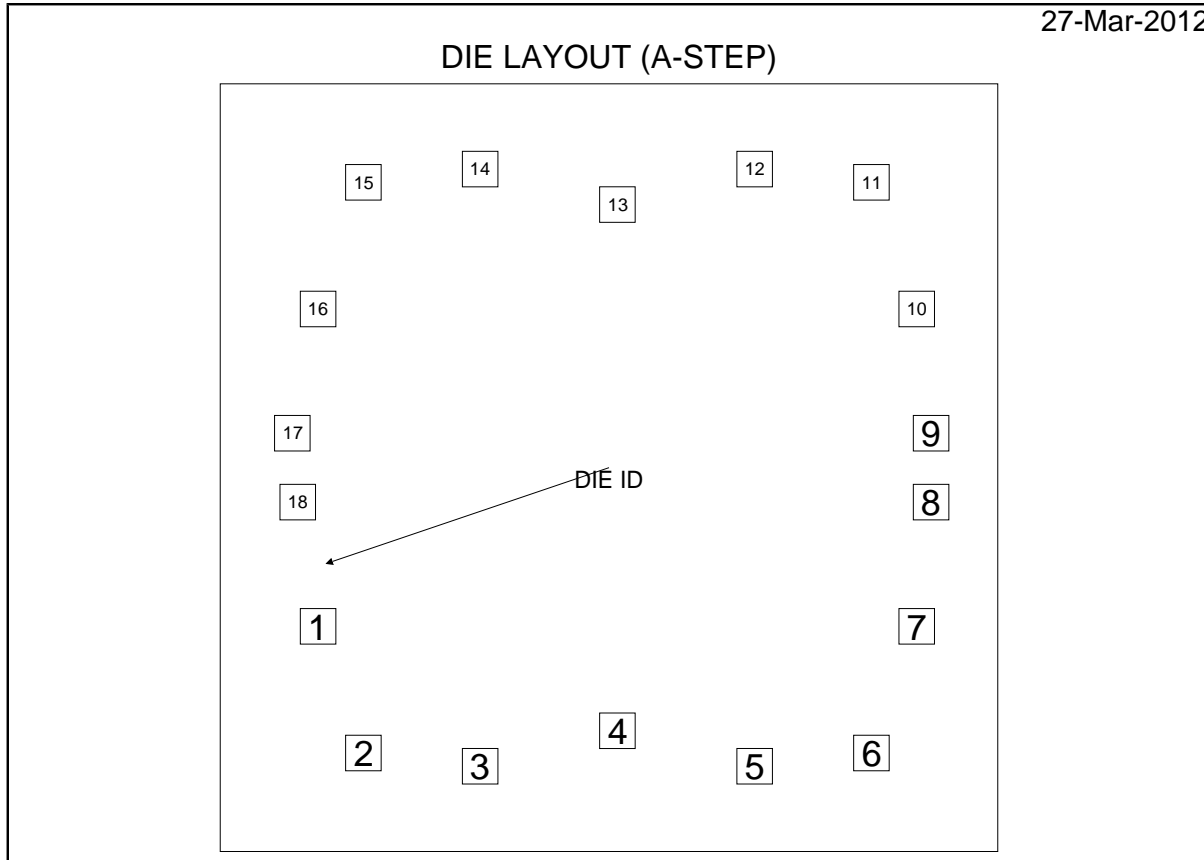


DS26LV32-MD8 WLTR MCD5080A  
3V Enhanced CMOS Quad Differential Line Receiver

27-Mar-2012



**DIE/WAFER CHARACTERISTICS**

Fabrication Attributes		General Die Information	
Physical Die Identification	DS26LV32	Bond Pad Opening Size (min)	91.70µm x 91.70µm
Die Step	A	Bond Pad Metalization	AL 0.5%CU
Physical Attributes		Passivation	PECVDOX NITRIDE
Wafer Diameter	152.4mm	Back Side Metal	Bareback
Die Size (Drawn)	2006.60µm x 1981.20µm 79.0mils x 78.0mils	Back Side Connection	GND
Thickness	304.8µm Nominal		
Min Pitch	177.80µm		

Note: All values are rounded to the nearest micron.

Special Assembly Requirements:

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Die Bond Pad Coordinate Locations(A-Step)						
(Referenced to die center, coordinates in $\mu\text{m}$ ) NC = No Connection, N.U. = Not Used						
Signal Name	Pad Number	X/Y Coordinates		Pad Size		
		X	Y	X	Y	
RI 1-	1	-751	-410	92	x	92
RI 1+	2	-634	-736	92	x	92
RO 1	3	-333	-771	92	x	92
EN	4	22	-679	92	x	92
RO 2	5	376	-771	92	x	92
RI 2+	6	677	-736	92	x	92
RI 2-	7	794	-410	92	x	92
GND	8	831	-89	92	x	92
GND	9	831	89	92	x	92
RI 3-	10	794	410	92	x	92
RI 3+	11	677	736	92	x	92
RO 3	12	376	771	92	x	92
EN*	13	22	679	92	x	92
RO 4	14	-333	771	92	x	92
RI 4+	15	-634	736	92	x	92
RI 4-	16	-751	410	92	x	92
Vcc	17	-817	89	92	x	92
Vcc	18	-803	-89	92	x	92

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**3V Enhanced CMOS Quad Differential Line Receiver**

**Notes**

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