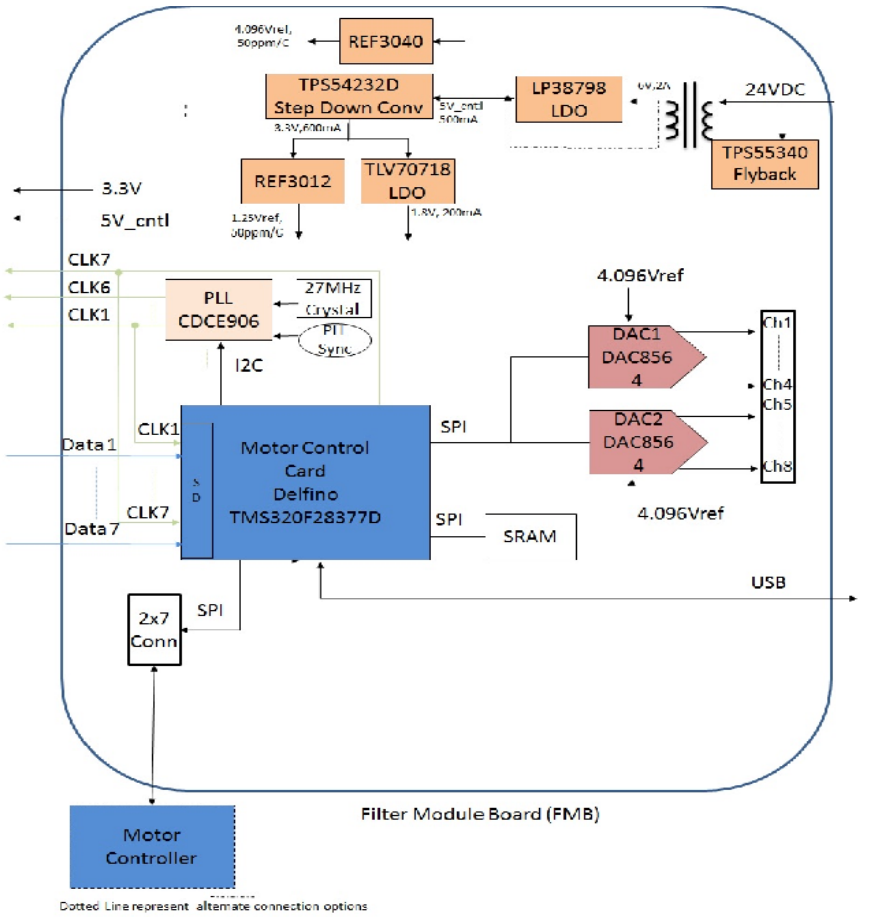
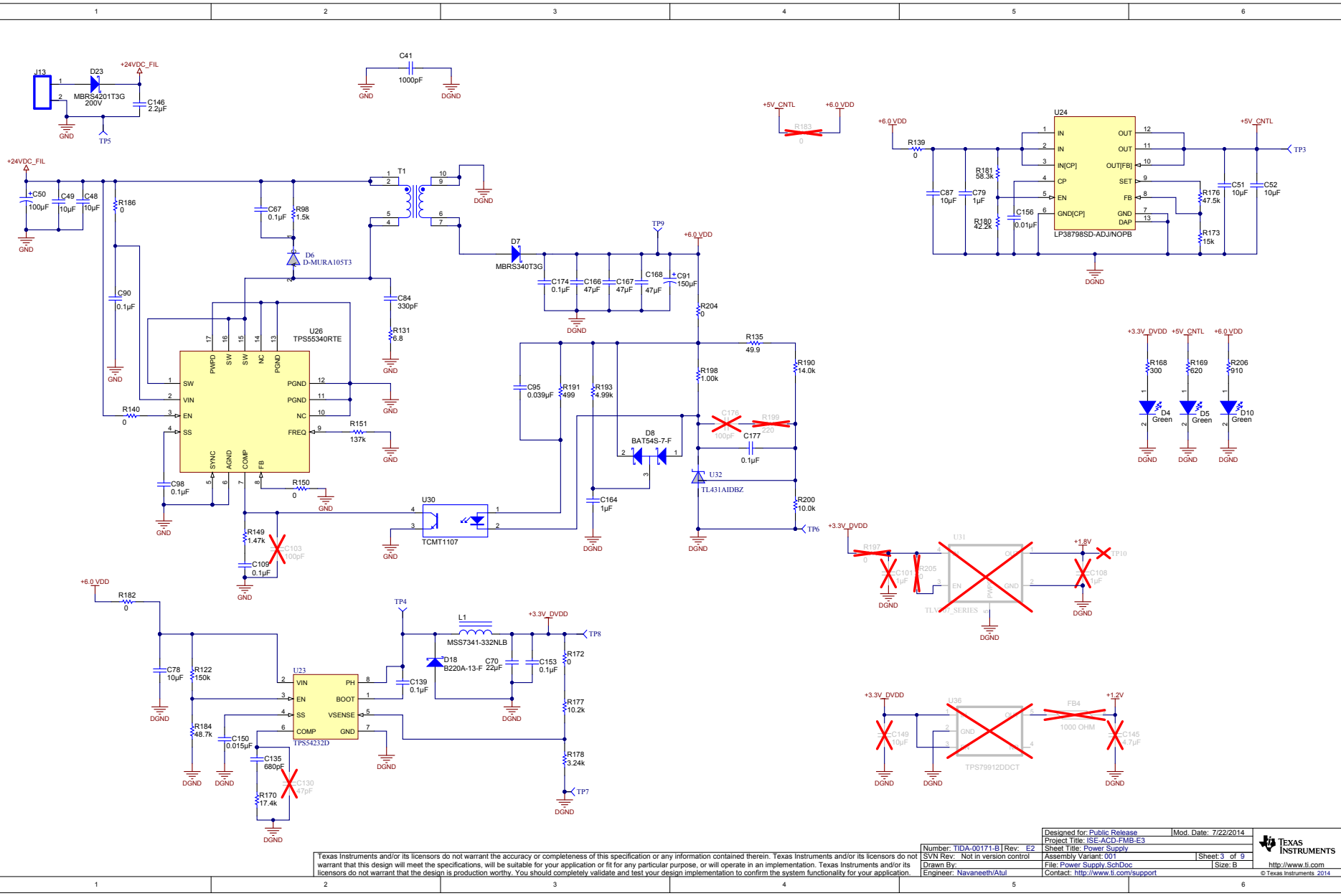
Revision History	
Revision	Notes

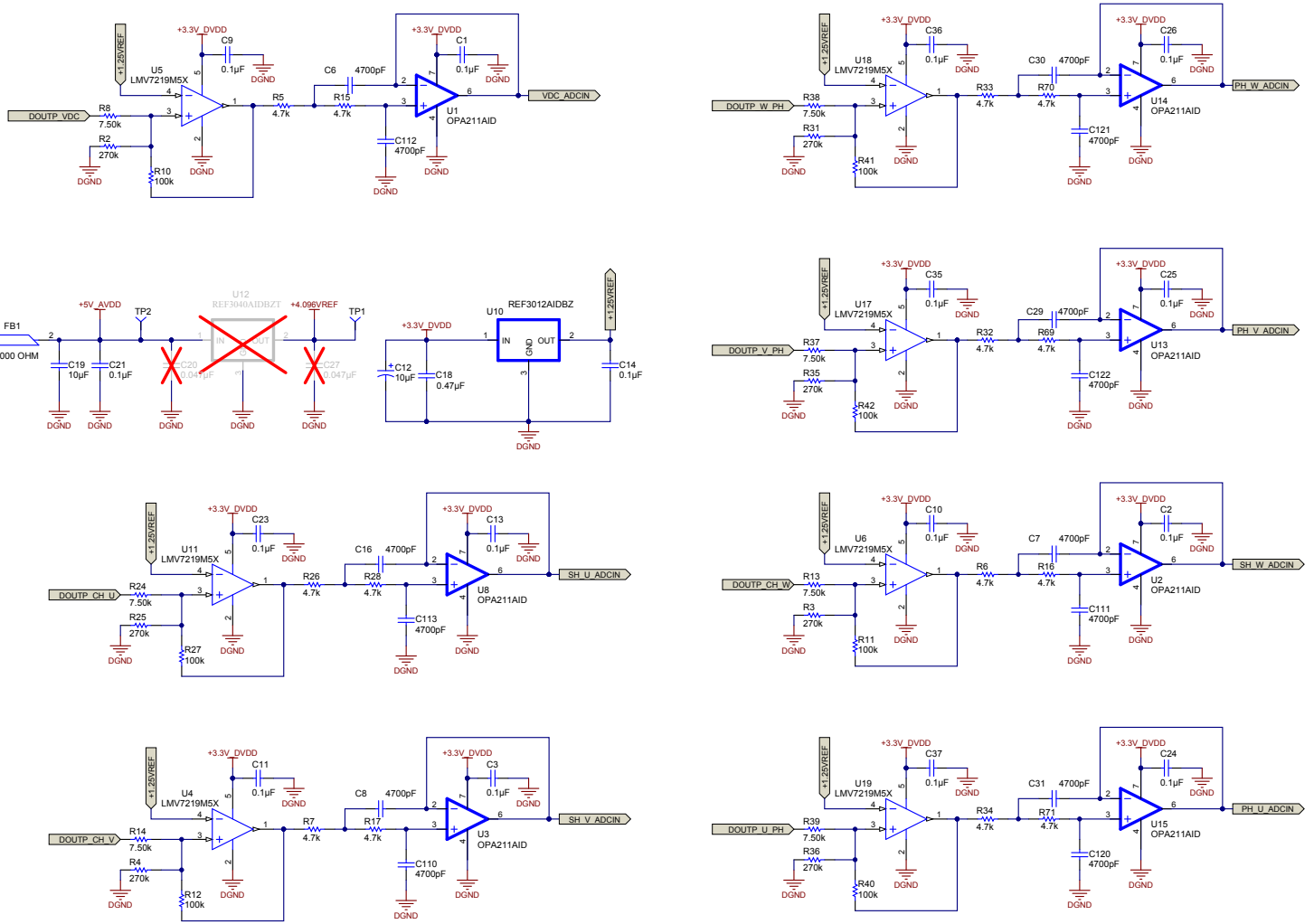
Number: TIDA-00171-B	Rev: E2	Designed for: Public Release	Mod. Date: 7/22/2014
SVN Rev: Not in version control		Project Title: ISE-ACD-FMB-E3	
Drawn By:		Sheet Title: Index page	
Engineer: Navaneeth/Atul		Assembly Variant: 001	Sheet: 1 of 9
		File: Index Pg.SchDoc	Size: B
		Contact: http://www.ti.com/support	http://www.ti.com



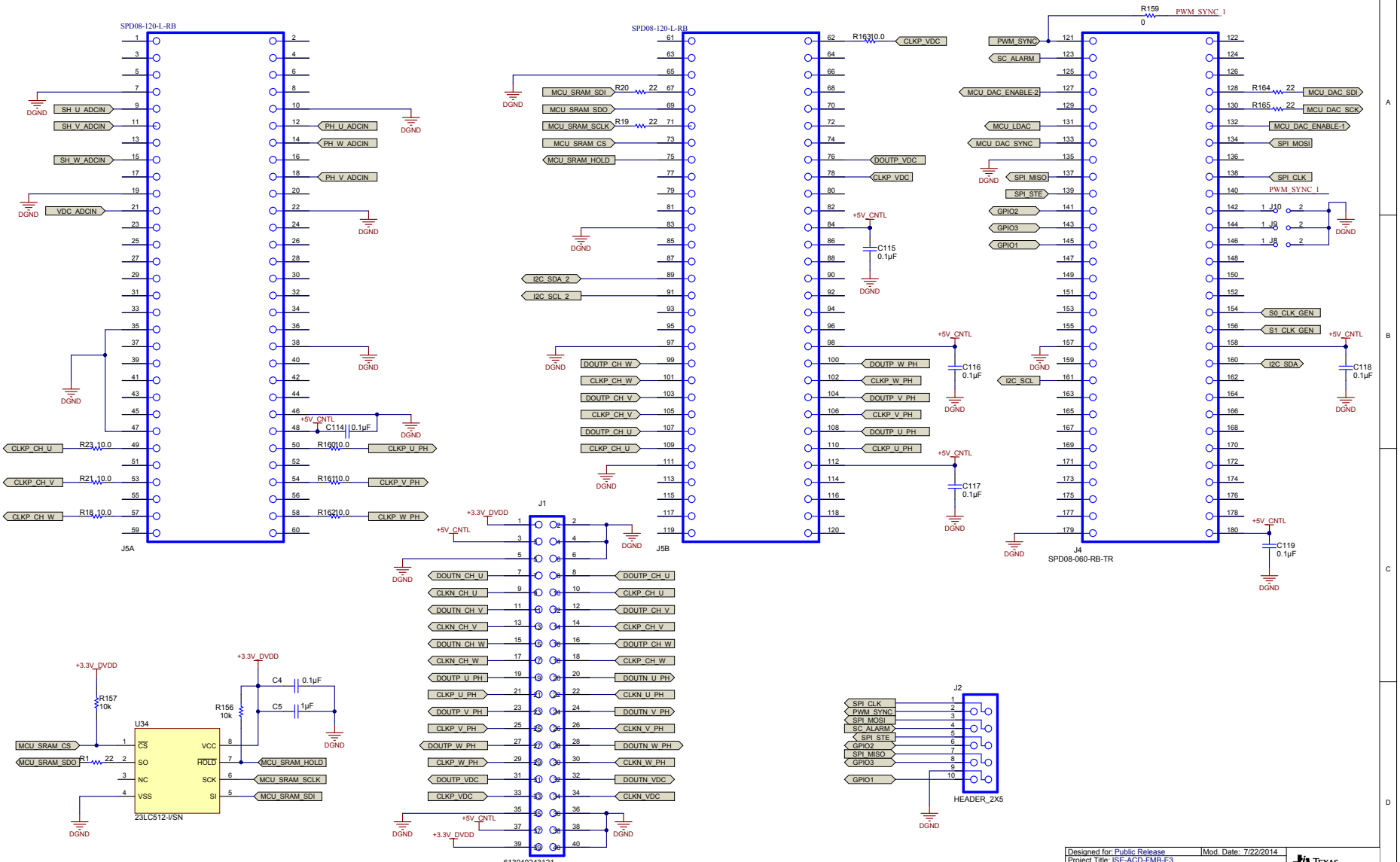
Designed for: Public Release	Mod. Date: 7/22/2014		
Project Title: ISE-ACD-FMB-E3			
Number: TIDA-00171-B	Rev: E2	Sheet Title: Block Diagram	
SVN Rev: Not in version control		Assembly Variant: 001	Sheet: 2 of 9
Drawn By:		File: Block Diagram.SchDoc	Size: B
Engineer: Navaneeth/Atul		Contact: http://www.ti.com/support	http://www.ti.com



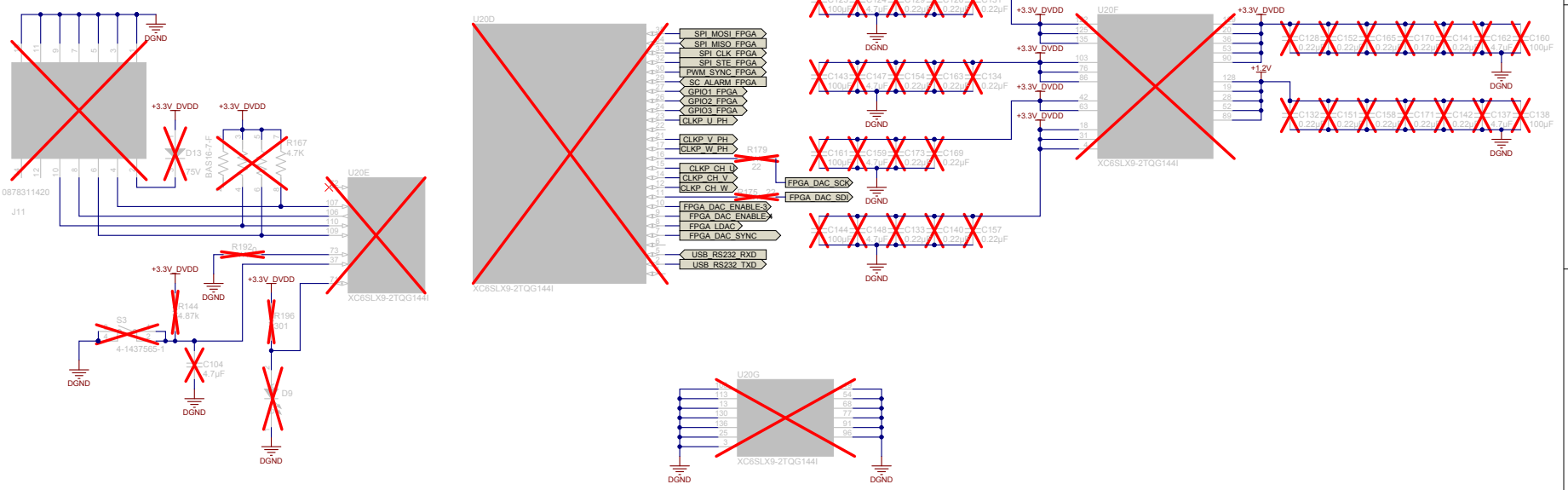
Designed for: Public Release		Mod. Date: 7/22/2014	
Project Title: ISE-ACD-FMB-E3			
Sheet Title: Power Supply			
Number: TIDA-00171-B	Rev: E2	Assembly Variant: 001	Sheet: 3 of 9
SVN Rev: Not in version control			
Drawn By:		File: Power Supply.SchDoc	Size: B
Engineer: Navaneeth/Atul		Contact: http://www.ti.com/support	http://www.ti.com



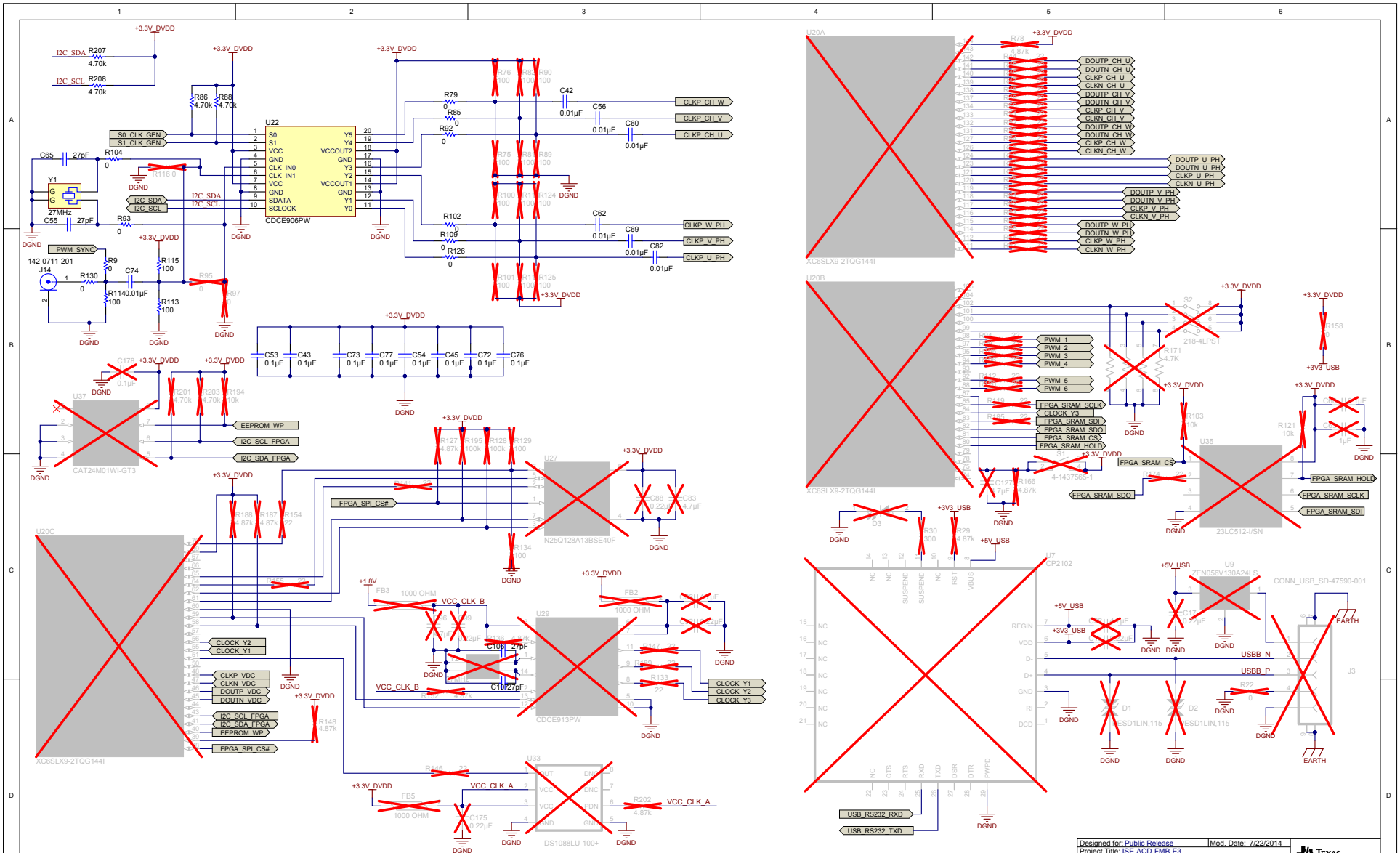
Designed for: Public Release		Mod. Date: 7/22/2014	
Project Title: ISE-ACD-FMB-E3			
Number: TIDA-00171-B	Rev: E2	Sheet Title: SDM OP Filtering	
SVN Rev: Not in version control		Assembly Variant: 001	Sheet: 4 of 9
Drawn By:		File: SDM OP Filtering.SchDoc	Size: B
Engineer: Navaneeth/Atul		Contact: http://www.ti.com/support	http://www.ti.com



Designed for: Public Release		Mod. Date: 7/22/2014	
Project Title: ISE-ACD-FMB-E3			
Sheet Title: Control card connector			
Number: TIDA-00171-B	Rev: E2	Assembly Variant: 001	
SVN Rev: Not in version control		Sheet: 5 of 9	
Drawn By:		File: Control card connector.SchDoc	Size: B
Engineer: Navaneeth/Atul		Contact: http://www.ti.com/support	http://www.ti.com



Designed for: Public Release	Mod. Date: 7/22/2014		
Project Title: ISE-ACD-FMB-E3			
Number: TIDA-00171-B	Rev: E2	Sheet Title: FPGA Power	
SVN Rev: Not in version control		Assembly Variant: 001	Sheet: 6 of 9
Drawn By:		File: FPGA Power.SchDoc	Size: B
Engineer: Navaneeth/Atul		Contact: http://www.ti.com/support	http://www.ti.com



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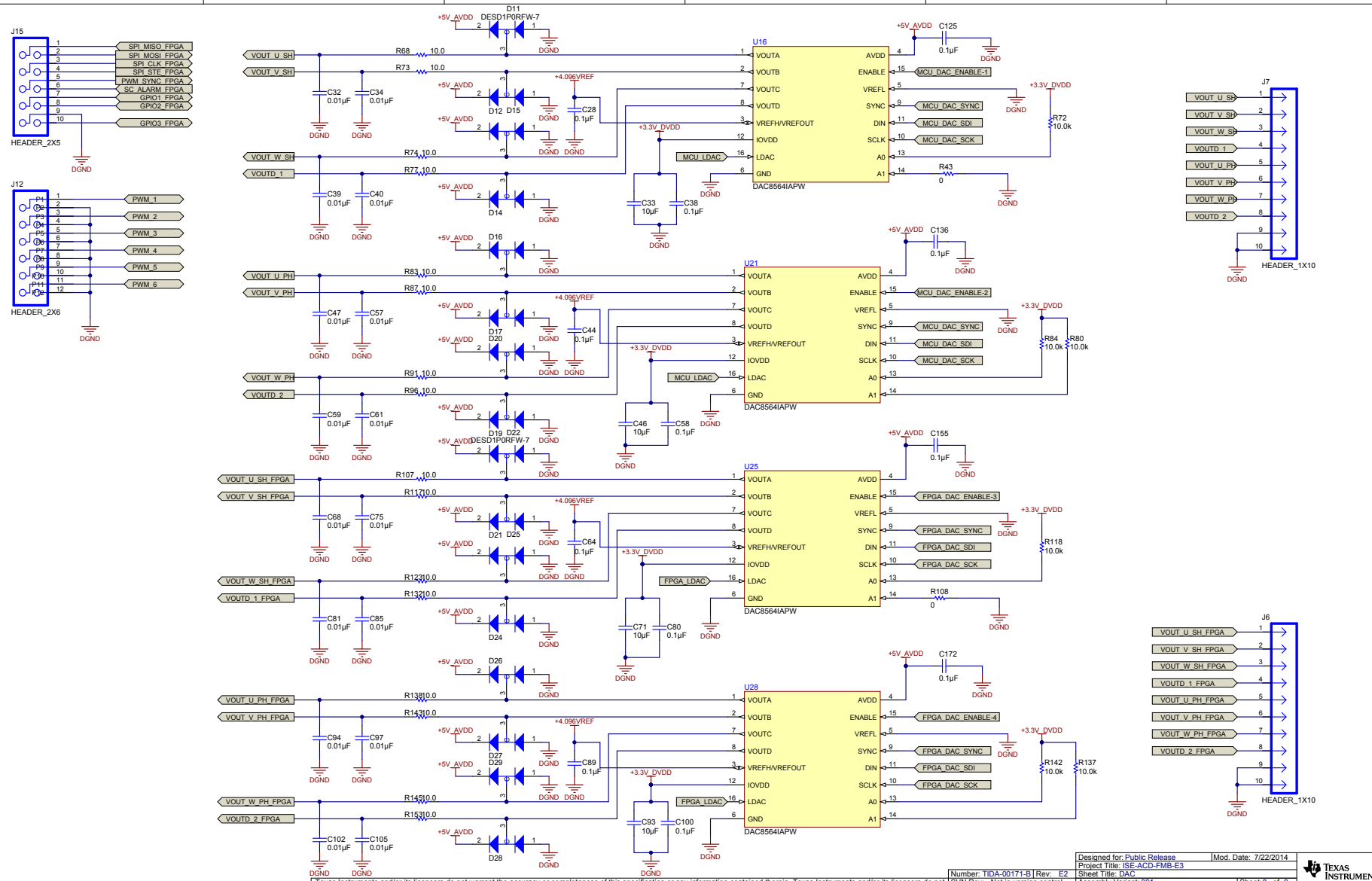
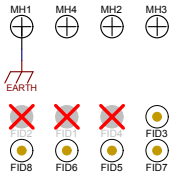
Designed for: Public Release		Mod. Date: 7/22/2014	
Project Title: ISE-ACD-FMB-E3			
Number: TIDA-00171-B	Rev: E2	Sheet Title: FPGA CLOCK_USB	
SVN Rev: Not in version control		Assembly Variant: 001	Sheet: 7 of 9
Drawn By:		File: FPGA CLOCK_USB.SchDoc	Size: B
Engineer: Navaneeth/Atul		Contact: http://www.ti.com/support	http://www.ti.com



Number: TIDA-00171-B	Rev: E2	Designed for: Public Release	Mod. Date: 7/22/2014
SVN Rev: Not in version control		Project Title: ISE-ACD-FMB-E3	
Drawn By:		Sheet Title: DAC	
Engineer: Navaneeth/Atul		Assembly Variant: 001	Sheet: 8 of 9
		File: DAC.SchDoc	Size: B
		Contact: http://www.ti.com/support	http://www.ti.com



PCB Number: TIDA-00171-B
PCB Rev: E2

PCB
LOGO

Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

LBL1
PCB Label

ZZ1
Label Assembly Note

ZZ2
Assembly Note

ZZ3
Assembly Note

ZZ4
Assembly Note

Designed for: Public Release		Mod. Date: 7/22/2014	
Project Title: ISE-ACD-FMB-E3			
Number: TIDA-00171-B	Rev: E2	Sheet Title: Hardware - Miscellaneous	
SVN Rev: Not in version control		Assembly Variant: 001	Sheet: 9 of 9
Drawn By:		File: Hardware.SchDoc	Size: B
Engineer: Navaneeth/Atul		Contact: http://www.ti.com/support	http://www.ti.com