







DS90UB9722-Q1 SNLS752 - DECEMBER 2023

DS90UB9722-Q1 Automotive FPD-Link IV Dual Deserializer Hub With D-PHY CSI-2 **Output Ports for 8MP+ Cameras & Other Sensors**

1 Features

- AEC-Q100 Qualified for automotive applications:
 - Device temperature grade 2: –40°C to 105°C
- Deserializer aggregates data from up to 2 sensors simultaneously
 - Supports 7/8MP+ imagers
 - Line rate at 7.55 Gbps
 - Single-ended coaxial or Shielded Twisted-Pair (STP) cable
 - Power-over-Cable (PoC) support
- CSI v2.1 Compliant system interface
 - Supports up to 2 MIPI CSI-2 output ports
 - 16 Virtual Channels and VC-ID remapping
- MIPI D-PHY v2.1 compatible
 - Up to 4 data lanes with 2 clocks per port
 - Up to 2.5 Gbps per lane, 10 Gbps per port
- Two CMLOUT output ports for off-board processing
 - 2 x CMLOUT ports replicated from 2 x FPD RX
 - Capable of driving another deserializer at the same line rate
- 10 GPIO Pins for sensor synchronization and diagnostics
- Dual I2C ports
- · Frozen frame detection
- Automatic receiver equalization
- Compatible with DS90UB971-Q1, DS90UB981-Q1, DS90UB953-Q1, DS90UB953A-Q1, DS90UB951-Q1, DS90UB935-Q1, DS90UB933-Q1, and DS90UB913A-Q1 serializers
- Low Power Sleep Mode with GPIO-state retention

2 Applications

- Automotive Driver Assistance Systems (ADAS)
- Security and surveillance
- Industrial and consumer remote cameras
- Medical imaging systems

3 Description

The DS90UB9722-Q1 is an FPD-Link deserializer that delivers robust ultra-high-speed 7.55 Gbps forward channel and 47.1875 Mbps Bidirectional Control Channel for connecting up to two raw data sensors to central processing units over an automotive coaxial or STP cable. When coupled with DS90UB971-Q1 serializer, the deserializer receives video data from imagers supporting ultra-high resolutions (8MP+/ 40fps) or multiple sensors in various topologies.

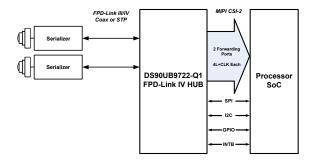
Data is received and aggregated into two MIPI CSI-2 D-PHY outputs for interfacing with a downstream processor. The flexible MIPI CSI-2 outputs support multiple virtual channels interleaving per port to differentiate multiple sensors, exposures, and data types. This functionality features video aggregation and replication modes and supports input-to-output port as well as virtual channel (VC-ID) remapping.

Advanced data protection and diagnostic features support overall system functional safety. Multiple levels of data integrity checking and protection in conjunction with programmable health status interrupt helps ensure robust sensor module and link operation in vehicles.

Device Information

PART NUMBER	PACKAGE (1)	PACKAGE SIZE (2)				
DS90UB9722-Q1	VQFNP (88)	12.00 mm × 12.00 mm				

- For more information, see Section 5. (1)
- The package size (length × width) is a nominal value and includes pins, where applicable.



Typical Application Schematic



4 Device and Documentation Support

4.1 Documentation Support

4.1.1 Related Documentation

For related documentation see the following:

- Texas Instruments, I2C Communication Over FPD-Link III with Bidirectional Control Channel, application note
- · Texas Instruments, I2C Bus Pullup Resistor Calculation, application note

4.2 Support Resources

TI E2E[™] support forums are an engineer's go-to source for fast, verified answers and design help — straight from the experts. Search existing answers or ask your own question to get the quick design help you need.

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4.3 Trademarks

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4.4 Electrostatic Discharge Caution



This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

4.5 Glossary

TI Glossary

This glossary lists and explains terms, acronyms, and definitions.

5 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

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PACKAGING INFORMATION

Orderable Device	Status	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead finish/ Ball material	MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Samples
DS90UB9722TRURRQ1	ACTIVE	VQFNP	RUR	88	2500	RoHS & Green	NIPDAUAG	Level-3-260C-168 HR	-40 to 105	UB9722	Samples
DS90UB9722TRURTQ1	ACTIVE	VQFNP	RUR	88	250	RoHS & Green	NIPDAUAG	Level-3-260C-168 HR	-40 to 105	UB9722	Samples

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) RoHS: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

Green: TI defines "Green" to mean the content of Chlorine (CI) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

- (3) MSL, Peak Temp. The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.
- (4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
- (5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.
- (6) Lead finish/Ball material Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

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PACKAGE OPTION ADDENDUM

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PACKAGE MATERIALS INFORMATION

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TAPE AND REEL INFORMATION





	-
A0	Dimension designed to accommodate the component width
В0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
DS90UB9722TRURRQ1	VQFNP	RUR	88	2500	330.0	24.4	12.3	12.3	1.1	16.0	24.0	Q2
DS90UB9722TRURTQ1	VQFNP	RUR	88	250	180.0	24.4	12.3	12.3	1.1	16.0	24.0	Q2

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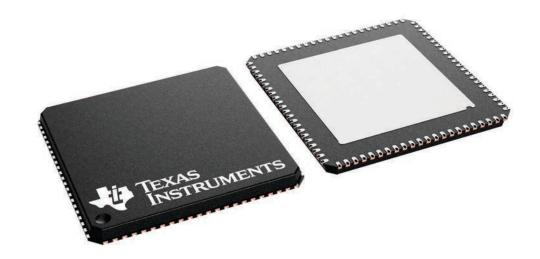
*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
DS90UB9722TRURRQ1	VQFNP	RUR	88	2500	367.0	367.0	45.0
DS90UB9722TRURTQ1	VQFNP	RUR	88	250	211.0	193.0	46.0

12 x 12, 0.5 mm pitch

PLASTIC QUAD FLATPACK - NO LEAD

This image is a representation of the package family, actual package may vary. Refer to the product data sheet for package details.



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