EVM User's Guide: TPS56A37EVM

TPS56A37 Step-Down Converter Evaluation Module

Description

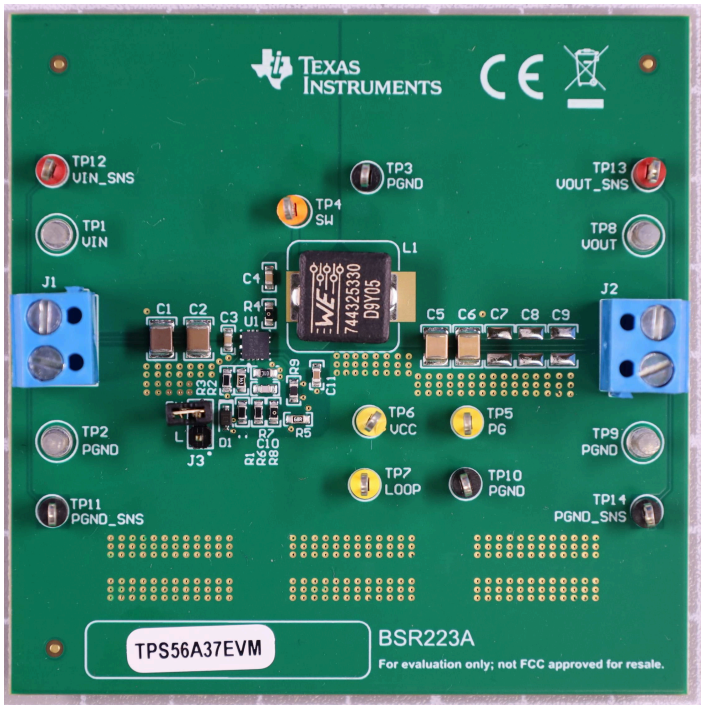
The TPS56A37 is a high efficiency, high-voltage input, easy-to-use synchronous buck converter. With the wide operating input voltage range of 4.5 V to 28 V, the TPS56A37 is designed for systems powered from 12-V, 19-V, 24-V power-bus rails. The device supports up to 10-A continuous output current. The output voltage range is from 0.6 V to 13 V. DCAP3™ control mode provides an easy-to-design, stable regulation with very little external components, and supports cost-effective ceramic capacitors.

Features

- 4.5-V to 28-V input voltage range
- 0.6-V to 13-V output voltage range (default: 5-V)
- 10-A continuous output current capability
- Supports up to 98% duty operation
- 10-Pin 3.0-mm × 3.0-mm QFN HotRod™ package

Applications

- Industrial PC, EPOS, factory automation and control
- Multifunction printers, video conference system
- Monitors, TV, speakers, PC and notebooks, portable electronics
- General purposes for 12-V,19-V, 24-V power-bus supply



TPS56A37EVM Front Photo

1 Evaluation Module Overview

1.1 Introduction

The TPS56A37EVM evaluation module (EVM) is a single, synchronous buck converter providing 5-V at 10-A output from 5.5-V to 28-V input. This user's guide describes the TPS56A37EVM performance.

Table 1-1 shows the rated input voltage and output current ranges for the evaluation module.

Table 1-1. Input Voltage and Output Current Summary

EVM	Input Voltage (V_{IN}) RANGE	OUTPUT CURRENT (I_{OUT}) RANGE
TPS56A37EVM	5.5 V to 28 V	0 A to 10 A

This user's guide contains information for the TPS56A37 as well as support documentation for the TPS56A37EVM evaluation module. This user's guide includes the performance specifications, schematic, and the bill of materials of the TPS56A37EVM.

1.2 Kit Contents

- One TPS56A37EVM Board
- EVM disclaimer Read Me

1.3 Specification

A summary of the TPS56A37EVM performance specifications is provided in Table 1-2. Specifications are given for an input voltage of V_{IN} = 24 V and an output voltage of 5 V, unless otherwise noted. The ambient temperature is 25°C for all measurement, unless otherwise noted.

Table 1-2. TPS56A37EVM Performance Specifications Summary

SPECIFICATIONS	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input voltage range (V_{IN})		5.5	24	28	V
Output voltage			5		V
Operating frequency	V_{IN} = 24 V, I_{OUT} = 10 A		500		kHz
Output current range		0		10	A
Output ripple voltage	V_{IN} = 24 V, I_{OUT} = 10 A		40		mV_{PP}
Maximum efficiency	V_{IN} = 24 V, I_{OUT} = 5 A		94.96		%

1.4 Device Information

The purpose of TPS56A37EVM is to showcase the typical application of the TPS56A37 device.

2 Hardware

2.1 Modifications

These evaluation modules are designed to provide access to the features of the TPS56A37. Some modifications can be made to this module.

2.1.1 Output Voltage Setpoint

To change the output voltage of the EVMs, change the value of resistor R6 (R_{FB_TOP}) and resistor R7 (R_{FB_BOT}). Changing the value of R6 and R7 can change the output voltage above 0.6 V. The value of R6 and R7 for a specific output voltage can be calculated using Equation 1.

$$V_{OUT} = 0.6\ x\left(1 + \frac{R6}{R7}\right) \quad (1)$$

Table 2-1 lists the R6 and R7 values for some common output voltages.

Table 2-1. Recommended Component Values

Output Voltage(1) (V)	R6(2) (kΩ)	R7 (kΩ)	L1 (µH)	C_{OUT} (3) (µF)	C10 (4)(pF)
				TYP	
1.05	7.5	10	1	68	
1.8	20	10	1.5	68	
3.3	45.3	10	2.2	62	150
5	73.2	10	3.3	35	150
9	140	10	4.7	22	100
12	383	20	5.6	17	30

(1) Please use the recommended L1 and C_{OUT} combination of the higher and closest output rail for unlisted output rails.
(2) R6 = 0 Ω for V_{OUT} = 0.6 V.
(3) C_{OUT} is the sum of effective output capacitance. In this data sheet, the effective capacitance is defined as the actual capacitance under DC bias and temperature, not the rated or nameplate values. All high value ceramic capacitors have a large voltage coefficient in addition to normal tolerances and temperature effects. A careful study of bias and temperature variation of any capacitor bank must be made to verify that the minimum value of effective capacitance is provided. Refer to the information of DC bias and temperature characteristics from manufacturers of ceramic capacitors.
(4) R8 and C10 can be used to improve the load transient response or improve the loop-phase margin. The *Optimizing Transient Response of Internally Compensated DCDC Converters with Feed-forward Capacitor* application report is helpful when experimenting with a feed-forward capacitor.

2.1.2 Adjustable UVLO

The undervoltage lockout (UVLO) can be adjusted externally using R1 ($R_{EN(TOP)}$) and R2 ($R_{EN(BOT)}$). See the *TPS56A37 4.5-V to 28-V Input, 10-A Synchronous Buck Converter Data Sheet* for detailed instructions for setting the external UVLO.

3 Implementation Results

3.1 Test Setup and Results

This section describes how to properly connect, set up, and use the TPS56A37EVM. The section also includes test results of output voltage ripple, start-up, and shut-down.

3.1.1 Input, Output Connections

Table 3-1 shows the provided input, output connectors and test points on the TPS56A37EVM. A power supply capable of supplying 10 A must be connected to J1 through a pair of 20-AWG wires. The load must be connected to J2 through a pair of 20-AWG wires. The maximum load current capability is 10 A. Wire lengths must be minimized to reduce losses in the wires. Test point TP12 provides a place to monitor the V_{IN} input voltages with TP11 providing a convenient ground reference. TP13 is used to monitor the output voltage with TP14 as the ground reference.

Table 3-1. Connection and Test Points

Reference Designator	Function
J1	V_{IN} (see Table 1-1 for V_{IN} range)
J2	V_{OUT}, 5 V at 10-A maximum
J3	EN control. Connect EN to GND to disable
TP1	V_{IN} terminal near J1
TP2	GND terminal near J1
TP3	GND test point
TP4	Switch node test point
TP5	Power good (PG) test point
TP6	Test point provided to connect external voltage source for PG pullup
TP7	Test point between voltage divider network and output. Used for loop response measurement
TP8	V_{OUT} terminal near J2
TP9	GND terminal near J2
TP10	GND test point
TP11	GND monitor test point near C1
TP12	V_{IN} monitor test point near C1
TP13	V_{OUT} monitor test point near C6
TP14	GND monitor test point near C6

3.1.2 Start-Up Procedure

1. Make sure that the jumper at J3 (Enable control) pins 1 and 2 are covered to shunt EN to GND, disabling the output.
2. Apply appropriate V_{IN} voltage to VIN (J1-1) and GND (J1-2).
3. Move the jumper at J3 (Enable control) away from pins 2 and 1 (EN and GND) to enable the output.

3.1.3 Output Voltage Ripple

The TPS56A37EVM output voltage ripple waveforms are shown below. The output currents are as indicated.

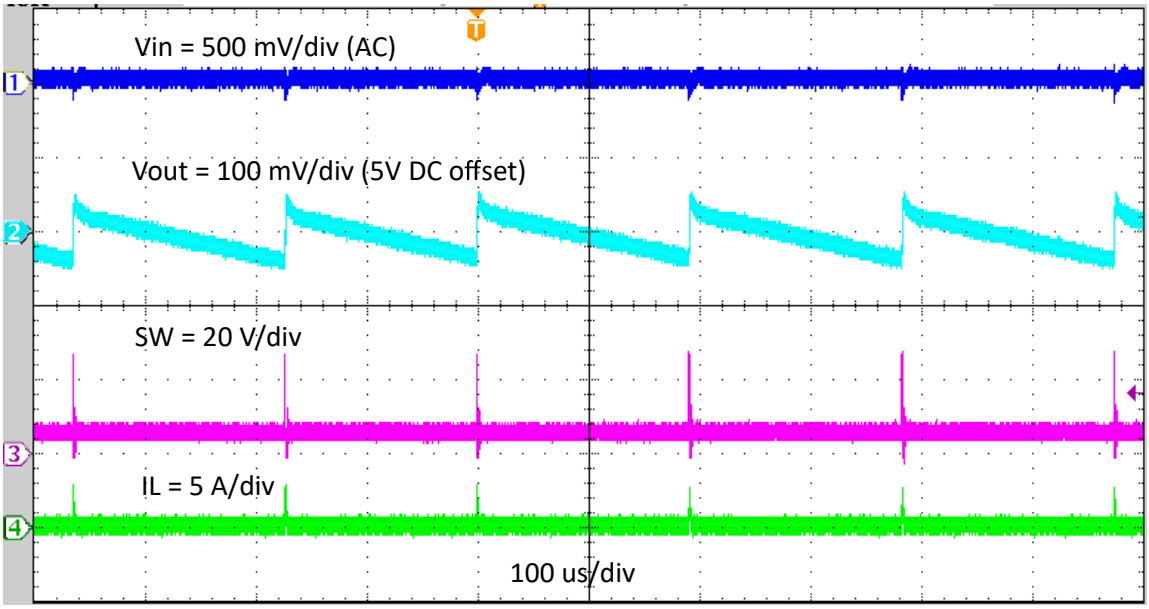


Figure 3-1. TPS56A37EVM Output Voltage Ripple, V_{IN} = 24 V, I_{OUT} = 0.01 A

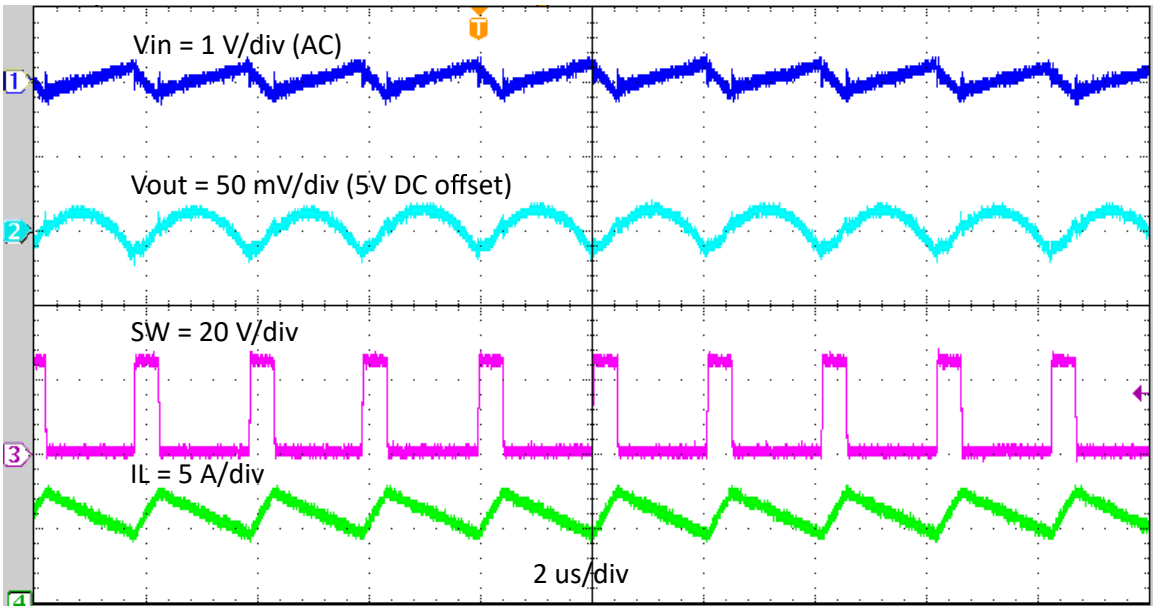


Figure 3-2. TPS56A37EVM Output Voltage Ripple, V_{IN} = 24 V, I_{OUT} = 10 A

3.1.4 Start-Up

The TPS56A37EVM start-up waveform relative to EN is shown below.

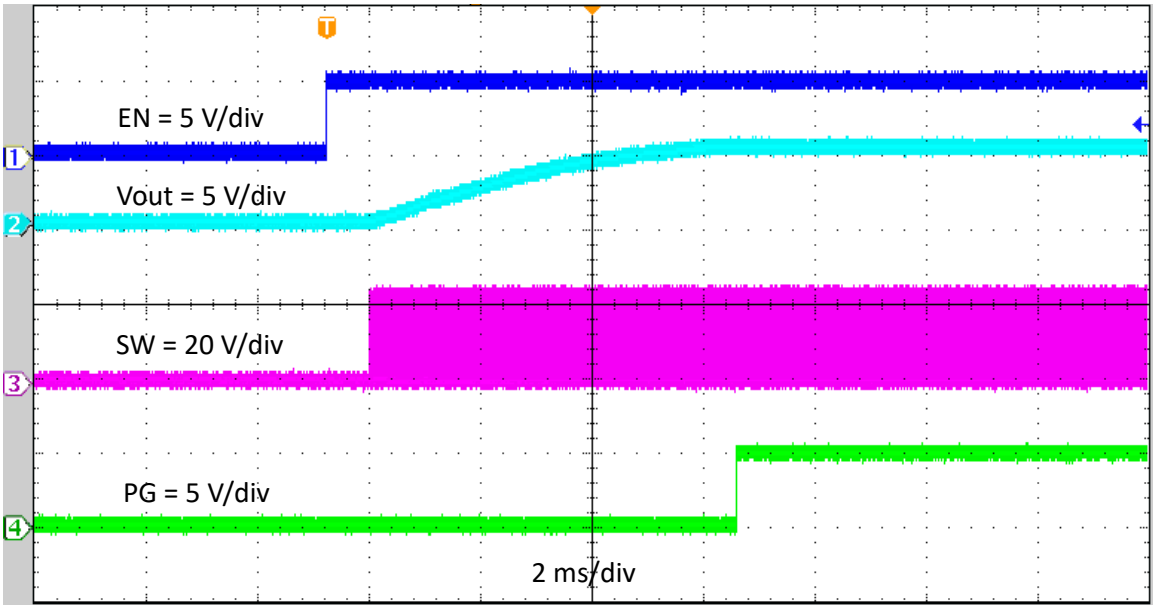


Figure 3-3. TPS56A37EVM Start-Up Relative to EN, I_{OUT} = 10 A

3.1.5 Shutdown

The TPS56A37EVM shutdown waveform relative to EN is shown below.

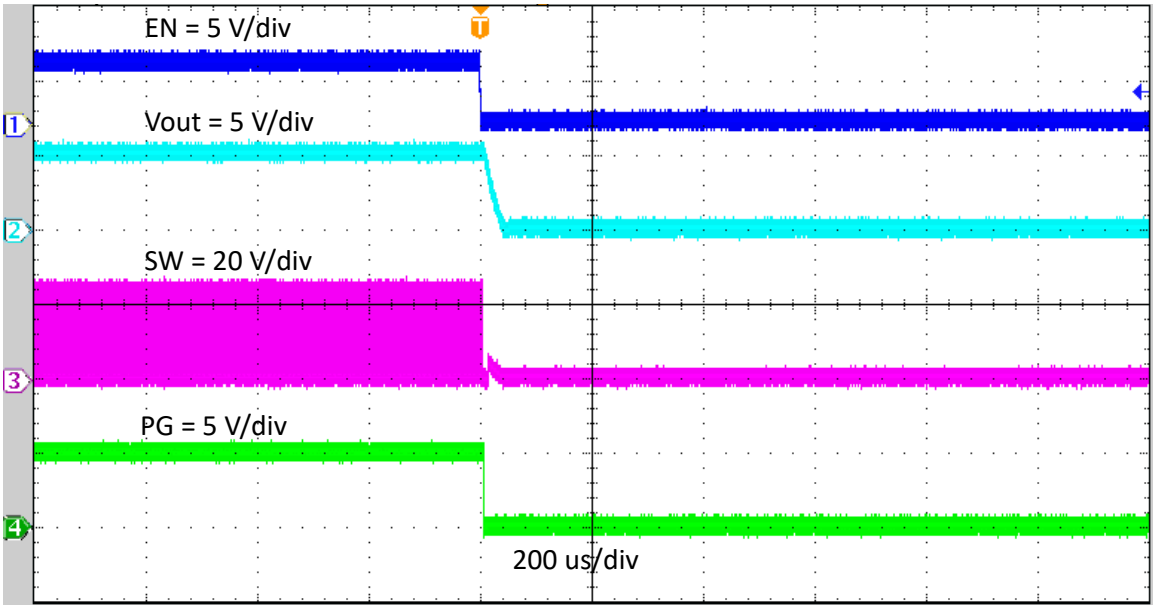


Figure 3-4. TPS56A37EVM Shutdown Relative to EN, I_{OUT} = 10 A

4 Hardware Design Files

4.1 Schematic

Figure 4-1 is the schematic for the TPS56A37EVM.

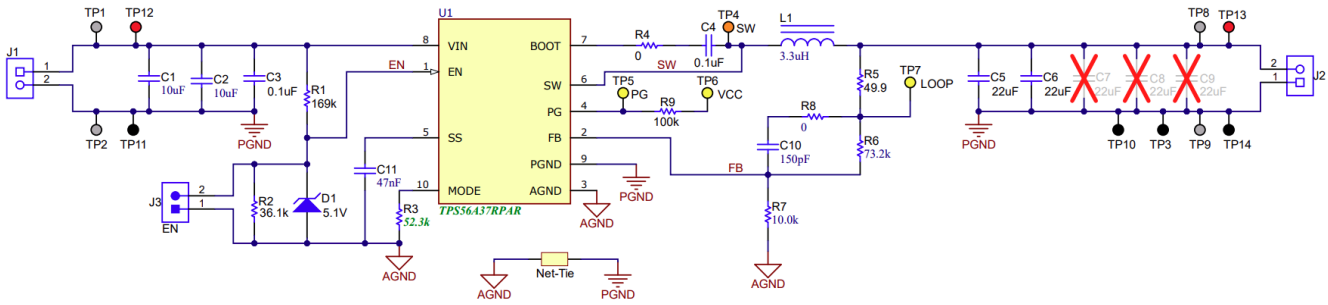


Figure 4-1. TPS56A37EVM Schematic Diagram

4.2 PCB Layout

This section provides a description of the TPS56A37EVM, board layout, and layer illustrations.

The board images are shown in Figure 4-2 and Figure 4-3. The board layouts are shown in Figure 4-4 to Figure 4-8. The top layer contains the main power traces for VIN, VOUT, and ground. Also on the top layer are connections for the pins of the TPS56837 and a large area filled with power ground (PGND). Most of the signal traces are also located on the top side. The input decoupling capacitors, C1, C2, and C3 are located as close to the IC as possible. The input and output connectors, test points, and all of the components are located on the top side. Middle layer 1, Middle layer 2, and the bottom layer are predominantly PGND planes. Analog ground (AGND) area is provided on Middle layer 1. Figure 4-6 shows the AGND and PGND are connected at a single point on the Middle layer 1. The bottom layer contains the output voltage feedback trace, the connection to the VIN pin of the EN control, and the connections of test points.

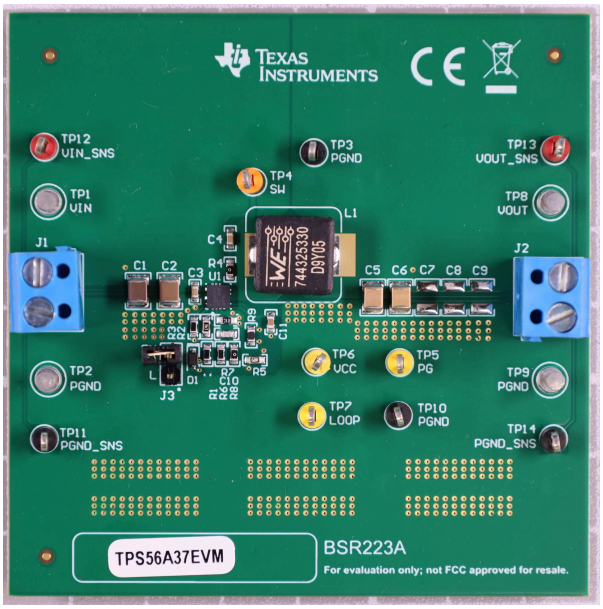


Figure 4-2. TPS56A37EVM Front Photo

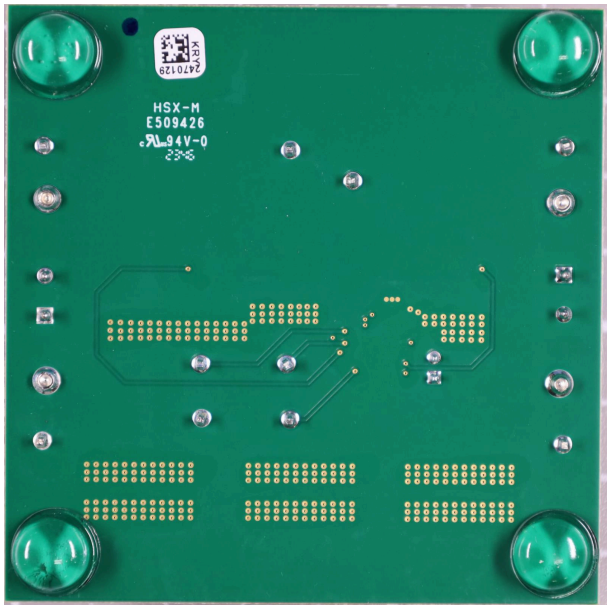


Figure 4-3. TPS56A37EVM Back Photo

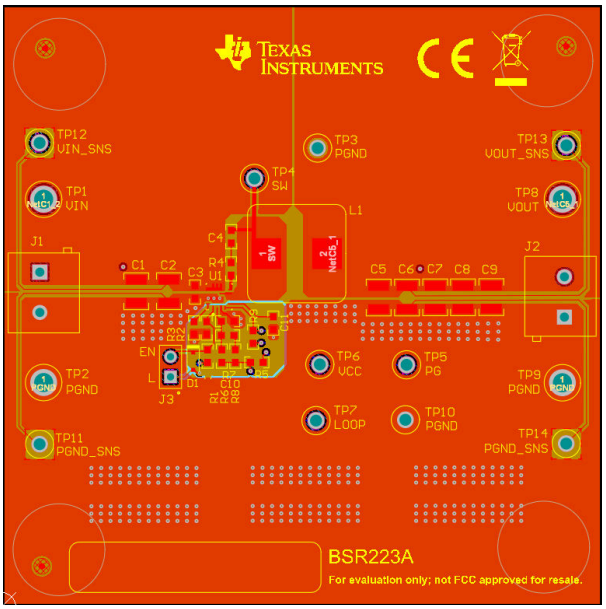


Figure 4-4. Top Assembly

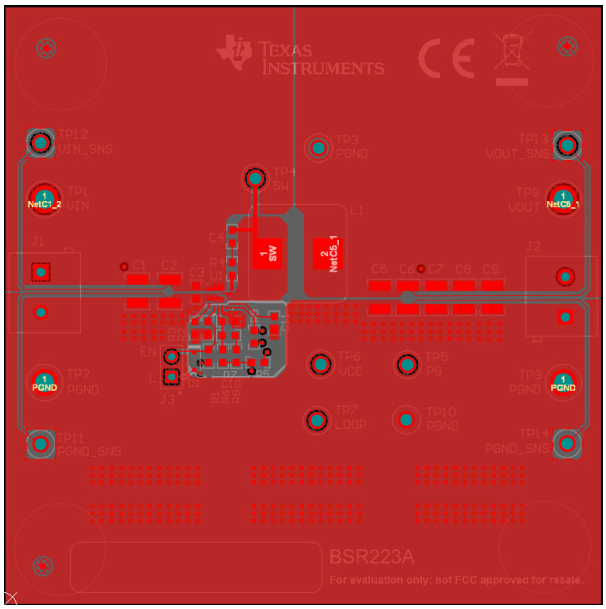


Figure 4-5. Top Layer

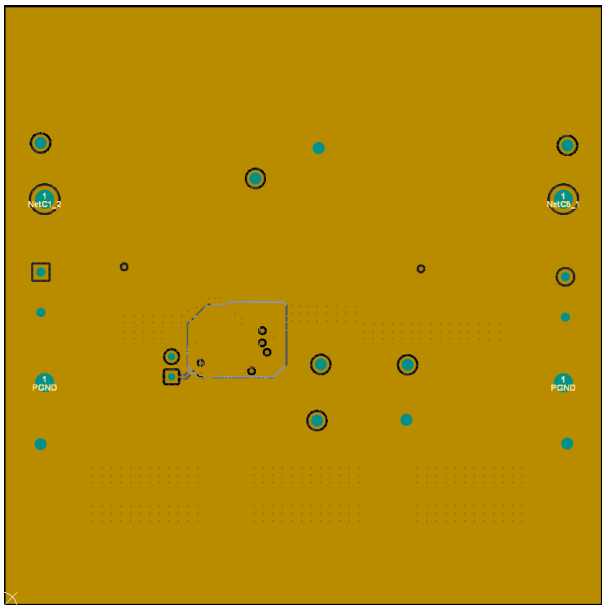


Figure 4-6. Middle Layer 1

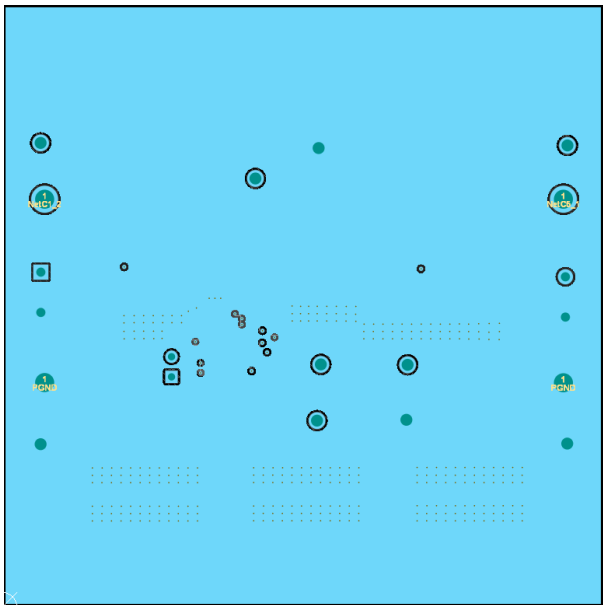


Figure 4-7. Middle Layer 2

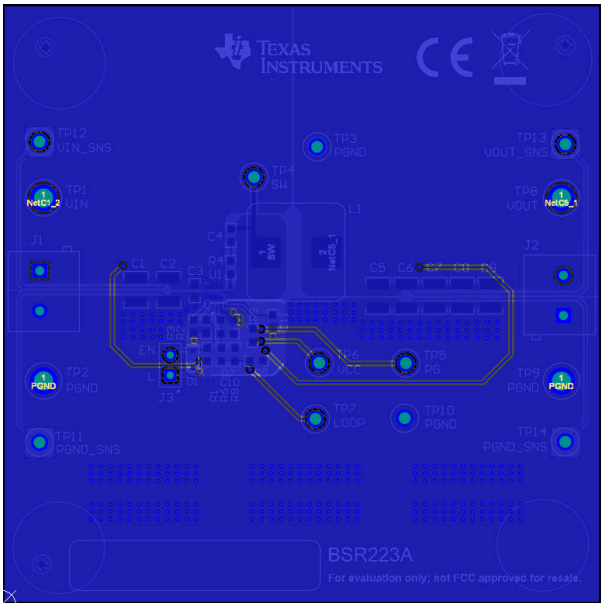


Figure 4-8. Bottom Layer

4.3 Bill of Materials

Table 4-1. Bill of Materials

Designator	QTY	Description	Part Number	Manufacturer
PCB	1	Printed circuit board, 3000 mil x 3000 mil	BSR223	Any
C1, C2	2	CAP, CERM, 10 uF, 35 V, +/- 10%, X7R, 1210	GRM32ER7YA106KA12L	MuRata
C3, C4	2	CAP, CERM, 0.1 uF, 50 V, +/- 10%, X7R, 0603	885012206095	Wurth Elektronik
C5, C6	2	CAP, CERM, 22 uF, 25 V, +/- 10%, X7R, 1210	GRM32ER71E226KE15L	MuRata
C10	1	CAP, CERM, 150 pF, 50 V, +/- 5%, C0G/NP0, 0603	GRM1885C1H151JA01D	MuRata
C11	1	CAP, CERM, 0.047 uF, 50 V, +/- 10%, X7R, 0603	C1608X7R1H473K080AA	TDK
D1	1	Diode, Zener, 5.1 V, 200 mW, SOD-323	MMSZ5231BS-7-F	Diodes Inc.
H9, H10, H11, H12	4	Bumpon, Hemisphere, 0.44 X 0.20, Clear	SJ-5303 (CLEAR)	3M
J1, J2	2	Terminal Block, 5.08 mm, 2x1, Brass, TH	ED120/2DS	On-Shore Technology
J3	1	Header, 100mil, 2x1, Gold, TH	TSW-102-07-G-S	Samtec
L1	1	Inductor, Shielded Drum Core, Superflux, 3.3 uH, 12 A, 0.0059 ohm, SMD	744325330	Wurth Elektronik
LBL1	1	Thermal Transfer Printable Labels, 1.250" W x 0.250" H - 10,000 per roll	THT-13-457-10	Brady
R1	1	RES, 169 k, 0.1%, 0.1 W, 0603	RT0603BRD07169KL	Yageo America
R2	1	RES, 36.1 k, 0.1%, 0.1 W, 0603	RT0603BRD0736K1L	Yageo America
R3	1	RES, 52.3 k, 0.5%, 0.1 W, 0603	RT0603DRE0752K3L	Yageo America
R4	1	RES, 0, 5%, 0.1 W, 0603	CRCW06030000Z0EA	Vishay-Dale
R5	1	RES, 49.9, 0.5%, 0.1 W, 0603	RT0603DRE0749R9L	Yageo America
R6	1	RES, 73.2 k, 0.5%, 0.1 W, 0603	RT0603DRE0773K2L	Yageo America
R7	1	RES, 10.0 k, 0.5%, 0.1 W, 0603	RT0603DRE0710KL	Yageo America
R8	1	RES, 0, 5%, 0.1 W, 0603	RC0603JR-070RL	Yageo
R9	1	RES, 100 k, 1%, 0.1 W, 0603	CRCW0603100KFKEA	Vishay-Dale
SH-J3	1	Shunt, 100mil, Gold plated, Black	SNT-100-BK-G	Samtec
TP1, TP2, TP8, TP9	4	Terminal, Turret, TH, Double	1502-2	Keystone
TP3, TP10, TP11, TP14	4	Test Point, Multipurpose, Black, TH	5011	Keystone
TP4	1	Test Point, Multipurpose, Orange, TH	5013	Keystone

Table 4-1. Bill of Materials (continued)

Designator	QTY	Description	Part Number	Manufacturer
TP5, TP6, TP7	3	Test Point, Multipurpose, Yellow, TH	5014	Keystone
TP12, TP13	2	Test Point, Multipurpose, Red, TH	5010	Keystone
U1	1	4.5-V to 28-V Input, 10-A Synchronous Buck Converter	TPS56A37RPAR	Texas Instruments
C7, C8, C9	0	CAP, CERM, 22 uF, 25 V, +/- 10%, X7R, 1210	GRM32ER71E226KE15L	MuRata

5 Additional Information

Trademarks

HotRod™ is a trademark of Texas Instruments.
All trademarks are the property of their respective owners.

6 Reference

1. Texas Instruments, *TPS56A37 4.5-V to 28-V Input, 10-A Synchronous Buck Converter data sheet*

STANDARD TERMS FOR EVALUATION MODULES

1. *Delivery:* TI delivers TI evaluation boards, kits, or modules, including any accompanying demonstration software, components, and/or documentation which may be provided together or separately (collectively, an “EVM” or “EVMs”) to the User (“User”) in accordance with the terms set forth herein. User's acceptance of the EVM is expressly subject to the following terms.

   1.1 EVMs are intended solely for product or software developers for use in a research and development setting to facilitate feasibility evaluation, experimentation, or scientific analysis of TI semiconductors products. EVMs have no direct function and are not finished products. EVMs shall not be directly or indirectly assembled as a part or subassembly in any finished product. For clarification, any software or software tools provided with the EVM (“Software”) shall not be subject to the terms and conditions set forth herein but rather shall be subject to the applicable terms that accompany such Software

   1.2 EVMs are not intended for consumer or household use. EVMs may not be sold, sublicensed, leased, rented, loaned, assigned, or otherwise distributed for commercial purposes by Users, in whole or in part, or used in any finished product or production system.

2 *Limited Warranty and Related Remedies/Disclaimers*:

   2.1 These terms do not apply to Software. The warranty, if any, for Software is covered in the applicable Software License Agreement.

   2.2 TI warrants that the TI EVM will conform to TI's published specifications for ninety (90) days after the date TI delivers such EVM to User. Notwithstanding the foregoing, TI shall not be liable for a nonconforming EVM if (a) the nonconformity was caused by neglect, misuse or mistreatment by an entity other than TI, including improper installation or testing, or for any EVMs that have been altered or modified in any way by an entity other than TI, (b) the nonconformity resulted from User's design, specifications or instructions for such EVMs or improper system design, or (c) User has not paid on time. Testing and other quality control techniques are used to the extent TI deems necessary. TI does not test all parameters of each EVM. User's claims against TI under this Section 2 are void if User fails to notify TI of any apparent defects in the EVMs within ten (10) business days after delivery, or of any hidden defects with ten (10) business days after the defect has been detected.

   2.3 TI's sole liability shall be at its option to repair or replace EVMs that fail to conform to the warranty set forth above, or credit User's account for such EVM. TI's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by TI and that are determined by TI not to conform to such warranty. If TI elects to repair or replace such EVM, TI shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.

WARNING

Evaluation Kits are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems.

User shall operate the Evaluation Kit within TI’s recommended guidelines and any applicable legal or environmental requirements as well as reasonable and customary safeguards. Failure to set up and/or operate the Evaluation Kit within TI’s recommended guidelines may result in personal injury or death or property damage. Proper set up entails following TI’s instructions for electrical ratings of interface circuits such as input, output and electrical loads.

NOTE:

EXPOSURE TO ELECTROSTATIC DISCHARGE (ESD) MAY CAUSE DEGREDATION OR FAILURE OF THE EVALUATION KIT; TI RECOMMENDS STORAGE OF THE EVALUATION KIT IN A PROTECTIVE ESD BAG.

3 *Regulatory Notices:*

3.1 *United States*

3.1.1 *Notice applicable to EVMs not FCC-Approved:*

FCC NOTICE: This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

3.1.2 *For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:*

CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- *Reorient or relocate the receiving antenna.*
- *Increase the separation between the equipment and receiver.*
- *Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.*
- *Consult the dealer or an experienced radio/TV technician for help.*

3.2 *Canada*

3.2.1 *For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247*

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur

3.3 *Japan*

3.3.1 *Notice for EVMs delivered in Japan:* Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_01.page 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。

https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-delivered-in-japan.html

3.3.2 *Notice for Users of EVMs Considered "Radio Frequency Products" in Japan:* EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required to follow the instructions set forth by Radio Law of Japan, which includes, but is not limited to, the instructions below with respect to EVMs (which for the avoidance of doubt are stated strictly for convenience and should be verified by User):

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

【無線電波を送信する製品の開発キットをお使いになる際の注意事項】 開発キットの中には技術基準適合証明を受けていないものがあります。 技術適合証明を受けていないもののご使用に際しては、電波法遵守のため、以下のいずれかの措置を取っていただく必要がありますのでご注意ください。

1. 電波法施行規則第6条第1項第1号に基づく平成18年3月28日総務省告示第173号で定められた電波暗室等の試験設備でご使用いただく。
2. 実験局の免許を取得後ご使用いただく。
3. 技術基準適合証明を取得後ご使用いただく。

なお、本製品は、上記の「ご使用にあたっての注意」を譲渡先、移転先に通知しない限り、譲渡、移転できないものとします。

上記を遵守頂けない場合は、電波法の罰則が適用される可能性があることをご留意ください。 日本テキサス・インスツルメンツ株式会社

東京都新宿区西新宿 6 丁目 2 4 番 1 号

西新宿三井ビル

3.3.3 *Notice for EVMs for Power Line Communication:* Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_02.page

電力線搬送波通信についての開発キットをお使いになる際の注意事項については、次のところをご覧ください。https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-for-power-line-communication.html

3.4 *European Union*

3.4.1 *For EVMs subject to EU Directive 2014/30/EU (Electromagnetic Compatibility Directive)*:

This is a class A product intended for use in environments other than domestic environments that are connected to a low-voltage power-supply network that supplies buildings used for domestic purposes. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

4 *EVM Use Restrictions and Warnings:*

4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.

4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.

4.3 *Safety-Related Warnings and Restrictions:*

4.3.1 User shall operate the EVM within TI's recommended specifications and environmental considerations stated in the user guide, other available documentation provided by TI, and any other applicable requirements and employ reasonable and customary safeguards. Exceeding the specified performance ratings and specifications (including but not limited to input and output voltage, current, power, and environmental ranges) for the EVM may cause personal injury or death, or property damage. If there are questions concerning performance ratings and specifications, User should contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may also result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM user guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, even with the inputs and outputs kept within the specified allowable ranges, some circuit components may have elevated case temperatures. These components include but are not limited to linear regulators, switching transistors, pass transistors, current sense resistors, and heat sinks, which can be identified using the information in the associated documentation. When working with the EVM, please be aware that the EVM may become very warm.

4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.

4.4 User assumes all responsibility and liability to determine whether the EVM is subject to any applicable international, federal, state, or local laws and regulations related to User's handling and use of the EVM and, if applicable, User assumes all responsibility and liability for compliance in all respects with such laws and regulations. User assumes all responsibility and liability for proper disposal and recycling of the EVM consistent with all applicable international, federal, state, and local requirements.

5. *Accuracy of Information:* To the extent TI provides information on the availability and function of EVMs, TI attempts to be as accurate as possible. However, TI does not warrant the accuracy of EVM descriptions, EVM availability or other information on its websites as accurate, complete, reliable, current, or error-free.

6. *Disclaimers:*

6.1 EXCEPT AS SET FORTH ABOVE, EVMS AND ANY MATERIALS PROVIDED WITH THE EVM (INCLUDING, BUT NOT LIMITED TO, REFERENCE DESIGNS AND THE DESIGN OF THE EVM ITSELF) ARE PROVIDED "AS IS" AND "WITH ALL FAULTS." TI DISCLAIMS ALL OTHER WARRANTIES, EXPRESS OR IMPLIED, REGARDING SUCH ITEMS, INCLUDING BUT NOT LIMITED TO ANY EPIDEMIC FAILURE WARRANTY OR IMPLIED WARRANTIES OF MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF ANY THIRD PARTY PATENTS, COPYRIGHTS, TRADE SECRETS OR OTHER INTELLECTUAL PROPERTY RIGHTS.

6.2 EXCEPT FOR THE LIMITED RIGHT TO USE THE EVM SET FORTH HEREIN, NOTHING IN THESE TERMS SHALL BE CONSTRUED AS GRANTING OR CONFERRING ANY RIGHTS BY LICENSE, PATENT, OR ANY OTHER INDUSTRIAL OR INTELLECTUAL PROPERTY RIGHT OF TI, ITS SUPPLIERS/LICENSORS OR ANY OTHER THIRD PARTY, TO USE THE EVM IN ANY FINISHED END-USER OR READY-TO-USE FINAL PRODUCT, OR FOR ANY INVENTION, DISCOVERY OR IMPROVEMENT, REGARDLESS OF WHEN MADE, CONCEIVED OR ACQUIRED.

7. *USER'S INDEMNITY OBLIGATIONS AND REPRESENTATIONS.* USER WILL DEFEND, INDEMNIFY AND HOLD TI, ITS LICENSORS AND THEIR REPRESENTATIVES HARMLESS FROM AND AGAINST ANY AND ALL CLAIMS, DAMAGES, LOSSES, EXPENSES, COSTS AND LIABILITIES (COLLECTIVELY, "CLAIMS") ARISING OUT OF OR IN CONNECTION WITH ANY HANDLING OR USE OF THE EVM THAT IS NOT IN ACCORDANCE WITH THESE TERMS. THIS OBLIGATION SHALL APPLY WHETHER CLAIMS ARISE UNDER STATUTE, REGULATION, OR THE LAW OF TORT, CONTRACT OR ANY OTHER LEGAL THEORY, AND EVEN IF THE EVM FAILS TO PERFORM AS DESCRIBED OR EXPECTED.

8. *Limitations on Damages and Liability:*

   8.1 *General Limitations*. IN NO EVENT SHALL TI BE LIABLE FOR ANY SPECIAL, COLLATERAL, INDIRECT, PUNITIVE, INCIDENTAL, CONSEQUENTIAL, OR EXEMPLARY DAMAGES IN CONNECTION WITH OR ARISING OUT OF THESE TERMS OR THE USE OF THE EVMS , REGARDLESS OF WHETHER TI HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES. EXCLUDED DAMAGES INCLUDE, BUT ARE NOT LIMITED TO, COST OF REMOVAL OR REINSTALLATION, ANCILLARY COSTS TO THE PROCUREMENT OF SUBSTITUTE GOODS OR SERVICES, RETESTING, OUTSIDE COMPUTER TIME, LABOR COSTS, LOSS OF GOODWILL, LOSS OF PROFITS, LOSS OF SAVINGS, LOSS OF USE, LOSS OF DATA, OR BUSINESS INTERRUPTION. NO CLAIM, SUIT OR ACTION SHALL BE BROUGHT AGAINST TI MORE THAN TWELVE (12) MONTHS AFTER THE EVENT THAT GAVE RISE TO THE CAUSE OF ACTION HAS OCCURRED.

   8.2 *Specific Limitations.* IN NO EVENT SHALL TI'S AGGREGATE LIABILITY FROM ANY USE OF AN EVM PROVIDED HEREUNDER, INCLUDING FROM ANY WARRANTY, INDEMITY OR OTHER OBLIGATION ARISING OUT OF OR IN CONNECTION WITH THESE TERMS, , EXCEED THE TOTAL AMOUNT PAID TO TI BY USER FOR THE PARTICULAR EVM(S) AT ISSUE DURING THE PRIOR TWELVE (12) MONTHS WITH RESPECT TO WHICH LOSSES OR DAMAGES ARE CLAIMED. THE EXISTENCE OF MORE THAN ONE CLAIM SHALL NOT ENLARGE OR EXTEND THIS LIMIT.

9. *Return Policy.* Except as otherwise provided, TI does not offer any refunds, returns, or exchanges. Furthermore, no return of EVM(s) will be accepted if the package has been opened and no return of the EVM(s) will be accepted if they are damaged or otherwise not in a resalable condition. If User feels it has been incorrectly charged for the EVM(s) it ordered or that delivery violates the applicable order, User should contact TI. All refunds will be made in full within thirty (30) working days from the return of the components(s), excluding any postage or packaging costs.

10. *Governing Law:* These terms and conditions shall be governed by and interpreted in accordance with the laws of the State of Texas, without reference to conflict-of-laws principles. User agrees that non-exclusive jurisdiction for any dispute arising out of or relating to these terms and conditions lies within courts located in the State of Texas and consents to venue in Dallas County, Texas. Notwithstanding the foregoing, any judgment may be enforced in any United States or foreign court, and TI may seek injunctive relief in any United States or foreign court.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES “AS IS” AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI’s products are provided subject to TI’s Terms of Sale or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI’s provision of these resources does not expand or otherwise alter TI’s applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265