



Shaunak Deshpande

ABSTRACT

Tracing is a technique that monitors software in real-time to help developers debug and diagnose the problems, exceptions and run-time behavior of applications. Tracing can also be used for performance bench-marking or logging. Real-time tracing is an excellent choice for resolving complex issues.

Lauterbach® is a globally recognized provider of development tools for embedded systems, specializing in high-performance debugging and tracing designs. The TRACE32® tool suite offered by Lauterbach integrates hardware and software to deliver comprehensive support for high-speed trace and debugging, code analysis, and real-time tracing. The tool suite is widely used across industries for optimizing and validating embedded software. For AM26x devices, Lauterbach tools facilitate seamless debugging and enable detailed visibility into system behavior, empowering developers to efficiently address challenges in embedded software development. Tracing with Lauterbach’s TRACE32 tools enables developers to gain a detailed understanding of software execution on AM26x devices. By recording the sequence of executed instructions, memory accesses, and peripheral interactions, the trace functionality helps identify performance issues, debug complex scenarios, and verify software correctness. This capability is critical for real-time embedded systems where timing and execution flow are pivotal. Lauterbach’s non-intrusive tracing approach, combined with features like event time stamping, supports in-depth analysis while preserving system integrity, ultimately enhancing development productivity and system reliability.

Lauterbach Tracing is supported on the ARM R5F Cores as well as the ARM M4 cores.

Note

This document version is valid only for the AM263x and AM263Px devices.

Table of Contents

1 List of Acronyms	3
2 Software Setup	3
3 Hardware Setup	4
3.1 AM263x Connections.....	4
3.2 AM263Px Connections.....	5
3.3 Lauterbach® Connections.....	6
4 Building MCU_PLUS_SDK Examples	8
5 CMM Scripts	8
6 Flashing SBL Null	8
6.1 Using UniFlash tool.....	8
6.2 Using Command Line Python Scripts.....	8
7 Debugging with Trace32 Software	9
8 Summary	10
9 References	10

Trademarks

Code Composer Studio™ is a trademark of Texas Instruments.

FreeRTOS™ is a trademark of Amazon Web Services, Inc.

Lauterbach® and TRACE32® are registered trademarks of Lauterbach GmbH.

Windows® is a registered trademark of Microsoft Corporation.

Linux® is a registered trademark of Linus Torvalds.

All trademarks are the property of their respective owners.

1 List of Acronyms

1. ETM - Embedded Trace Macrocell
2. MCU - MicroController Unit
3. PRU - Programmable Real-time Unit
4. SDK - Software Development Kit
5. SBL - Secondary BootLoader
6. OSPI - Octal Serial Peripheral Interface
7. QSPI - Quad Serial Peripheral Interface
8. GPIO - General Purpose Input Output
9. IOMUX - Pin Multiplexing
10. I2C - Inter Integrated Circuit
11. ROM - Read only memory
12. CPU - Central Processing Unit

2 Software Setup

Install the following software and tools required to run MCU_PLUS_SDK applications on AM26x devices.

1. MCU_PLUS_SDK (Version 10.01 and above)
 - a. AM263x: [Download link for the AM263x MCU_PLUS_SDK](#)
 - b. AM263Px: [Download link for the AM263Px MCU_PLUS_SDK](#)
2. Code Composer Studio™: [Download link for Code Composer Studio \(CCS\)](#)
3. Syscfg: [Download link for Sysconfig tool](#)
4. TI-ARM-CLANG compiler: [Download link for the TI-ARM-CLANG Compiler](#)
5. TI UniFlash tool (optional): [Download link for the TI UniFlash tool](#)
6. Python3: [Download link for Python3](#)
7. OpenSSL: [Download link for OpenSSL](#)

If users need further help, then refer to the [Download, Install and Setup SDK and Tools](#) page from the official documentation.

Lauterbach software - The Trace32 software package can be downloaded from: [Lauterbach Support and Training](#). Install this at C:\T32 for Windows® and install at the default location for Linux®.

3 Hardware Setup

The hardware needed to enable Lauterbach® trace are listed below:

1. AM26x microcontroller:
 - a. AM263x controlCARD: [TMDSCNCD263](#).
 - b. AM263Px controlCARD: [TMDSCNCD263P](#).
2. HSEC dock and breakout board: [TMDSHSECDOCK-AM263](#).
3. Standard power supply for the HSEC Dock breakout board.
4. USB Type A to USB micro-B cable for JTAG, XDS110 connection to the AM26x microcontroller.
5. Lauterbach connector adapter.
6. Trace probe.
7. PowerView Trace.
8. JTAG cable.
9. Power supply to Lauterbach.
10. Trace ribbon cable.

3.1 AM263x Connections

1. Dock the AM263x device on the HSEC Dock breakout board.
2. Connect the power supply to the HSEC Dock breakout board.
3. Connect the USB Type-A to micro-B from the Host PC to the AM26x microcontroller.
4. On the AM263Px, put the SW-5 switch to OFF to disconnect the on-board debugger.

Switch	State
SW-5	Low

5. Power on the HSEC Dock breakout board. Users now see the LD1, LD6, LD14, LD15 glowing on the AM263x.

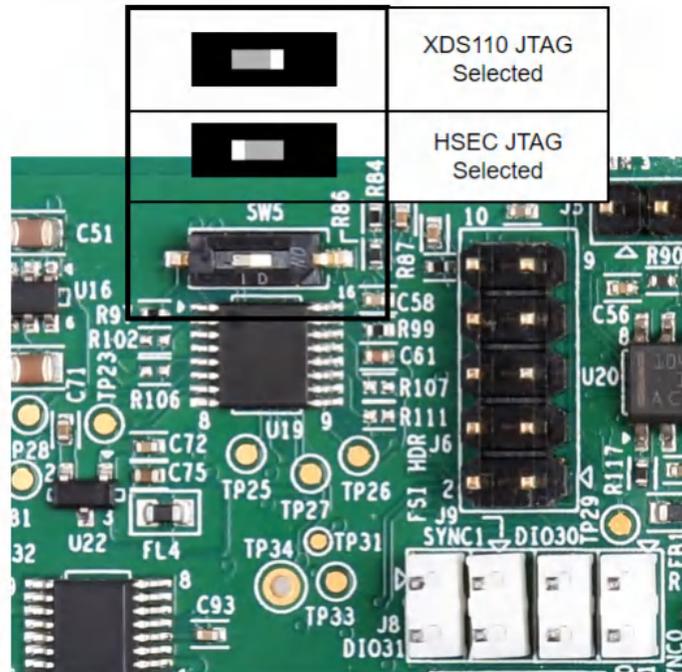


Figure 3-1. AM263x PCB# PROC E2 SW-5 Switch



Figure 3-2. AM263x Control Card Mounted on HSEC Dock Board

3.2 AM263Px Connections

The AM263Px schematics differ from the AM263x. As a result, some of the steps can be different in-case of AM263Px.

1. Dock the AM263Px device on the HSEC Dock breakout board.
2. Connect the power supply to the HSEC Dock breakout board.
3. Connect the USB Type-A to micro-B from the Host PC to the AM26x microcontroller.
4. On the AM263Px, put the SW-1 switch to OFF to disconnect the onboard debugger. Next, put SW-14 switch to OFF, SW-15 switch to OFF and SW-16 switch to ON state. This is necessary to route the signals to the HSEC board to which the Lauterbach trace pins are connected.

Switch	State
SW-1	Low
SW-14	Low
SW-15	Low
SW-16	High

5. Power on the HSEC Dock breakout board. LD2, LD4, LD5, LD9 glows on the AM263Px.

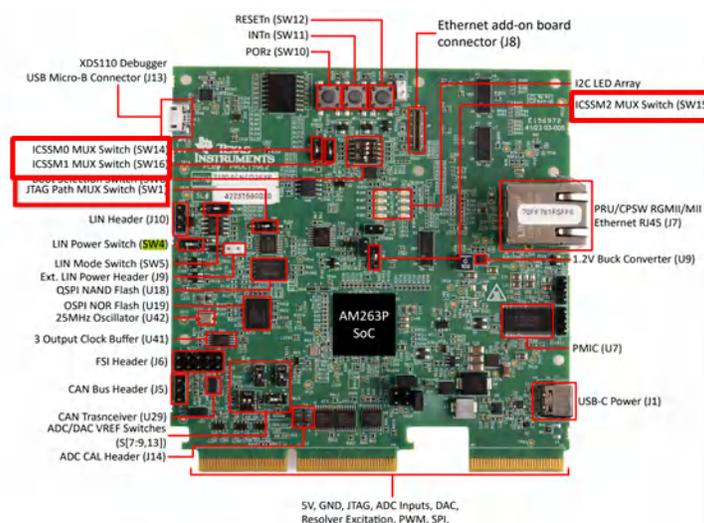


Figure 3-3. AM263Px PCB# PROC E2 Switches

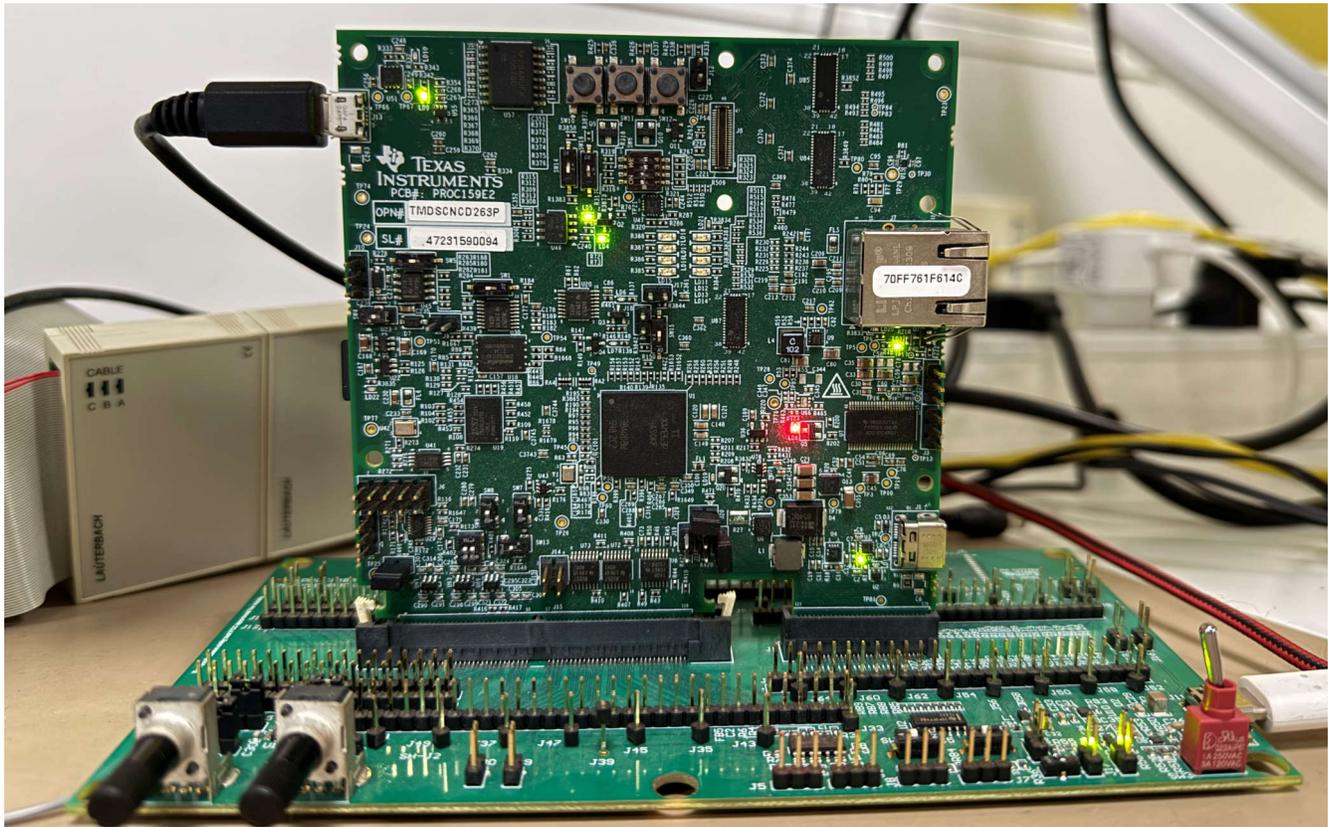


Figure 3-4. AM263Px Control Card Mounted on HSEC Dock Board

3.3 Lauterbach® Connections

1. Connect the Lauterbach adapter to the Dock board.
2. Connect the Lauterbach trace probe to the same port (A or B) as on the adapter connected to the Dock board.
3. Make sure the Trace probe and PowerView trace are connected properly to the adapter.
4. Connect the JTAG cable between PowerView Trace and adapter.
5. Connect the Lauterbach power supply to the PowerView trace and power on the hardware.
6. Connect the Trace ribbon cable between the Trace probe and adapter.

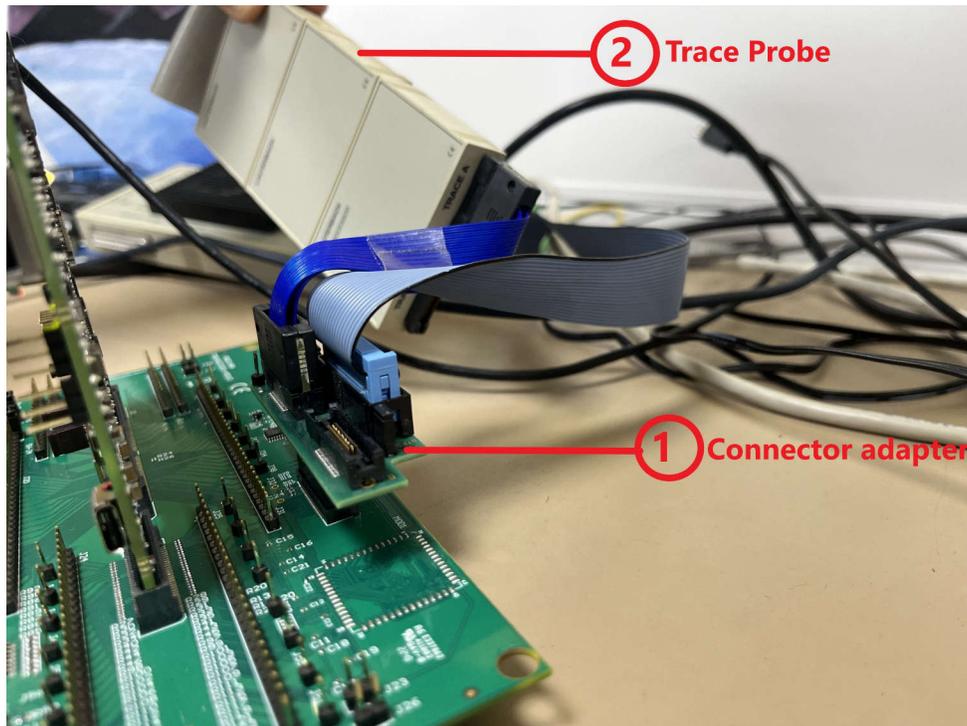


Figure 3-5. Lauterbach Setup Connections

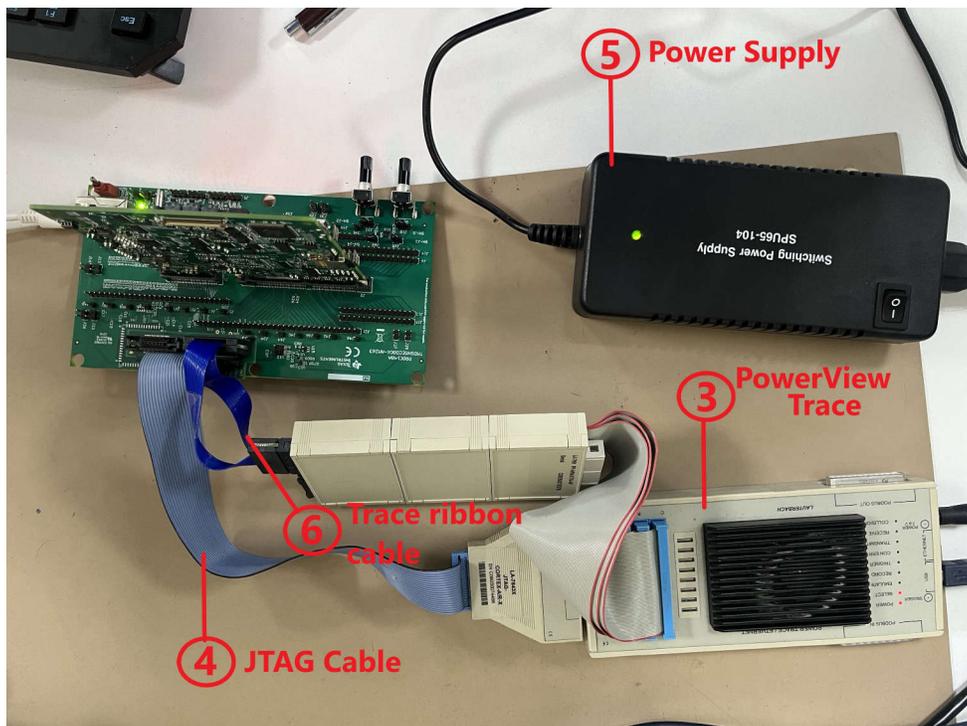


Figure 3-6. Lauterbach Setup Connections

Now, users can power on the Lauterbach power supply.

4 Building MCU_PLUS_SDK Examples

CAUTION

The debug firewalls are opened by the hsmRtlmg. This image is present at <mcu_plus_sdk>/source/security/security_common/drivers/hsmclient/soc/<device_name>/hsmRtlmg.h

If users are using an older MCU_PLUS_SDK version (before v10.01), then replace the hsmRtlmg.h with the updated files:

AM263x - [Github](#)

AM263Px- [Github](#)

As a part of this application note, users use MCU_PLUS_SDK examples, run them on the AM26x MCU and get the trace. In case users want to use a different application, skip this section. Make sure there is a *.debug* configuration build for consistent trace results.

5 CMM Scripts

CMM is a batch type scripting language used by debuggers. The CMM scripts below handle the reset and connection of cores, configuring I2C clocks, trace pins, IO Expander configurations, generating off-chip trace results and displaying them in a window. By default, this works for the R5F Core-0, and can be modified to run for other ARM R5F and ARM M4 cores.

CAUTION

The scripts are validated for NoRTOS based applications. Some additional steps are required to get FreeRTOS™ task table and FreeRTOS component details. Refer to the [OS awareness manual FreeRTOS](#).

6 Flashing SBL Null

TI recommends that users have SBL Null flashed to the AM26x device.

6.1 Using UniFlash tool

To use the UniFlash tool to flash the SBL Null image to the device, follow the steps below.

1. Follow the steps in this video: [TI Video](#).
2. Make sure to select the files as `uart_uniflash` and `sbl_null` for flashing.

6.2 Using Command Line Python Scripts

To use the SDK python scripts for flashing, follow the steps below.

1. Power off the device and switch to UART boot mode.
2. From the following folder: `mcu_plus_sdk/tools/boot` run the below commands in the command prompt. (Check the UART COM Port from the device manager).
3. `python uart_uniflash.py -p COM<your_port_number> --cfg=sbl_prebuilt/<device>/default_sbl_null.cfg`.
4. Confirm the successful flashing and execution of both the steps in the above script.
5. Power off the device and switch to OSPI/QSPI Bootmode based on the AM26x device.

For more detailed steps, refer to [Flashing applications on AM26x devices](#).

7 Debugging with Trace32 Software

The Lauterbach hardware, AM26x device, and the application are prepared to be traced. To extract trace for an application, follow the steps below:

1. Launch the TRACE32 software.
2. From the T32Start screen, select the PowerView Instance and click on *Start*.

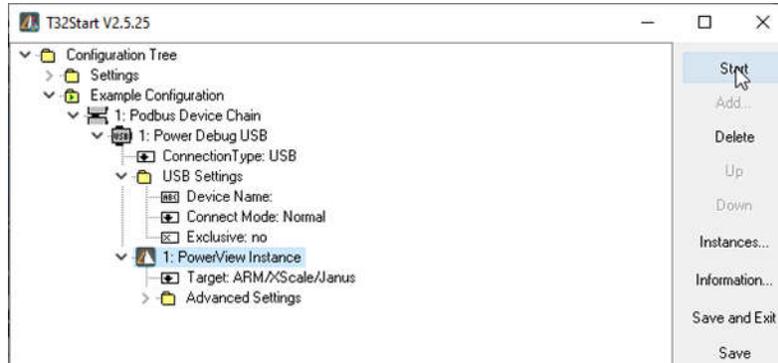


Figure 7-1. T32 Start Window

3. Set the device in OSPI, QSPI boot mode and power cycle your device (HSEC Dock board).
4. Click on File → Run Script → Select the .cmm script *am263px-trace.cmm*.
5. First, this file resets the CPU, attaches to the system bus and unlocks MSS_CTRL registers, then eclipse ROM and runs the R5F core. The file then prompts users to choose an application binary to debug. Browse the file explorer and select the file.
6. The script then configures trace clock source, IOMUX, and I2C to route signals to the docking station.
7. The script opens up windows to show the debug trace.

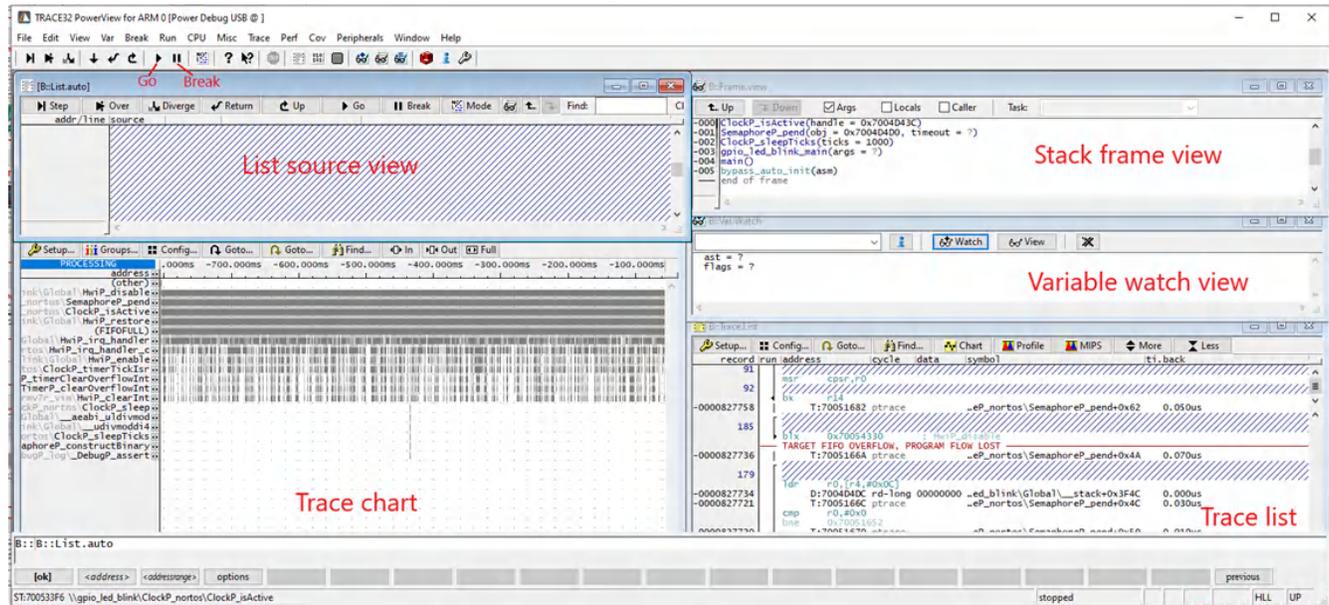


Figure 7-2. Trace Result Windows

See [Figure 8-1](#) for the debug trace of GPIO Led blink example on the AM263Px controlCard.

8 Summary

This document provides a step-by-step guide to enable Lauterbach ETM Trace for Texas Instruments high-performance AM26x micro controllers. By following the hardware and software setup steps above and using the .cmm scripts with the Trace32 software, Lauterbach trace is enabled on the AM26x microcontroller. [Figure 8-1](#) shows the trace for the GPIO LED blink example from the MCU_PLUS_SDK, running on the R5F Core-0, on the AM263P4 in OSPI boot mode.

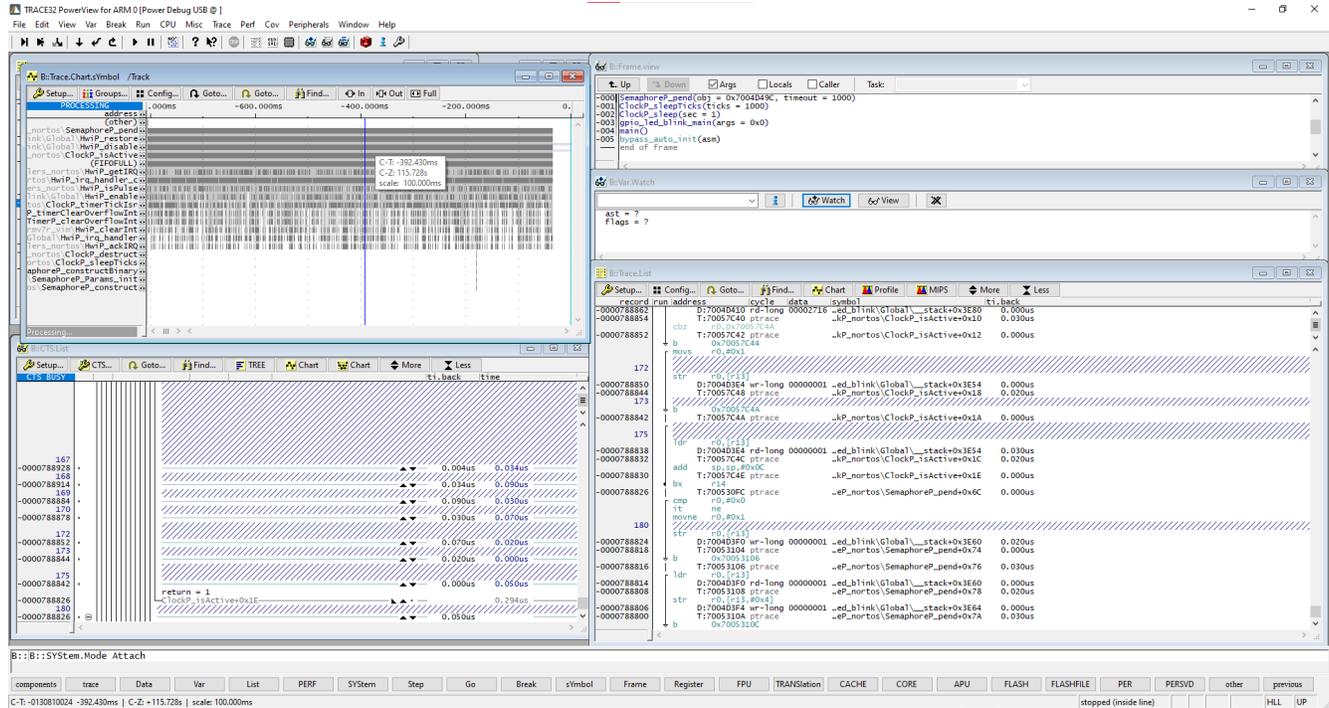


Figure 8-1. Sample Trace Output

9 References

- Lauterbach AM2634 support, [Official Lauterbach support for AM2634 devices](#), webpage
- Lauterbach, [Official Lauterbach error messages guide](#), error guide
- Lauterbach, [OS awareness manual FreeRTOS](#), FreeRTOS manual
- Texas Instruments, [Official MCU_PLUS_SDK documentation for AM263x devices](#), AM263x MCU_PLUS_SDK
- Texas Instruments, [Official MCU_PLUS_SDK documentation for AM263Px devices](#), AM263Px MCU_PLUS_SDK
- Texas Instruments, [AM26x Academy to learn more and get started with development](#), TI AM26x Academy
- Texas Instruments, [Collection of AM26x video trainings](#), AM26x video trainings

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to [TI's Terms of Sale](#) or other applicable terms available either on [ti.com](https://www.ti.com) or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2024, Texas Instruments Incorporated