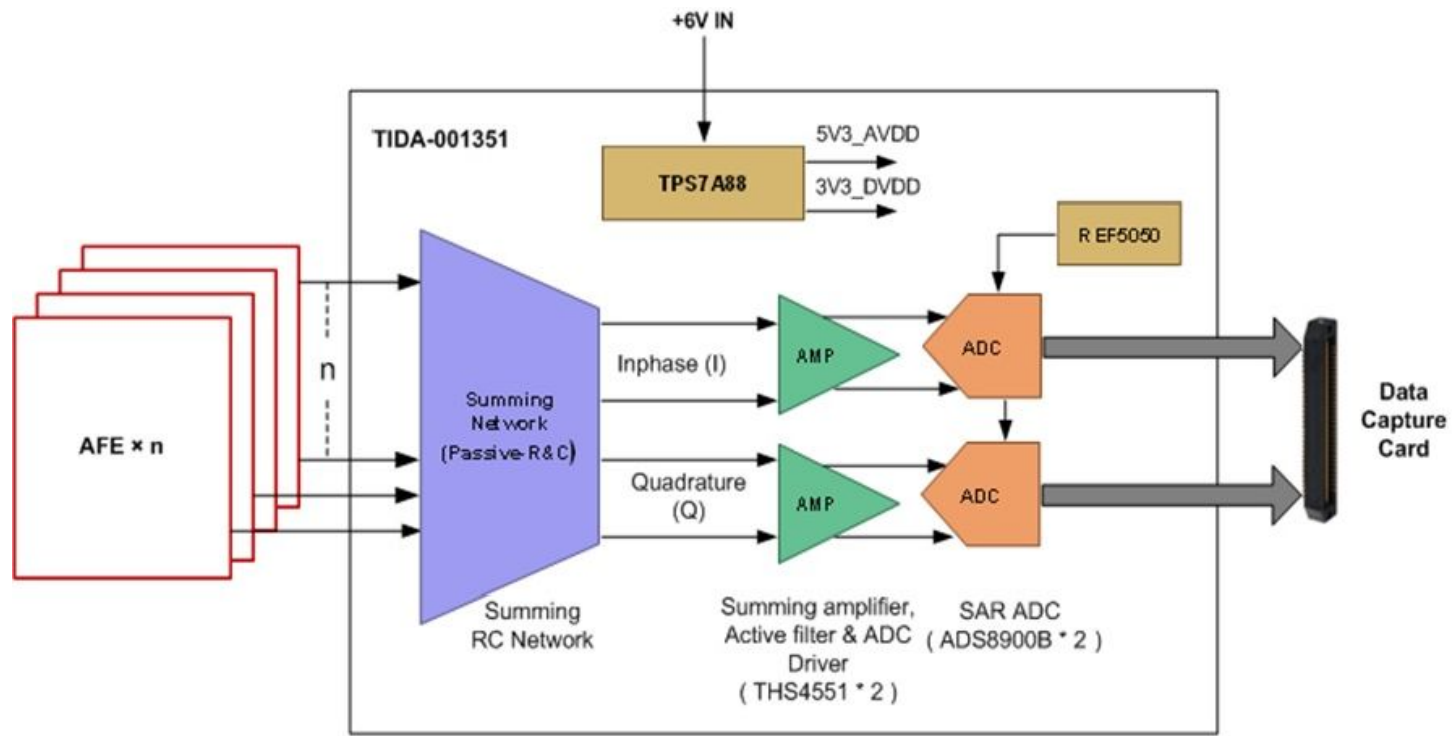
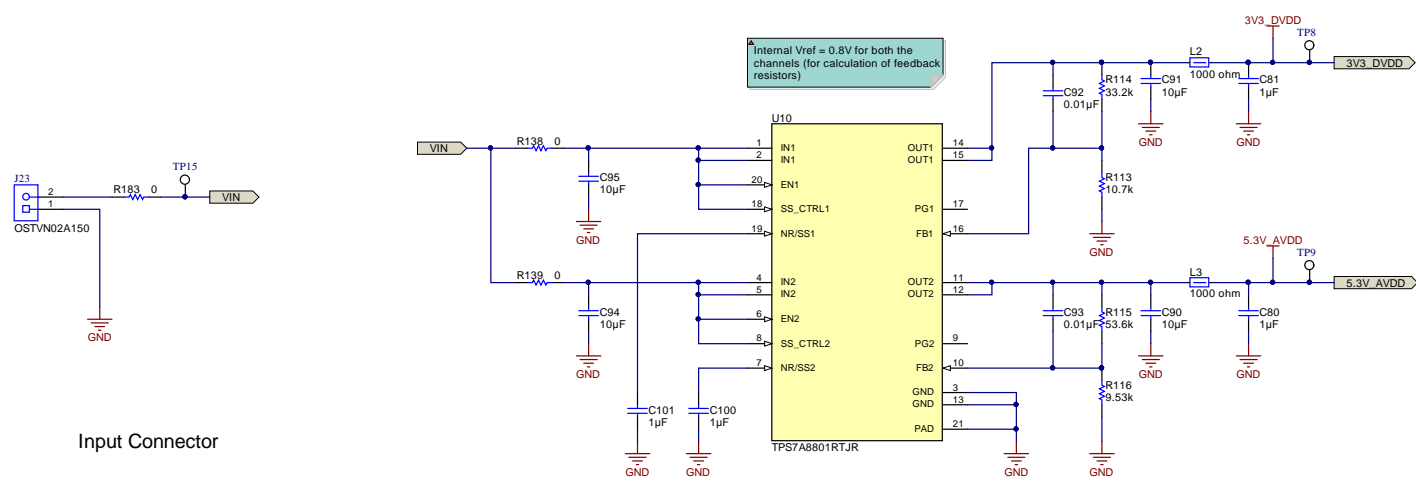


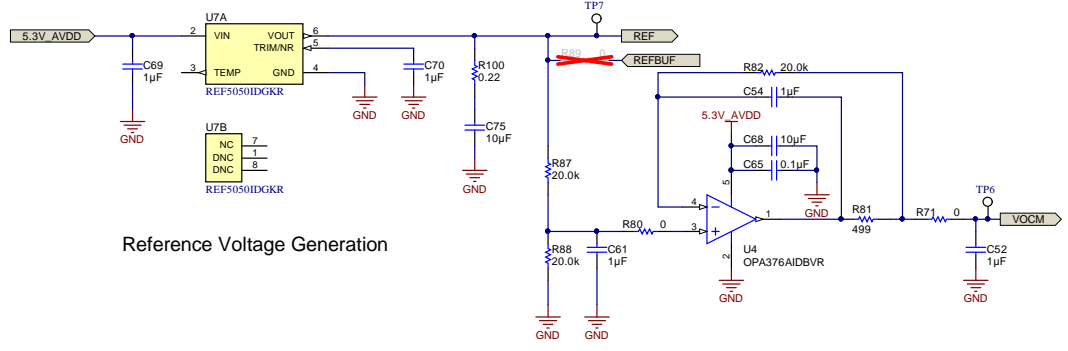
Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A





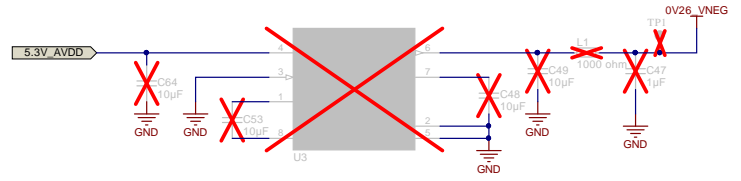
Input Connector

3.3V DVDD and 5.3V AVDD Generation



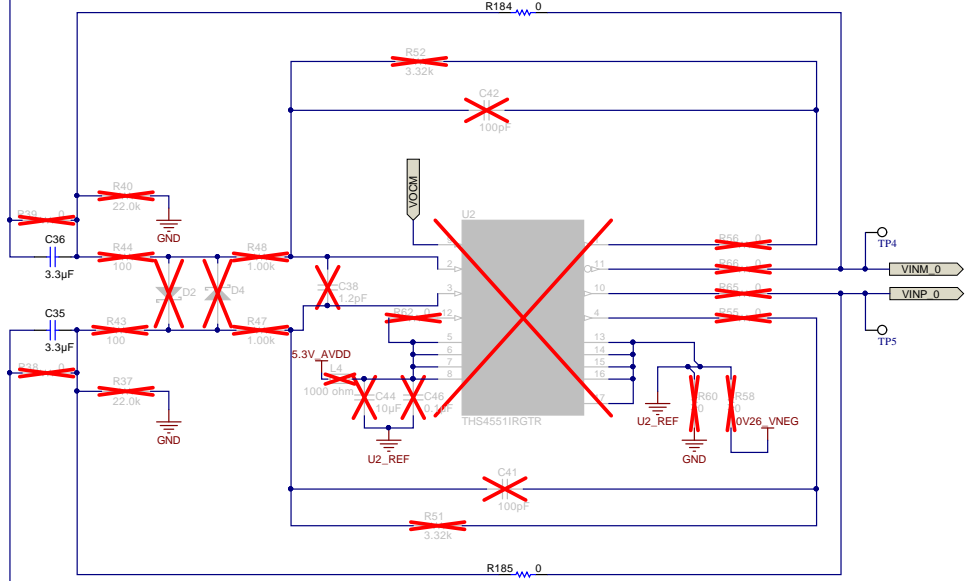
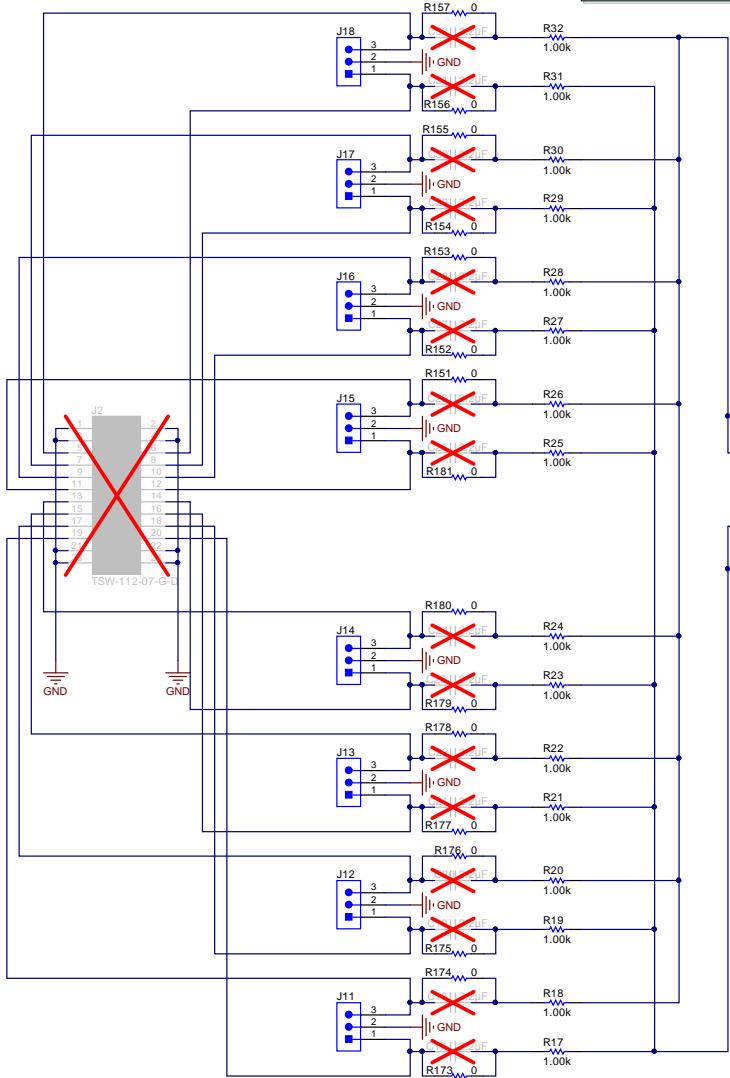
Reference Voltage Generation

CM Voltage Generation



Negative Bias Generation

Series capacitors are for DC Blocking, if the AFE has inbuilt DC Blocking caps, open the external DC Blocking cap and place the zero ohms resistors in parallel.



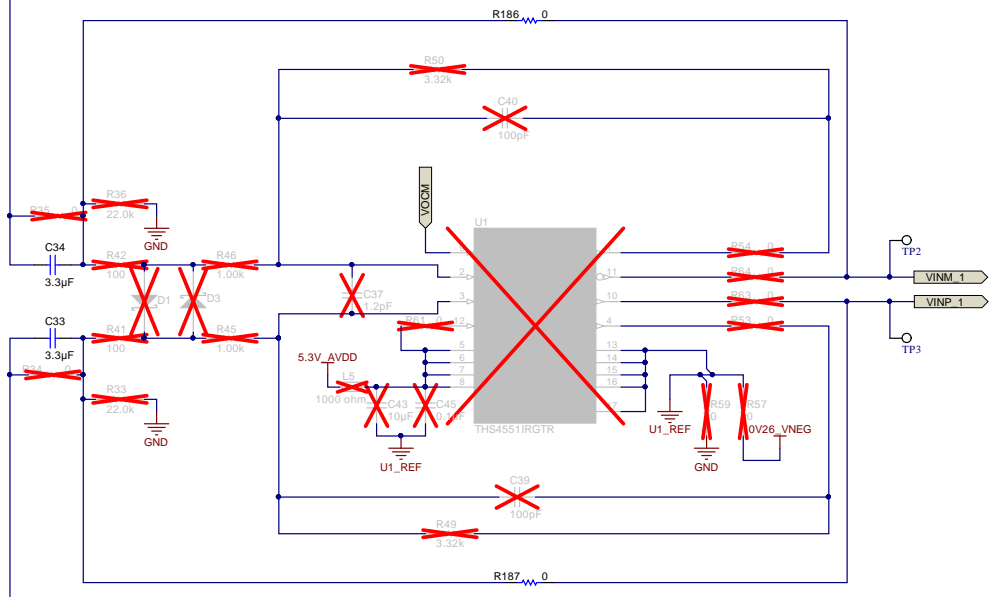
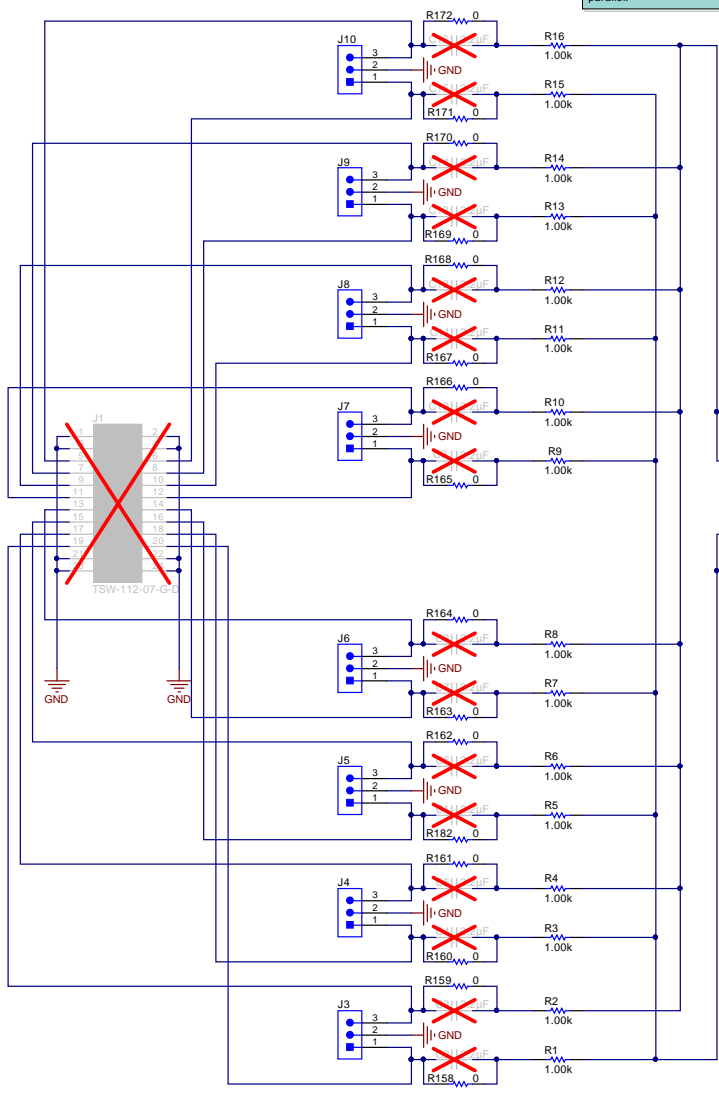
Bandpass Filter for I-Channel

Orderable: N/A	Designed for: Public Release	Mod. Date: 7/23/2018
TID #: TIDA-01351	Project Title: CW Signal Conditioning	
Number: TIDA-01351 Rev: E1	Sheet Title: Filter and Summing (I-ch)	
Drawn By: Sanjay Pithadia	Assembly Variant: 001	Sheet: 3 of 7
Engineer: Sanjay Pithadia	File: TIDA-01351_Filter_Summing-I_SchDoc	Size: B
	Contact: http://www.ti.com/support	

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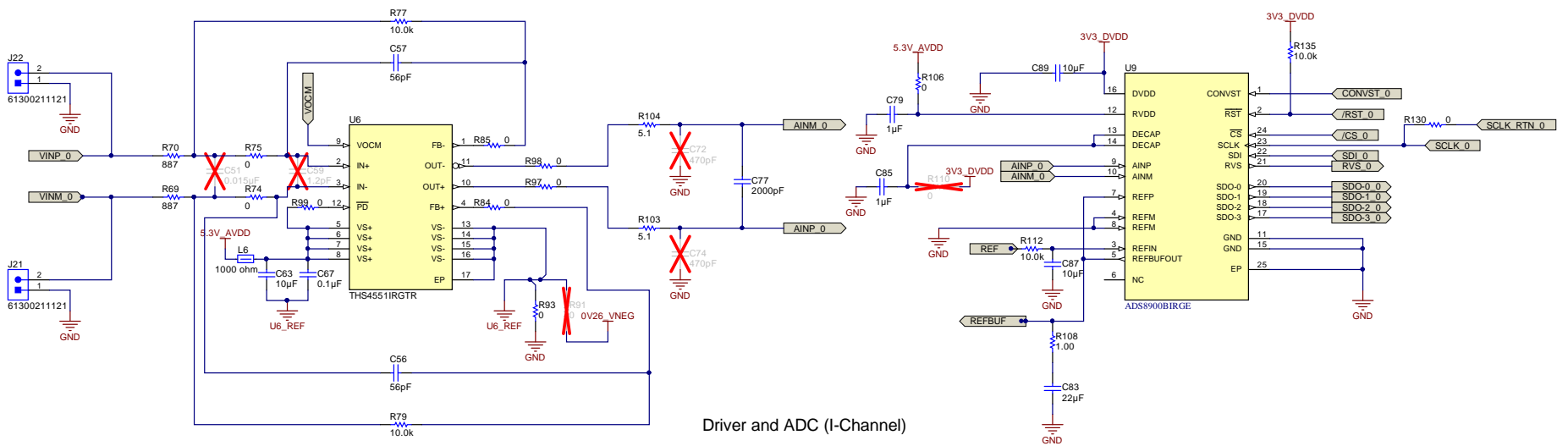


Series capacitors are for DC Blocking. If the AFE has inbuilt DC Blocking caps, open the external DC blocking cap and place the zero ohms resistors in parallel.

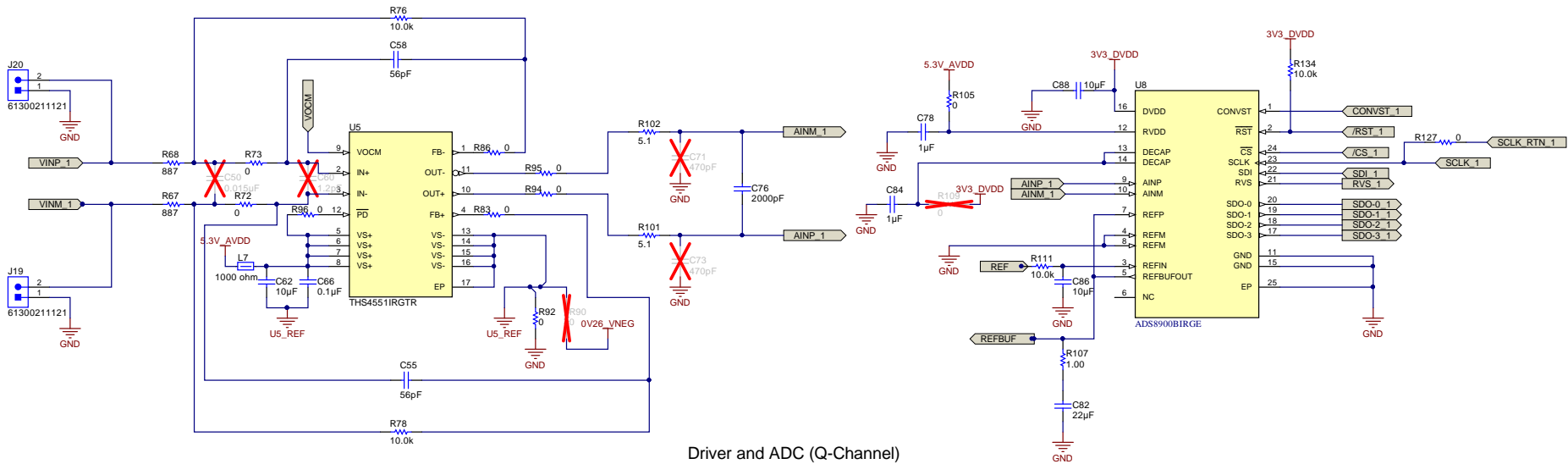


Bandpass Filter for Q-Channel

fHP = 20Hz
fLP = 20kHz



Driver and ADC (I-Channel)



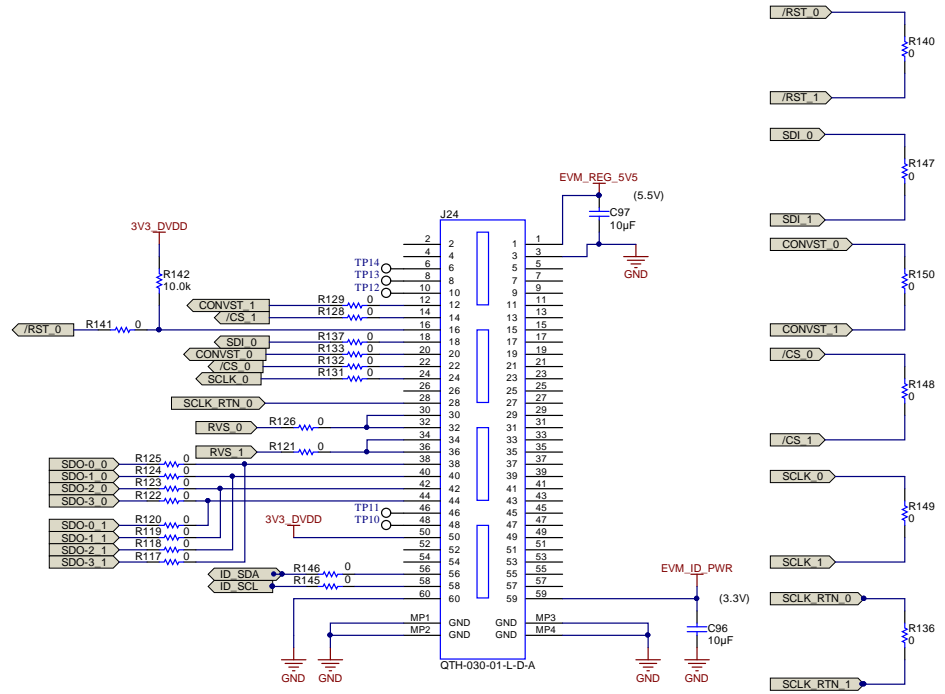
Driver and ADC (Q-Channel)

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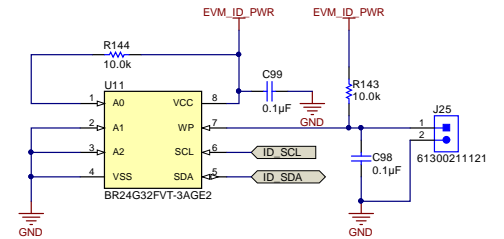
Orderable: N/A	Designed for: Public Release	Mod. Date: 7/23/2018
TID #: TIDA-01351	Project Title: CW Signal Conditioning	
Number: TIDA-01351 Rev: E1	Sheet Title: ADC	
Version: Not in version control	Assembly Variant: 001	Sheet: 5 of 7
Drawn By: Sanjay Pithadia	File: TIDA-01351_ADC_SchDoc	Size: B
Engineer: Sanjay Pithadia	Contact: http://www.ti.com/support	



Cannot open file C:\Users\aj0393901\Desktop\Pin-out-Capture-TIDA-01351.jpg



Connection to PHI Board for data capture



EEPROM for ADC Identification

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Orderable: N/A	Designed for: Public Release	Mod. Date: 7/23/2018
TID #: TIDA-01351	Project Title: CW Signal Conditioning	
Number: TIDA-01351 Rev: E1	Sheet Title: Capture	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 6 of 7
Drawn By: Sanjay Pithadia	File: TIDA-01351_Capture_SchDoc	Size: B
Engineer: Sanjay Pithadia	Contact: http://www.ti.com/support	



H2 1 NY PMS 440 0025 PH
 H6 1 NY PMS 440 0025 PH
 H1 1 NY PMS 440 0025 PH
 H5 1 NY PMS 440 0025 PH

H14 1902C
 H8 1902C
 H13 1902C
 H7 1902C

FID1
 FID3
 FID2
 FID6
 FID4
 FID5

H4 9774050360R
 H3 9774050360R

PCB Number: TIDA-01351
 PCB Rev: E1

PCB LOGO
 Pb-Free Symbol
 PCB LOGO
 FCC disclaimer
 PCB LOGO

Variant/Label Table

Variant	Label Text
001	ChangeMe!
002	ChangeMe!

LBL1
 PCB Label
 Size: 0.65" x 0.20"

ZZ4
 Label Assembly Note
 This Assembly Note is for PCB labels only

ZZ3
 Assembly Note
 These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ2
 Assembly Note
 These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ1
 Assembly Note
 These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Orderable: N/A	Designed for: Public Release	Mod. Date: 7/23/2018
TID #: TIDA-01351	Project Title: CW Signal Conditioning	
Number: TIDA-01351 Rev: E1	Sheet Title: Hardware	
Drawn By: Sanjay Pithadia	Assembly Variant: 001	Sheet: 7 of 7
Engineer: Sanjay Pithadia	File: TIDA-01351_Hardware.SchDoc	Size: B
	Contact: http://www.ti.com/support	

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