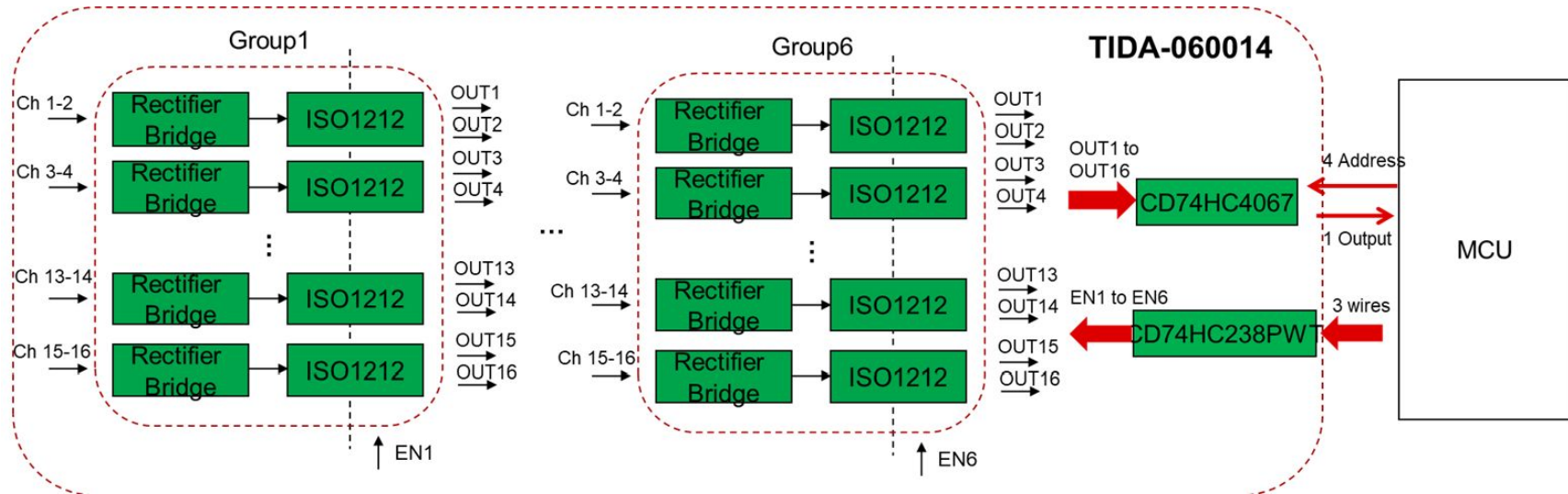


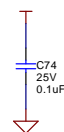
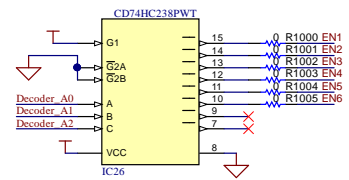
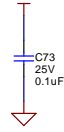
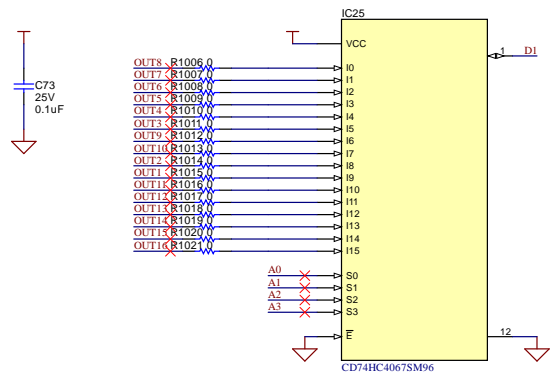
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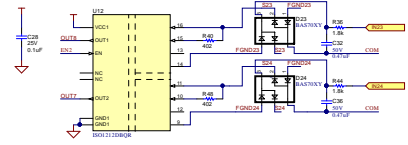
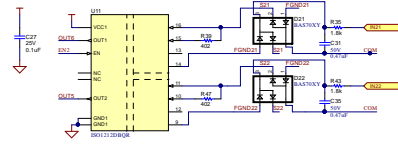
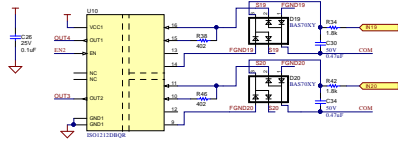
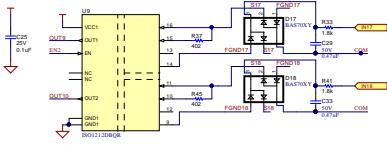
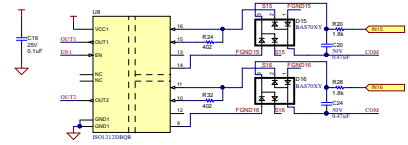
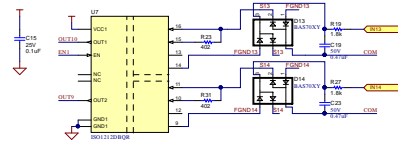
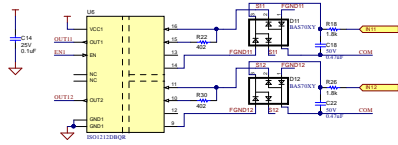
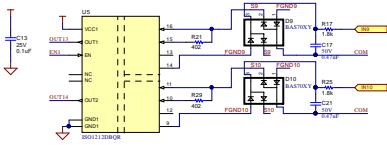
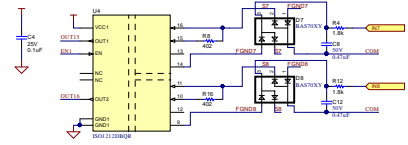
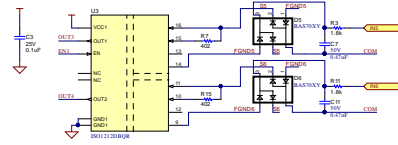
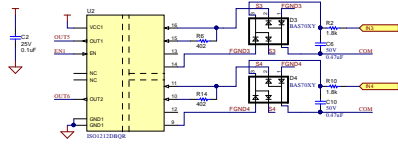
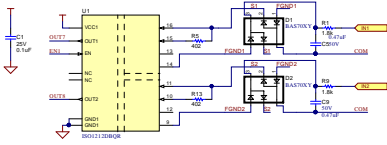


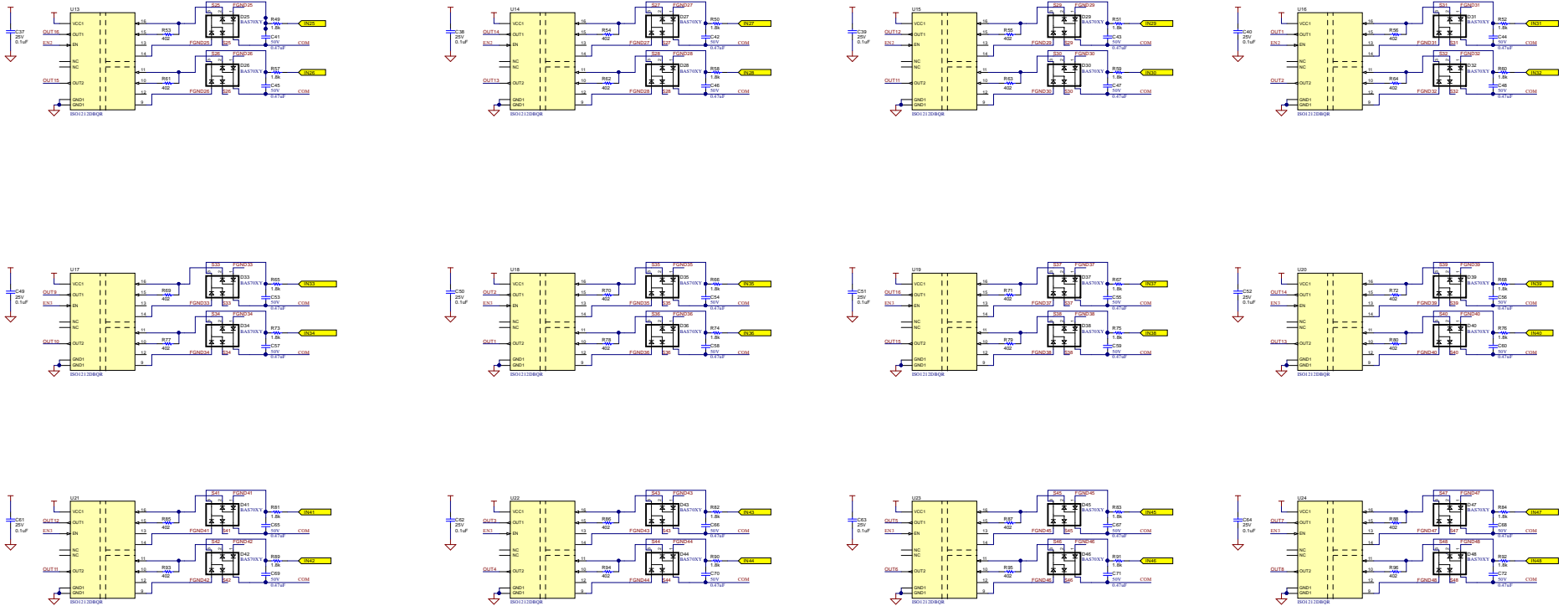
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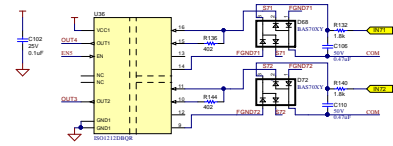
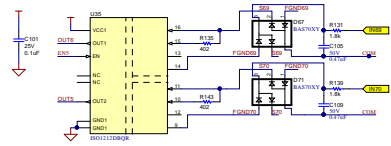
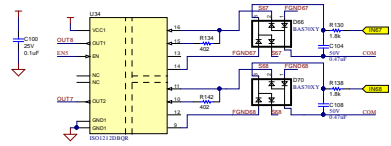
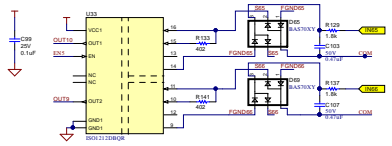
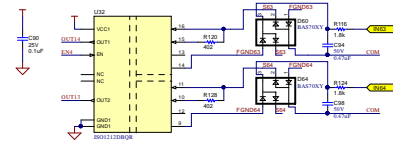
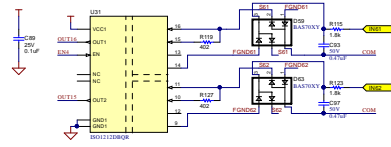
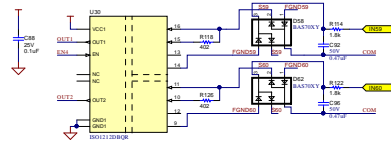
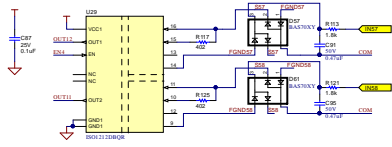
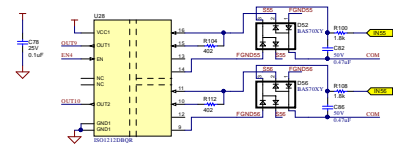
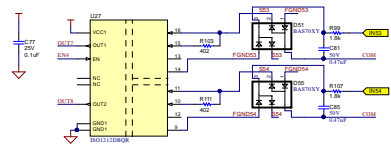
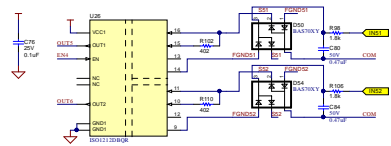
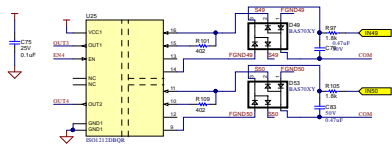
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Number: TIDA-060014 Rev: E1	Assembly Variant: 001	Sheet: 1 of 3
Drawn By: Charles Lin	File: 96 channels DI board design for PLC Coversheet	Contact: http://www.ti.com/support
Engineer: Charles Lin	© Texas Instruments 2018	

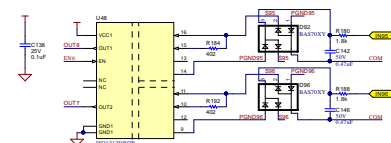
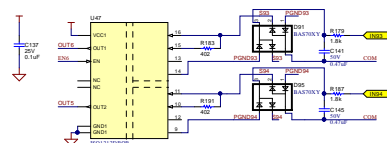
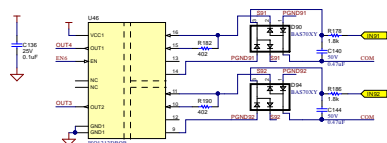
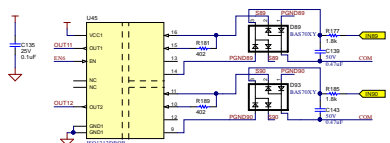
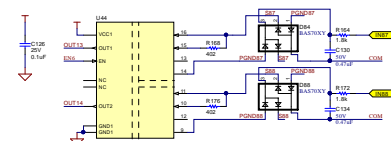
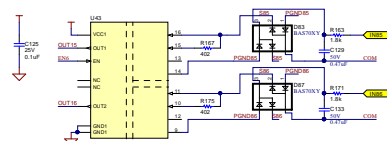
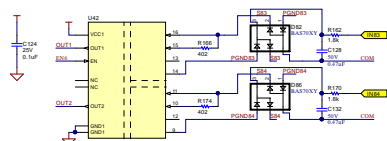
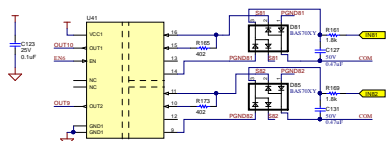
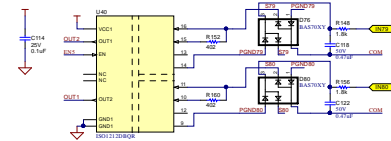
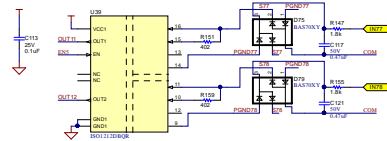
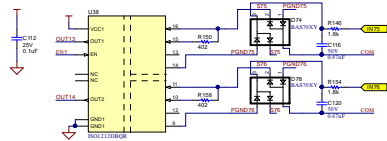
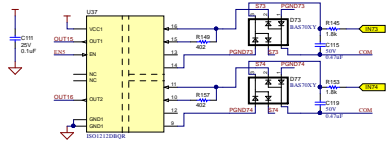












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