

Model Features and Limitations: LDO Topology

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* Model Usage Notes:
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* A. Features have been modelled
*   1. Enable functionality
*   2. Output Current Limit
*   3. Internal fixed Soft Start Time
*   4. Line/Load Transients
*   5. VINLDO and LDO_EN UVLO
*   6. Power Good functionality
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* B. Features haven't been modelled
*   1. Operating Quiescent Current
*   2. Shutdown Current & Leakage Currents
*   3. Temperature dependent characteristic
*   4. I2c Interface, DVS & few supply or input/output pins
*   5. Ground Pins have been tied to 0V internally and hence model does not support Inverting topologies
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* C. Application Notes
*   1. SS = 0 for Start-up behaviour & SS = 1 for model to work in Steady state
*   2. I2C_CTRL = 0 for normal operation & I2C_CTRL = 1 for output voltage of LD01/2 set by I2C commands
*   3. LD01_VOUT/LD02_VOUT are passed to model when I2C feature is enabled(I2C_CTRL =1)
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