

A

B

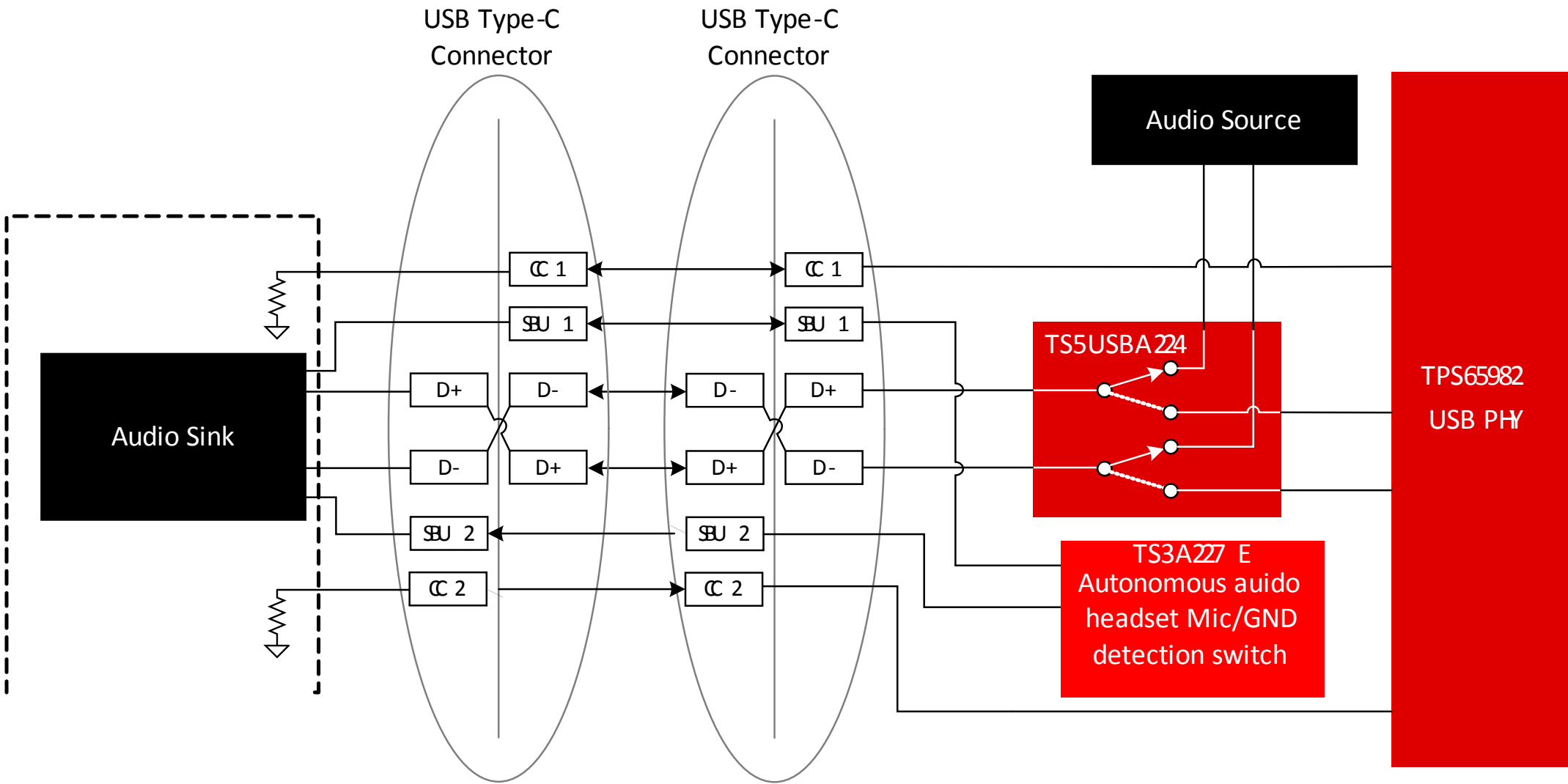
C

D

U\_TIDA00565\_Hardware\_ANSI-B  
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U\_TIDA00565\_Power\_Path  
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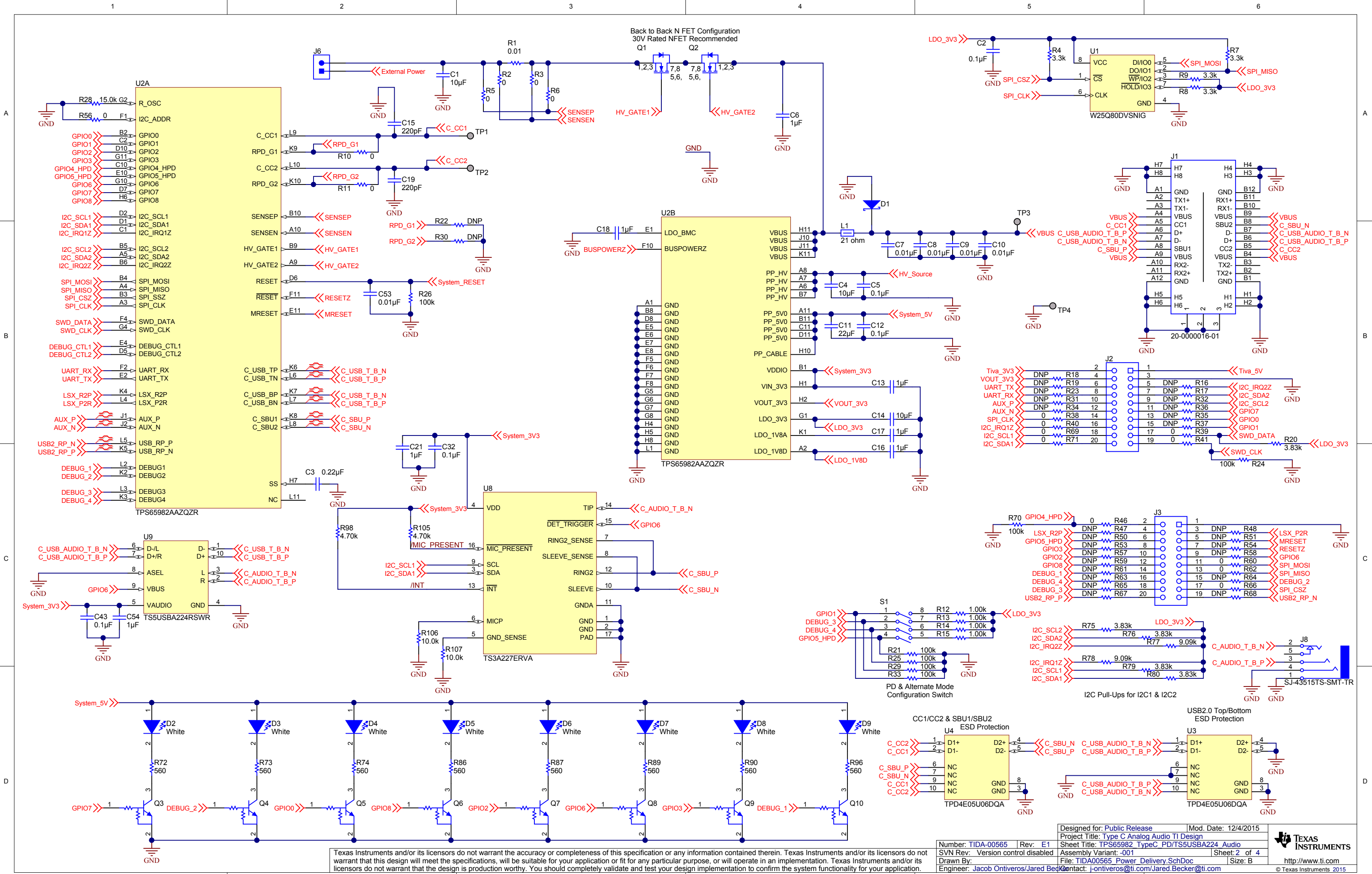


A

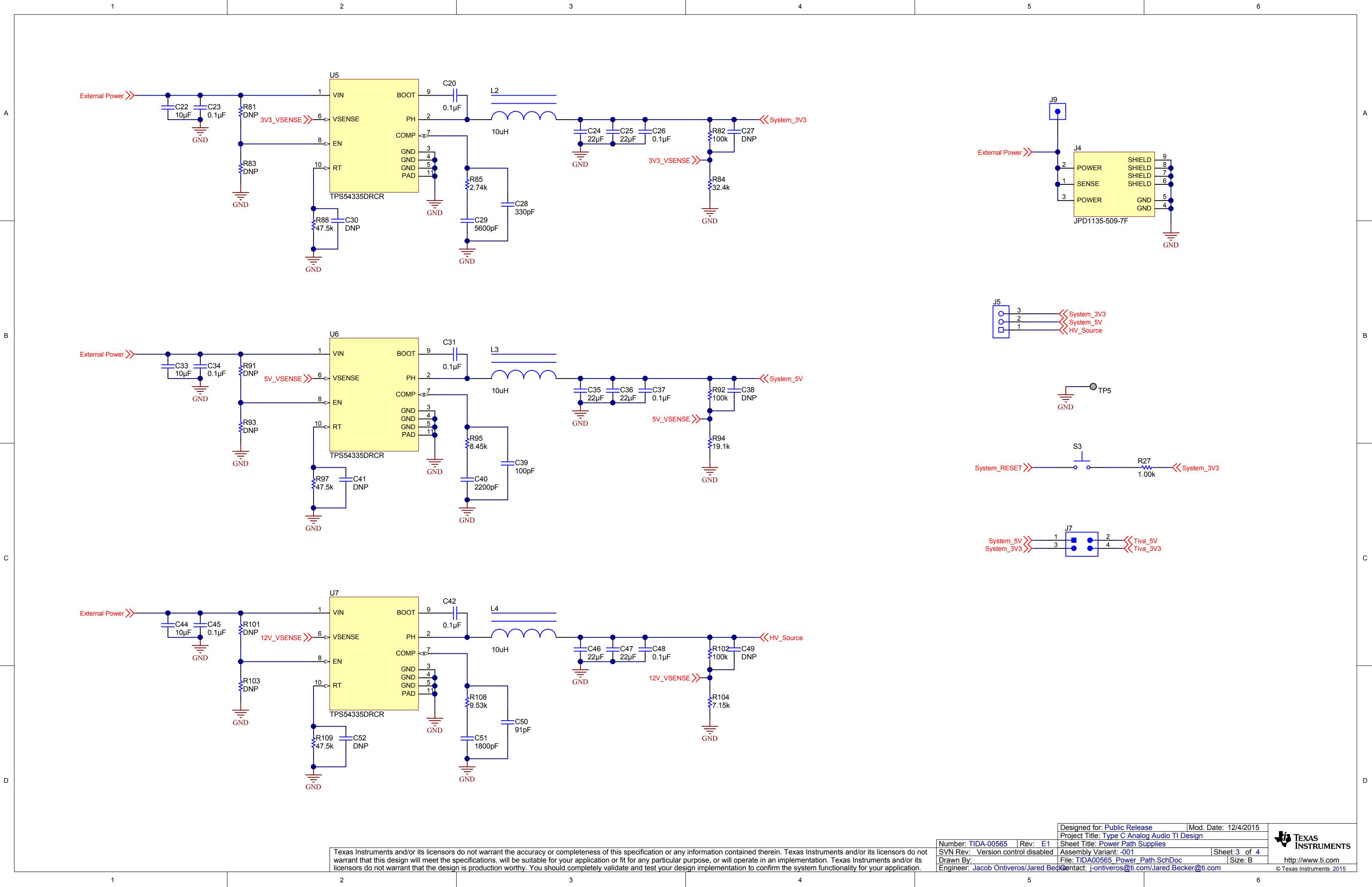
B

C

D



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